

Article

Analysis of Subthreshold Current Reset Noise in Image Sensors

Nobukazu Teranishi ^{1,2}

¹ Research Institute of Electronics, Shizuoka University; 3-5-1 Johoku, Naka-ku, Hamamatsu 432-8011, Japan; teranishi@idl.rie.shizuoka.ac.jp; Tel.: +81-53-478-1313

² Laboratory of Advanced Science and Technology for Industry, University of Hyogo; 1-1-2 Koto, Kamigori, Ako-gun, Hyogo 678-1205, Japan

Academic Editor: Eric R. Fossum

Received: 26 January 2016; Accepted: 4 May 2016; Published: 10 May 2016

Abstract: To discuss the reset noise generated by slow subthreshold currents in image sensors, intuitive and simple analytical forms are derived, in spite of the subthreshold current nonlinearity. These solutions characterize the time evolution of the reset noise during the reset operation. With soft reset, the reset noise tends to $\sqrt{mkT/2C_{PD}}$ when $t \rightarrow \infty$, in full agreement with previously published results. In this equation, C_{PD} is the photodiode (PD) capacitance and m is a constant. The noise has an asymptotic time dependence of t^{-1} , even though the asymptotic time dependence of the average (deterministic) PD voltage is as slow as $\log t$. The flush reset method is effective because the hard reset part eliminates image lag, and the soft reset part reduces the noise to soft reset level. The feedback reset with reverse taper control method shows both a fast convergence and a good reset noise reduction. When the feedback amplifier gain, A , is larger, even small value of capacitance, C_P , between the input and output of the feedback amplifier will drastically decrease the reset noise. If the feedback is sufficiently fast, the reset noise limit when $t \rightarrow \infty$, becomes $\frac{mkT(C_{PD}+C_{P1})^2}{2q^2A(C_{PD}+(1+A)C_P)}$ in terms of the number of electron in the PD. According to this simple model, if $C_{PD} = 10$ fF, $C_P/C_{PD} = 0.01$, and $A = 2700$ are assumed, deep sub-electron rms reset noise is possible.

Keywords: CMOS image sensor; 3-transistor scheme; reset noise; subthreshold current; hard reset; soft reset; feedback reset; tapered reset

1. Introduction

Four-transistor (4-Tr) complementary metal-oxide-semiconductor (CMOS) image sensors [1] are widely used in various applications, such as mobile phone cameras, digital still cameras, security, industrial, medical equipment, *etc.* They have significant advantages compared with three-transistor (3-Tr) CMOS image sensors. Firstly, the 4-Tr scheme can use pinned photodiodes (PPDs) [2–6] to reduce the dark current. Secondly, the complete charge transfer by the PPD [2] realizes “first reset, later signal” and correlated double sampling (CDS) [7], which eliminates both the reset noise at the floating diffusion node and the low frequency noise at the source follower amplifier. Thirdly, the capacitance of the floating diffusion can be decreased by fine processing technology and a large conversion gain can be obtained, which increases the signal-to-noise ratio. Fourthly, the shared transistor technology [8,9] reduces the number of transistors per pixel. The minimum reported transistor number per pixel is 1.375 transistors/pixel [10], which is much smaller than that of 3-Tr scheme.

The 3-Tr scheme is now being used for large pixel CMOS image sensors. One example is its use in medical X-ray image sensors. The typical pixel size is around 100 μm . There are several reasons why the 3-Tr scheme is being used. The first one is that it is difficult to achieve a complete charge transfer of the PPD with such large pixels or PDs. Another one lies in the fact that X-ray image sensors usually

suffer from photon shot noise and the readout noise of the 3-Tr scheme is acceptable. A third reason is that the fabrication process for the 3-Tr scheme is simpler than that of the 4-Tr scheme. A fourth reason is the fact that the 3-Tr scheme can be operated in non-destructive readout mode, and can realize dose sensing during radiation or auto exposure control (AEC) using fast-frame-rate skip mode [11]. Finally, a fifth reason is that the 3-Tr scheme can reach a higher number of saturation electrons at the PD than that of the 4-Tr scheme.

If the 3-Tr scheme were able to achieve low readout noise, it could be used in more applications, in particular, elevated image sensors, or photosensitive material hybrid image sensors, which cannot use the PPD complete charge transfer scheme. Some organic photoconductive films have larger absorption coefficients than that of silicon, and smaller photosensitive layer thicknesses can provide enough sensitivity. Crosstalk could then be reduced even for small pixel size, and elevated image sensors with organic photoconductive films would become candidates for small pixel image sensors [12,13]. Elevated image sensors can have sensitivities beyond the silicon sensitive wavelength range, well within the ultraviolet (UV) and infrared (IR) range. For example, crystal selenium (c-Se) has a 1.74 eV bandgap and is a good sensitive material for both UV and visible light [14]. Germanium (Ge) and indium-gallium-arsenide (InGaAs) have 0.8 eV and 0.36–1.43 eV direct bandgaps, respectively, and are good photosensitive materials for near IR [15,16]. These hybrid image sensor developments might be accelerated by recent advances in 3D and hybrid technology.

A 3-Tr pixel consists of an N-type PD, a reset transistor (RST) to reset PD, a source follower amplifier (SF) which picks up the PD voltage and sends the voltage signal to the column circuit, and a select transistor (SEL) which activates the selected row, as shown in Figure 1. The reset noise of the PD is the dominant noise source in the 3-Tr scheme. The original reset method is hard reset. Its noise variance is calculated as kTC [17,18], where k is the Boltzmann constant, T the absolute temperature, and C the detection capacitance. This noise is therefore called “ kTC noise”. Various other reset methods have been proposed to reduce the reset noise and will be discussed later soft reset [19,20], feedback reset [21,22], feedback reset with taper control [13,23–26]. Feedback reset has realized a reset noise level as small as $2.9 e^- \cdot \text{rms}$ (electrons rms) [13]. While those approaches aim to reduce the reset noise itself, other approaches to the problem have been attempted; one of them is to reduce effective detection capacitance, thus increasing signal voltage. For this purpose, a charge sensitive amplifier or capacitive transimpedance amplifier is introduced [27,28]. Another approach is to introduce in-pixel CDS [29].

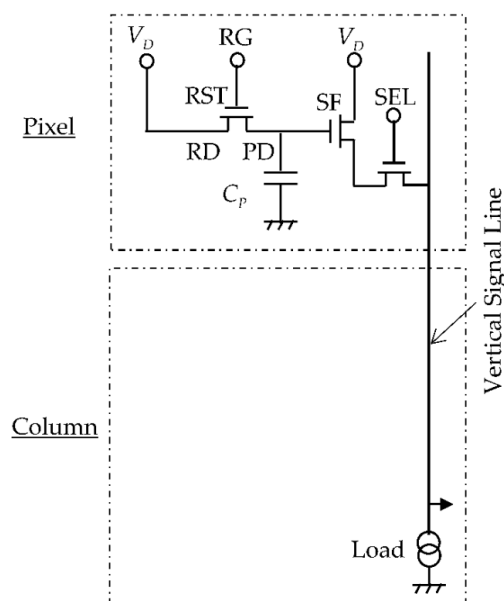


Figure 1. Pixel and column schematic for 3-Tr scheme CMOS image sensor.

In this paper, we will discuss the reset noise reduction itself. A fundamental time-domain analysis of various reset methods is presented, and the reset noise is studied in detail. In the next section, our reset noise analysis technique is introduced. In Sections 3–6 the hard reset, soft reset, tapered reset, and feedback reset with reverse taper control methods will be analyzed. In Section 7, the possibility of photon counting by the 3-Tr scheme is discussed.

2. Reset Noise Analysis Technique

To discuss the various reset methods, a reset noise analysis technique must be prepared, preferably one capable of providing an intuitive and simple analytical solution without numerical or Monte Carlo simulations in spite of the nonlinearity of the subthreshold current. The subthreshold current causes a slow reset operation, therefore, the time dependence of the reset noise during the reset operation period needs to be evaluated, from the initial condition to the final state.

A frequency domain analysis has previously been published, where the estimated reset noise was compared with measurement result [26]. The steady-state noise (final stage noise) was calculated using a resistor instead of the reset transistor. A time domain analysis was proposed, using effectively-second-order differential equation [25]. To derive a closed form expression, a fixed resistance was also used instead for the reset transistor. Another time domain method was proposed for soft reset analysis, directly treating the subthreshold current nonlinearity and assuming the existence of a shot noise in the subthreshold current [20]; it obtained a soft reset noise of $kT/2C$, which agrees well with the measurements. However, it would be desirable that improved reset methods such as feedback reset with taper control could also be analytically treated.

In the rest of this section, our reset noise analysis is introduced. The PD node voltage $V_{PD}(t)$ is decomposed into a deterministic (or average) part $V_{PDa}(t)$ and a stochastic (or noise) part $v_{PD}(t)$. Naturally:

$$V_{PD}(t) = V_{PDa}(t) + v_{PD}(t) \quad (1)$$

To derive the analytical form of the reset noise variance $\langle v_{PD}(t)^2 \rangle$ three steps are needed in this analysis:

- Step 1: The equation for the average part $V_{PDa}(t)$ is derived, and the solution is obtained.
- Step 2: The equation for the noise part $v_{PD}(t)$ is derived, and $v_{PD}(t)$ is obtained explicitly.
- Step 3: The variance $\langle v_{PD}(t)^2 \rangle$ is calculated.

This approach is straightforward and logically simple. In the following sections, this analysis is applied to hard reset, soft reset, tapered reset and feedback reset with taper control.

3. Hard Reset

Hard reset is originally applied in the reset of the floating diffusion of CCD (Charge coupled device), and is the original reset method of 3-Tr CMOS image sensor. Its timing diagram is shown in Figure 2a. The hard reset noise variance was derived as kTC using frequency domain analysis [17] and time domain analysis [18]. The same result will be derived here.

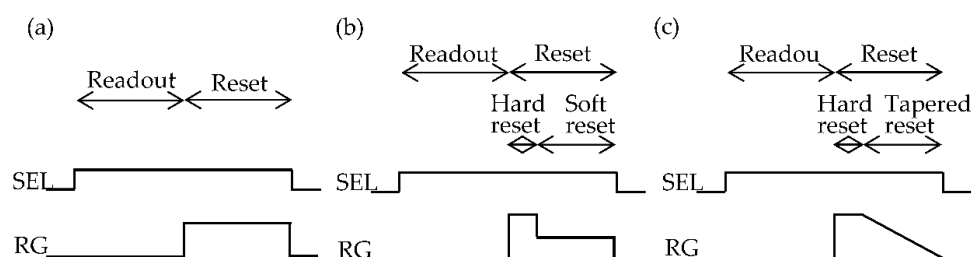


Figure 2. Timing diagram for one pixel. (a) Hard reset and soft reset; (b) Flush reset; and (c) Tapered reset.

With hard reset, the RST channel can be regarded as a pure resistance R , because the RST operates in the linear region. Resistances generate Johnson noise or thermal noise, from which the reset noise arises. To simplify the model, it is also assumed that there is no dark current or no incident light during the reset phase. This assumption is also used in Sections 4–6. The continuity equation is:

$$C_{PD} \frac{dV_{PD}(t)}{dt} = \frac{V_{RD0} - V_{PD}(t)}{R} + i_n(t) \quad (2)$$

where C_{PD} is the PD capacitance, V_{RD0} is the reset transistor drain (RD) voltage, and $i_n(t)$ is the thermal noise associated with resistance R , whose autocorrelation is:

$$\langle i_n(t_1) i_n(t_2) \rangle = \frac{2kT}{R} \delta(t_1 - t_2) \quad (3)$$

When applied at Step 1, the equation of continuity becomes:

$$C_{PD} \frac{dV_{PDa}(t)}{dt} = \frac{V_{RD0} - V_{PDa}(t)}{R} \quad (4)$$

The solution is obtained as:

$$V_{PDa}(t) = V_{PDa}(0) e^{-\frac{t}{\tau_{HR}}} + V_{RD0} \left(1 - e^{-\frac{t}{\tau_{HR}}}\right) \quad (5)$$

where time constant, τ_{HR} is given by:

$$\tau_{HR} \equiv C_{PD}R \quad (6)$$

If the parameters of a typical RST are assumed, with a 0.4 μm channel width, 0.55 μm channel length, 6 nm thick gate oxide, $V_{GS} = 3.3$ V, and $V_{RD0} = 3.3$ V, we will have $R \approx 10$ k Ω . For $C_{PD} = 10$ fF, τ_{HR} becomes 100 ps, which is much smaller than the typical reset period, 1 μs . When $t \rightarrow \infty$, $V_{PDa}(t)$ converges to V_{RD0} .

For Step 2, we substitute Equation (5) into Equation (2), to obtain the equation for $v_{PD}(t)$ as:

$$C_{PD} \frac{dv_{PD}(t)}{dt} = \frac{v_{PD}(t)}{R} + i_n(t) \quad (7)$$

The solution of this equation is:

$$v_{PD}(t) = \frac{1}{C_{PD}} \int_0^t dt' e^{\frac{t'-t}{\tau_{HR}}} i_n(t') + e^{-\frac{t}{\tau_{HR}}} v_{PD}(0) \quad (8)$$

Finally, for Step 3, we square Equation (8) to obtain:

$$v_{PD}(t)^2 = \frac{1}{C_{PD}^2} \int_0^t \int_0^t dt_1 dt_2 e^{\frac{t_1-t}{\tau_{HR}} + \frac{t_2-t}{\tau_{HR}}} i_n(t_1) i_n(t_2) + \frac{2e^{-\frac{t}{\tau_{HR}}} v_{PD}(0)}{C_{PD}} \int_0^t dt_1 e^{\frac{t_1-t}{\tau_{HR}}} i_n(t_1) + e^{-\frac{2t}{\tau_{HR}}} v_{PD}^2(0) \quad (9)$$

Averaging Equation (9) and using Equation (3), the hard reset noise variance is obtained as:

$$\langle v_{PD}(t)^2 \rangle = \frac{kT}{C_{PD}} \left(1 - e^{-\frac{2t}{\tau_{HR}}}\right) + \langle v_{PD}(0)^2 \rangle e^{-\frac{2t}{\tau_{HR}}} \quad (10)$$

The first term is caused by thermal noise, and the second term comes from the initial condition. Because of the exponential decay, the reset noise variance $\langle v_{PD}(t)^2 \rangle$ is sufficiently settled within the reset period. When $t \rightarrow \infty$:

$$\langle v_{PD}(\infty)^2 \rangle = \frac{kT}{C_{PD}}. \quad (11)$$

The well-known kTC noise is therefore produced.

4. Soft Reset

The soft reset method was introduced to reduce the reset noise. Even though the timing diagram is the same as that of the hard reset, the RG (RST gate) on voltage is smaller. With the soft reset method, the RST is operated first in the saturation region, and then in the subthreshold region. The signal charge transfer from the PD to the RD in the saturation region is smooth and the period is as small as a few nanoseconds, it does not substantially contribute to the reset noise, when compared with the following subthreshold region period. The reset noise will therefore be calculated neglecting the saturation period, using only subthreshold region period; $t = 0$ in this analysis corresponds to the moment when RST enters this region.

The equation of continuity then becomes:

$$C_{PD} \frac{dV_{PD}(t)}{dt} = I_a(t) + i_n(t) = I_0 e^{-\beta V_{PD}(t)} + i_n(t) \quad (12)$$

where $I_a(t)$ is the average drain current, I_0 is a constant, $\beta \equiv q/mkT$, $m \equiv 1 + C_D/C_G$, C_D is the depletion-layer capacitance and C_G is the gate capacitance. Typically, m is slightly above 1. The subthreshold current has shot noise with autocorrelation:

$$\langle i_n(t_1) i_n(t_2) \rangle = q I_a(t) \delta(t_1 - t_2) \quad (13)$$

For Step 1, the continuity equation for the average voltage $V_{PDa}(t)$ is given by:

$$C_{PD} \frac{dV_{PDa}(t)}{dt} = I_0 e^{-\beta V_{PDa}(t)} \quad (14)$$

Even though this equation is nonlinear, it has an analytical solution, which can be obtained with the variation of parameters method. The solution [2] is:

$$V_{PDa}(t) = \frac{1}{\beta} \log \left[e^{\beta V_{PDa}(0)} + \frac{t}{\tau} \right] \quad (15)$$

where:

$$\tau \equiv C_{PD} / \beta I_0 \quad (16)$$

The existence of this analytical solution is essential for the subthreshold current reset noise analyses. When $t \rightarrow \infty$, $V_{PDa}(t)$ diverges slowly as a logarithmic function. The soft reset has no finite limit, even though the hard reset has V_{RD0} as a limit. This is an important characteristic for the soft reset.

For Step 2, we substitute Equation (15) into Equation (12) and obtain the equation for $v_{PD}(t)$:

$$C_{PD} \frac{dv_{PD}(t)}{dt} = -I_0 e^{-\beta V_{PDa}(0)} \frac{1 - e^{-\beta v_{PD}(t)}}{1 + \frac{t}{\tau} e^{-\beta V_{PDa}(0)}} + i_n(t) \quad (17)$$

Considering that $\beta v_{PD}(t) \ll 1$, the approximation, $e^{-\beta v_{PD}(t)} \approx 1 - \beta v_{PD}(t)$ can be used. Equation (17) then becomes a linear equation:

$$C_{PD} \frac{dv_{PD}(t)}{dt} = -\frac{\beta I_0 e^{-\beta V_{PDa}(0)}}{1 + \frac{t}{\tau} e^{-\beta V_{PDa}(0)}} v_{PD}(t) + i_n(t) \quad (18)$$

Its solution is given by:

$$v_{PD}(t) = \frac{1}{C_{PD}} \int_0^t dt' \frac{1 + \frac{t'}{\tau} e^{-\beta V_{PDa}(0)}}{1 + \frac{t'}{\tau} e^{-\beta V_{PDa}(0)}} i_n(t') + \frac{1}{1 + \frac{t}{\tau} e^{-\beta V_{PDa}(0)}} v_{PD}(0) \quad (19)$$

For Step 3, squaring Equation (19), averaging and using Equation (13), the soft reset noise variance can be obtained:

$$\langle v_{PD}(t)^2 \rangle = \frac{mkT}{2C_{PD}} \left(1 - \frac{1}{\left(1 + \frac{t}{\tau} e^{-\beta V_{PDa}(0)}\right)^2} \right) + \langle v_{PD}(0)^2 \rangle \frac{1}{\left(1 + \frac{t}{\tau} e^{-\beta V_{PDa}(0)}\right)^2} \quad (20)$$

Using the fact that $I(0) = I_0 e^{-\beta V_{PDa}(0)}$, Equation (20) can be rewritten as:

$$\langle v_{PD}(t)^2 \rangle = \frac{mkT}{2C_{PD}} \left(1 - \frac{1}{\left(1 + \frac{qI_a(0)t}{mkTC_{PD}}\right)^2} \right) + \langle v_{PD}(0)^2 \rangle \frac{1}{\left(1 + \frac{qI_a(0)t}{mkTC_{PD}}\right)^2} \quad (21)$$

The first term is caused by shot noise, and the second term results from the initial condition. When $t \rightarrow \infty$, the asymptotic form and the limit are obtained as:

$$\langle v_{PD}(t)^2 \rangle \approx \frac{mkT}{2C_{PD}} \left(1 - \left(\frac{mkTC_{PD}}{qI_a(0)t}\right)^2 \right) + \langle v_{PD}(0)^2 \rangle \left(\frac{mkTC_{PD}}{qI_a(0)t}\right)^2 \quad (22)$$

$$\langle v_{PD}(t)^2 \rangle \rightarrow \frac{mkT}{2C_{PD}} \quad (23)$$

It should be noted that the asymptotic time dependence of the noise standard deviation $\sqrt{\langle v_{PD}(t)^2 \rangle}$ behaves as t^{-1} although the asymptotic time dependence of the average PD voltage, $V_{PDa}(t)$ behaves as $\log t$ (as shown in Equation (15)), which is much slower than t^{-1} . In the hard reset case, $V_{PDa}(t)$ and $\sqrt{\langle v_{PD}(t)^2 \rangle}$ have the same exponential time dependence (with $e^{-t/\tau_{HR}}$). The determinant time constant in Equations (21) and (22), $\tau_{SR} \equiv mkTC_{PD}/qI(0)$, is calculated for a typical case, as follows. Assuming that $C_{PD} = 10$ fF, $I_a(0) = 0.5$ μ A, $v_{th} \equiv kT/q = 26$ mV (at 300 K), $m = 1$, we have that $\tau_{SR} = 0.52$ ns. It is small enough when compared with the typical reset period, 1 μ s. The limit at $t \rightarrow \infty$ is $mkT/2C_{PD}$, which fits the results obtained in previous works [19,20,30–33].

To alleviate the image lag problem of soft reset image sensors [2], the flushed reset method was proposed [20,22,33]. In this method, during one reset period, a hard reset is first carried out to eliminate vestige of the previous signal, and a soft reset is then performed to reduce the reset noise. The timing chart for a simple case of the flushed reset method is shown in Figure 2b. The reset noise variance after the hard reset is kT/C_{PD} , as given as Equation (11), and this becomes the initial condition for the soft rest period. Substituting $\langle v_{PD}(0)^2 \rangle = kT/C_{PD}$ into Equation (21), the flushed reset noise can be derived as:

$$\langle v_{PD}(t)^2 \rangle = \frac{mkT}{2C_{PD}} \left(1 + \left(\frac{2}{m} - 1\right) \frac{1}{\left(1 + \frac{qI(0)t}{mkTC_{PD}}\right)^2} \right) \quad (24)$$

If the reset period is enough long, $\langle v_{PD}(t)^2 \rangle$ becomes:

$$\langle v_{PD}(t)^2 \rangle \rightarrow \frac{mkT}{2C_{PD}} \quad (25)$$

The hard reset part eliminates image lag, and the soft reset part reduces the reset noise to the soft reset level.

5. Tapered Reset

To improve the convergence at the soft reset method, tapered reset is proposed. In this method, the RST gate voltage is gradually decreased to 0 V during the soft reset period, as shown in Figure 2c. The continuity equation becomes:

$$C_{PD} \frac{dV_{PD}(t)}{dt} = I_a(t) + i_n(t) = I_0 e^{-\beta V_{PD}(t) - \beta at} + i_n(t) \quad (26)$$

where a is a positive constant characterizing the slope of the RST taper, in unit of V/s.

For Step 1, the continuity equation for average voltage $V_{PDa}(t)$ is:

$$C_{PD} \frac{dV_{PDa}(t)}{dt} = I_0 e^{-\beta V_{PDa}(t) - \beta at} \quad (27)$$

Its solution is:

$$V_{PDa}(t) = \frac{1}{\beta} \log \left[e^{\beta V_{PDa}(0)} + \frac{I_0}{C_{PDa}} (1 - e^{-\beta at}) \right] \quad (28)$$

When $t \rightarrow \infty$, $V_{PDa}(t)$ converges to:

$$V_{PDa}(\infty) = \frac{1}{\beta} \log \left[e^{\beta V_{PDa}(0)} + \frac{I_0}{C_{PDa}} \right] \quad (29)$$

As seen, while $V_{PDa}(t)$ for the soft reset diverges slowly as logarithmic function, that of the tapered reset converges exponentially to a constant; this happens because the drain current is extinguished as a consequent of the taper control.

For Step 2, substituting Equation (28) into Equation (26), the equation for $v_{PD}(t)$ can be obtained as:

$$C_{PD} \frac{dv_{PD}(t)}{dt} = -I_0 e^{-\beta V_{PDa}(0) - \beta at} \frac{1 - e^{-\beta v_{PD}(t)}}{1 + \frac{I_0 e^{-\beta V_{PDa}(0)}}{C_{PDa}} (1 - e^{-\beta at})} + i_n(t) \quad (30)$$

Considering that $\beta v_{PD}(t) \ll 1$, the approximation $e^{-\beta v_{PD}(t)} \approx 1 - \beta v_{PD}(t)$ can be used. Equation (30) then becomes a linear equation:

$$C_{PD} \frac{dv_{PD}(t)}{dt} = -\frac{\beta I_0 e^{-\beta V_{PDa}(0) - \beta at}}{1 + \frac{I_0 e^{-\beta V_{PDa}(0)}}{C_{PDa}} (1 - e^{-\beta at})} v_{PD}(t) + i_n(t) \quad (31)$$

Its solution is written as:

$$v_{PD}(t) = \frac{1}{C_{PD}} \int_0^t dt' \frac{1 + \frac{I_0 e^{-\beta V_{PDa}(0)}}{C_{PDa}} (1 - e^{-\beta at'})}{1 + \frac{I_0 e^{-\beta V_{PDa}(0)}}{C_{PDa}} (1 - e^{-\beta at})} i_n(t') + \frac{1}{1 + \frac{I_0 e^{-\beta V_{PDa}(0)}}{C_{PDa}} (1 - e^{-\beta at})} v_{PD}(0) \quad (32)$$

For Step 3, squaring Equation (32), averaging and using Equation (13), the tapered reset noise variance can be derived:

$$\langle v_{PD}(t)^2 \rangle = \frac{mkT}{2C_{PD}} \left(1 - \frac{1}{\left(1 + \frac{I_a(0)}{C_{PDa}} (1 - e^{-\beta at}) \right)^2} \right) + \langle v_{PD}(0)^2 \rangle \frac{1}{\left(1 + \frac{I_a(0)}{C_{PDa}} (1 - e^{-\beta at}) \right)^2} \quad (33)$$

If a is so small that $\beta at \ll 1$, $e^{-\beta at} \approx 1 - \beta at$, Equation (33) then becomes identical to that of the soft reset case, Equation (22). On the other hand, if a is enough large, $e^{-\beta at}$ decays so fast that the reset noise variance $\langle v_{PD}(t)^2 \rangle$ cannot reach the soft reset level. Therefore, the tapered reset shown in Figure 2c is not useful for noise reduction, although the average voltage $V_{PDa}(t)$, converges exponentially.

6. Feedback Reset with Reverse Taper Control (FRRT)

The feedback reset method was also proposed as a mean to reduce the reset noise [13,20–26]. In this method, during the reset period, the noisy PD voltage is detected and the resulting negative feedback forces the PD voltage to approach the reference level. A bidirectional current is needed at the RST for effective feedback, even though the subthreshold current is essentially unidirectional [13,24]. The concept of an “unidirectional current” means, in this context, that if the feedback (relaxation) times for upper and lower fluctuations are very different because of the current nonlinearity, the current looks unidirectional from the feedback point of view. One solution to overcome this contradiction is to constantly inject electrons into the PD; these injected electrons can then effectively play the role of a current flowing in the opposite direction. There are a couple of methods to perform this injection; one is to slowly ramp the RST gate toward the on-direction or positive direction [23,24], in contrast with what is done in the tapered reset method discussed in Section 5. Another method is to ramp the RST source voltage toward the on-direction or negative direction, as will be explained in detail in this section. It should be noted that electrons flow from the RD to the PD in both cases, regardless of the name of “drain”. Therefore, this method can be named feedback reset with reverse taper control (FRRT).

There is another important point to be considered when discussing reset noise reduction; excrescent noise should not be generated when the RST is turned off at the end of the reset period. If there are electrons at the RST channel just before it is turned off, these electrons are partitioned to the PD and the RD. This partitioning has a stochastic nature, and generates the partition noise [34]. Subthreshold operation tends to reduce this effect, because the electron number at the RST channel is smaller and unidirectional current is involved.

Figure 3a shows a schematic diagram of the pixel and the related column-based feedback circuits for the FRRT to be analyzed in this section. The feedback is applied through the RD. The pixel structure is the same as the conventional 3-Tr scheme, as shown in Figure 1. The exception is that the RD wiring is prepared separately from the SF drain line. Other feedback circuits and ramp circuits are column-based. The vertical signal line, transferring the SF output voltage, is connected to the negative input of a column-based differential amplifier together with the load transistor and the following signal circuits. The positive input is connected with a ramp generator, $V_{Ref} = a_0 - at$. The output of the amplifier is connected to the RD through the additional vertical RD line. Both the parasitic capacitance between PD and RD, C_{P1} , and the parasitic capacitance between the vertical signal line and the RD line, C_{P2} , are included in the analysis. It is assumed that C_{PD} does not include C_{P1} . The timing chart for this structure is shown in Figure 4. After one row is selected by SEL, signal is read out in a fashion similar to the one of the conventional 3-Tr scheme at first. During the reset period, a hard reset is carried out in front to eliminate vestiges of previous signals, by setting Flush to ON. Subsequently, FRRT is executed turning FB ON and gradually decreasing V_{Ref} .

Figure 3b shows the simplified schematic diagram for noise modeling; the SF is merged with the high-gain differential amplifier, of gain A . The amplifier is also assumed to be faster than the reset motion. The parasitic capacitances and parasitic resistances (which delay the feedback) for both the vertical signal line and the RD line are neglected because fast feedback is assumed. The parasitic capacitances C_{P1} and C_{P2} are included because they have an important role in the feedback connecting the amplifier’s input and output. Only reset noise or RST channel noise is considered here; noises from the SF, the differential amplifier, SEL and wiring resistances are not included, because the reset noise is dominant.

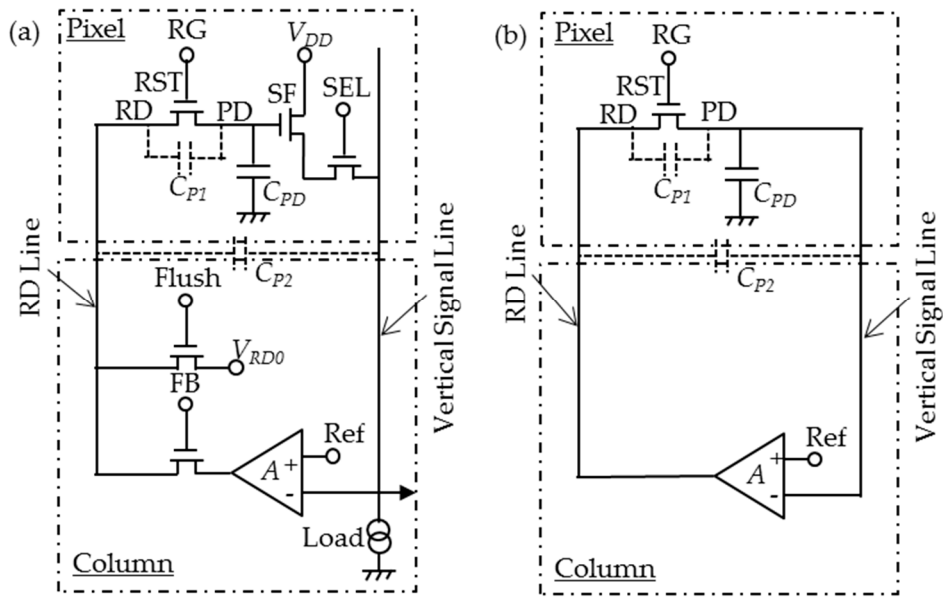


Figure 3. Feedback reset with reverse tapered control (FRRT); (a) Schematic; (b) Simplified model for noise analysis.

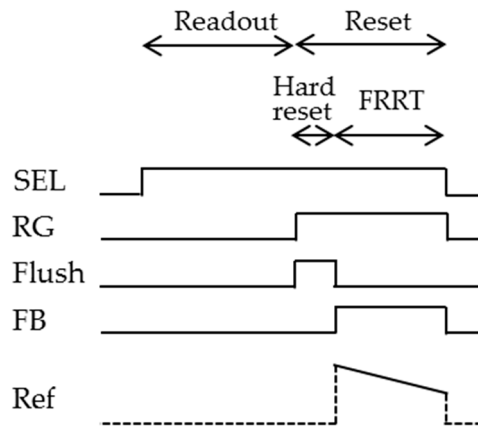


Figure 4. Timing diagram for one pixel in the case of feedback reset with reverse tapered control (FRRT).

The continuity equation becomes:

$$(C_{PD} + C_{P1} + C_{P2}) \frac{dV_{PD}(t)}{dt} - (C_{P1} + C_{P2}) \frac{dV_{RD}(t)}{dt} = -I_a(t) - i_n(t) = -I_0 e^{-\beta V_{RD}(t)} - i_n(t) \quad (34)$$

$$V_{RD}(t) = A (V_{Ref}(t) - V_{PD}(t)) = A (a_0 - at - V_{PD}(t)) \quad (35)$$

Substituting Equation (35) into Equation (34), the equation for $V_{PD}(t)$ is obtained as:

$$C_T \frac{dV_{PD}(t)}{dt} = -AC_{Pa} - I_0 e^{-\beta A(a_0 - at - V_{PD}(t))} - i_n(t) \quad (36)$$

where $C_T \equiv C_{PD} + (1 + A) C_P$ and $C_P \equiv C_{P1} + C_{P2}$.

For Step 1, the continuity equation for average voltage $V_{PDa}(t)$, is:

$$C_T \frac{dV_{PDa}(t)}{dt} = -AC_{Pa} - I_0 e^{-\beta A(a_0 - at - V_{PDa}(t))} \quad (37)$$

Equation (37) can be transformed to eliminate the constant term, as follows:

$$C_T \frac{d(V_{PDa}(t) + \frac{AC_P a}{C_T} t)}{dt} = -I_0 e^{-\beta A(a_0 - a \frac{C_{PD} + C_P}{C_T} t - (V_{PDa}(t) + \frac{AC_P a}{C_T} t))} \quad (38)$$

Its solution is:

$$V_{PDa}(t) = -\frac{AC_P a}{C_T} t - \frac{1}{\beta A} \log \left[e^{-\beta A V_{PDa}(0)} + \frac{I_0 e^{-\beta A a_0}}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1) \right] \quad (39)$$

where:

$$\tau_{FRRT} \equiv \frac{1}{\beta A a} \frac{C_T}{C_{PD} + C_P} \quad (40)$$

when $t \rightarrow \infty$, $V_{PDa}(t)$ approaches the asymptotic form exponentially:

$$V_{PDa}(t) \rightarrow -at \quad (41)$$

This divergence is reasonable because of the substantial charge injection to the PD.

For Step 2, substituting Equation (39) into Equation (36) we obtain the equation for $v_{PD}(t)$ as:

$$C_T \frac{dv_{PD}(t)}{dt} = \frac{I_a(0) e^{t/\tau_{FRRT}} (1 - e^{\beta A v_{PD}(t)})}{1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1)} - i_n(t) \quad (42)$$

Assuming that $\beta A v_{PD}(t) \ll 1$, one can use the approximation:

$$e^{\beta A v_{PD}(t)} \approx 1 + \beta A v_{PD}(t) \quad (43)$$

The validity of this assumption will be discussed later. Equation (42) then becomes a linear equation as:

$$C_T \frac{dv_{PD}(t)}{dt} = -\frac{\beta A I_a(0) e^{t/\tau_{FRRT}} v_{PD}(t)}{1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1)} - i_n(t) \quad (44)$$

The solution can be written as:

$$v_{PD}(t) = -\frac{1}{C_T} \int_0^t dt' \frac{1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t'/\tau_{FRRT}} - 1)}{1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t'/\tau_{FRRT}} - 1)} i_n(t') + \frac{1}{1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1)} v_{PD}(0) \quad (45)$$

For Step 3, squaring Equation (45), averaging and using Equation (13), the noise variance can be obtained as:

$$\langle v_{PD}(t)^2 \rangle = \frac{mkT}{2AC_T} \left(1 - \frac{1}{\left(1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1) \right)^2} \right) + \langle v_{PD}(0)^2 \rangle \frac{1}{\left(1 + \frac{I_a(0)}{a(C_{PD} + C_P)} (e^{t/\tau_{FRRT}} - 1) \right)^2} \quad (46)$$

The first term is caused by shot noise, and the second term results from the initial condition. When $C_P = 0$, the asymptotic form and the limit for $t \rightarrow \infty$ are obtained as:

$$\langle v_{PD}(t)^2 \rangle \approx \frac{mkT}{2AC_{PD}} (1 - e^{-2\beta A a t}) + \langle v_{PD}(0)^2 \rangle e^{-2\beta A a t} \quad (47)$$

$$\langle v_{PD}(\infty)^2 \rangle = \frac{mkT}{2AC_{PD}} \quad (48)$$

When $C_P \neq 0$, the asymptotic form and the limit for $t \rightarrow \infty$ become:

$$\langle v_{PD}(t)^2 \rangle \approx \frac{mkT}{2A(C_{PD} + (1+A)C_P)}(1 - e^{-2t/\tau_{FRRT}}) + \langle v_{PD}(0)^2 \rangle e^{-2t/\tau_{FRRT}} \quad (49)$$

$$\langle v_{PD}(\infty)^2 \rangle = \frac{mkT}{2A(C_{PD} + (1+A)C_P)} \quad (50)$$

Considering that the detection capacitance is $C_{PD} + C_{P1}$, the reset noise variance in electron numbers at the PD, $\langle n_{PD}(\infty)^2 \rangle$, is derived as:

$$\langle n_{PD}(\infty)^2 \rangle = \frac{mkT(C_{PD} + C_{P1})^2}{2q^2 A(C_{PD} + (1+A)C_P)} \quad (51)$$

where q denotes the electronic elementary charge.

When $C_P = 0$, the limit of the noise variance $\langle v_{PD}(\infty)^2 \rangle$ is $1/A$ times smaller than the soft reset noise variance, $mkT/2C_{PD}$, according to Equation (48). The reset noise is therefore much reduced. When $C_P \neq 0$, $\langle v_{PD}(\infty)^2 \rangle$ becomes even smaller than that when $C_P = 0$, because C_P couples the output of the amplifier to the PD directly, which contributes as a capacitive feedback [21], in addition to the feedback path through the RST. However, large values of C_P have some drawbacks as well; if the vertical signal line capacitance and the RD line capacitance are large, the feedback speed is limited. If C_{P1} is large, the conversion gain is decreased. Figure 5a,b show the reset noises for $C_{PD} = 10$ fF and 1 fF, respectively. The horizontal axis denotes the amplifier gain A , the left vertical axis represents the reset noise voltage $\sqrt{\langle v_{PD}(\infty)^2 \rangle}$, and the right vertical axis represents the reset noise in number of electrons, $\sqrt{\langle n_{PD}(\infty)^2 \rangle}$. The parameter for the curves is C_P/C_{PD} . Here, $C_{P1} = 0$ fF is assumed for simplicity. According to this simple model, the reset noise decreases as $A^{-1/2}$ when $C_P/C_{PD} = 0$. When $C_P/C_{PD} \neq 0$, the reset noise decreases also as $A^{-1/2}$ for large values of A . It should be noted that even small values of C_P/C_{PD} will drastically decrease the reset noise when A is larger. For example, when $C_P/C_{PD} = 0.01$, the reset noise is decreased to 30% at $A = 1000$. If $C_{PD} + C_{P1}$ is smaller, reset noise in number of electrons becomes smaller while the reset noise in voltage becomes larger. It is important to reduce the detection capacitance, $C_{PD} + C_{P1}$, as is also the case with the 4-Tr. scheme.

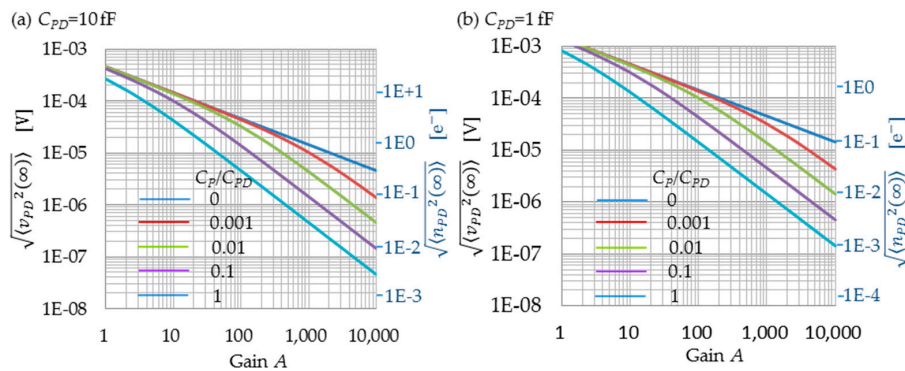


Figure 5. FRRT reset noise, with $C_{P1} = 1$ fF. (a) $C_{PD} = 10$ fF; (b) $C_{PD} = 1$ fF. $\langle v_{PD}(\infty)^2 \rangle$ is the reset noise variance in voltage at the PD as expressed by (50), and $\langle n_{PD}(\infty)^2 \rangle$ is the reset noise variance in number of electrons at the PD as expressed by (51).

Even though FRRT uses a subthreshold current mode, $\langle v_{PD}(t)^2 \rangle$ still converges with fast exponential decay. Figure 6 shows the time constant, $\tau_{FRRT}/2$. In the figure, the horizontal axis represents the gain A , assuming that $a = 0.1$ V/ μ s. When $C_P = 0$, the time constant decreases linearly with $1/A$. When $C_P \neq 0$, $\tau_{FRRT}/2$ decreases linearly with $1/A$ at $AC_P \ll C_{PD}$, while it becomes a

constant with the value $\frac{C_P}{2\beta a(C_{PD}+C_P)}$ for $AC_P \gg C_{PD}$. In the extremely unfavorable case of $C_P/C_{PD} = 10$, $\tau_{FRRT}/2$ is in practice sufficiently small (as small as 0.12 μ s) for $A > 20$.

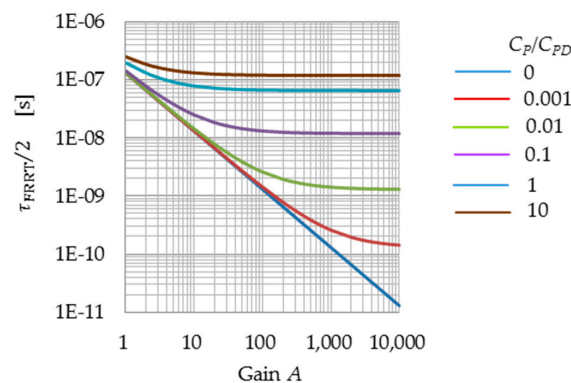


Figure 6. Time constant of the FRRT process (τ_{FRRT}), for $a = 0.1$ V/ μ s. $\frac{\tau_{FRRT}}{2} \equiv \frac{1}{2\beta A a} \frac{C_{PD} + (1+A)C_P}{C_{PD} + C_P}$.

Using FRRT, the reset voltage is reduced by aT_{FRRT} , where T_{FRRT} is FRRT period. This decreases the saturation of the PD. If a is increased τ_{FRRT} decreases as $1/a$, and T_{FRRT} can be decreased in the same manner. If T_{FRRT} is adjusted properly, a will not affect the reset voltage reduction, because the reset voltage reduction dependence on a is given as $aT_{FRRT} \sim a^0$.

According to this simple model, if $A \rightarrow \infty$, $\langle v_{PD}(\infty)^2 \rangle \rightarrow 0$. Limitations to this ideal case should be discussed below.

Firstly, the approximation:

$$e^{\beta A v_{PD}(t)} \approx 1 + \beta A v_{PD}(t)$$

is examined. If 30% of error is allowed, $\beta A v_{PD}(t)$ is limited by:

$$1 \leq \frac{e^{\beta A v_{PD}(t)}}{1 + \beta A v_{PD}(t)} < 1.3 \quad (52)$$

which is always larger than 1. This means that $\beta A v_{PD}(t)$ should be smaller than 0.91. Figure 7a,b shows the exponent $\beta A \sqrt{\langle v_{PD}(\infty)^2 \rangle}$, substituting $\sqrt{\langle v_{PD}(\infty)^2 \rangle}$ to $v_{PD}(t)$ and using $m = 1$ and $1/\beta = mkT/q = 0.026$ V. When $C_P = 0$, the upper limit of A is obtained as 2,700 for $C_{PD} = 10$ fF and as 270 for $C_{PD} = 1$ fF, respectively. When $C_P \neq 0$, and because Equation (50) < Equation (48), the range within which approximation Equation (43) can be used becomes larger. For example, when $C_P/C_{PD} = 0.01$, the upper limit becomes more than 10,000 for both $C_{PD} = 10$ fF and $C_{PD} = 1$ fF.

If the approximation Equation (43) becomes invalid, the quantitative discussion is difficult. However, the feedback effect becomes rather larger because the first term of right hand side at Equation (42) has larger negative value than that of Equation (44).

Secondly, the assumption that the feedback is faster than the reset motion should be discussed. Both the differential and SF amplifiers have finite output impedances, and both the vertical signal and RD lines have parasitic resistances and capacitances; this means that the feedback has a finite time constant. It increases as A increases. If the feedback becomes slow compared with the reset motion, the reset noise is increased in reverse. Therefore, the reset noise has a minimum at some value of A .

The dimensionless factor $I_a(0)/a(C_{PD} + C_P)$, at Equation (46) represents the ratio between the PD voltage change, $I_a(0)/(C_{PD} + C_P)$, and the taper slope, a . Assuming typical parameters: $I_a(0) = 0.5$ μ A, $C_{PD} = 10$ fF, $C_P/C_{PD} = 0.01$, and $a = 0.1$ V/ μ s, we obtain $I_a(0)/a(C_{PD} + C_P) = 0.5$, which is in the order of 1 and does not affect the convergence.

As discussed above, it can be said that FRRT reduces both reset noise and the convergence time constant. In fact, $2.5 e^-$ rms reset noise and $2.9 e^-$ rms readout noise have been reported, using organic photoconductive film CMOS image sensor with 3 μ m pixel, 5 μ s reset period and $A = 100$ [13].

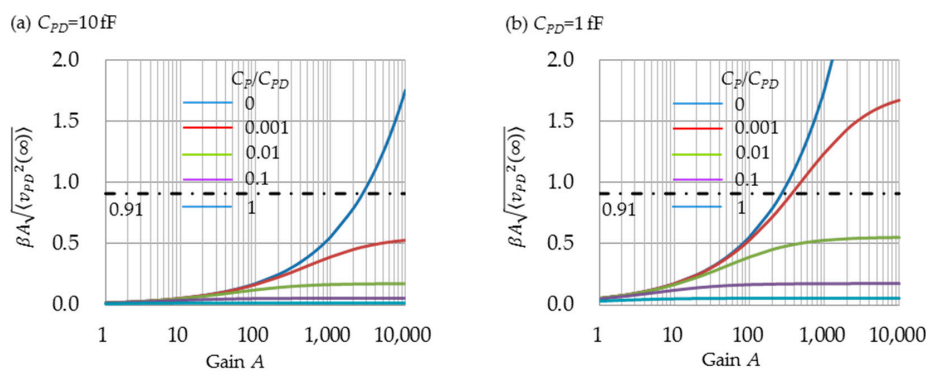


Figure 7. Dependence of $\beta A \sqrt{\langle v_{PD}^2(\infty) \rangle}$ on gain A and the ratio C_P/C_{PD} . If $\beta A \sqrt{\langle v_{PD}^2(\infty) \rangle}$ is smaller than 0.91, the approximation Equation (43) is permitted with an error of 30%.

7. Electron Counting Possibility

Photon counting imaging is one of the grand targets for image sensor development. It requires two conditions; electron counting and high quantum efficiency [35]. In this section, the possibility of electron counting using FRRT is discussed, leveraging the good properties of the 3-Tr scheme discussed in Section 1. Before that, the statuses of other approaches, such as single-photon avalanche diode (SPAD) image sensors [36,37] and 4-Tr CMOS image sensors are reviewed.

The Geiger mode avalanche in SPADs creates a sharp spike signal from one original photon-generated electron-hole pair. The spike signal is so large that the in-pixel circuitry detects it as a digital signal and subsequent stages do not add any noises. The sharp spike also realizes time stamp, which is important for various applications such as time of flight, or fluorescence lifetime imaging microscopy (FLIM). Its weak points are the large dark count, the after pulse, and small fill factor because of the necessity of a guardring.

The 4-Tr CMOS image sensor saw some progress in 2015 [38–42], obtained mainly by reducing the detection capacitance or floating diffusion capacitance. It brings large conversion gain (as large as $426 \mu\text{V}/e^-$) [41] and a readout noise as small as $0.27 e^- \cdot \text{rms}$ [42]. If the readout noise is less than $0.3 e^- \cdot \text{rms}$, it can be said that electron counting is possible with the 90% confidence level [35]. This method has a rather small dark current and a larger fill factor—even for small pixels—than SPADs. It also does not suffer from the after pulse. The 4-Tr scheme is not convenient for time stamping, because the pixel has to wait for a photon after holding the reset level for CDS and a longer period between reset sampling signal sampling makes the CDS $1/f$ noise reduction less effective. In contrast, the 3-Tr scheme is operated in a “signal-first, reset-later” mode, which is suitable for time stamping.

With the FRRT simple model shown in the previous section, the reset noise, $\sqrt{\langle n_{PD}(\infty)^2 \rangle}$, becomes $0.10 e^- \cdot \text{rms}$ for $C_{PD} = 10$ fF, $C_P/C_{PD} = 0.01$, and $A = 2,700$, and becomes $0.29 e^- \cdot \text{rms}$ for $C_{PD} = 1$ fF, $C_P/C_{PD} = 0.01$, and $A = 270$. In those cases, the possibility of electron counting exists if the other noises are small enough.

The readout noise for the 3-Tr scheme is constituted by the reset noise, SF thermal noise, SF $1/f$ noise, column circuit noise, and ADC quantization noise. The most effective method to reduce these noises is to reduce the detection capacitance and to increase the conversion gain as done in the 4-Tr scheme; the noises will then be reduced in terms of the number of electrons at the PD. There is, however, a sharp tradeoff between the reduction of the detection capacitance and sensitivity, because using a smaller PD area to decrease the detection capacitance originates also a small sensitivity. Therefore, it is much difficult to achieve an electron counting capability with a 3-Tr scheme than with a 4-Tr scheme. One possible circuit-based approach is to combine a capacitive transimpedance amplifier [27,28] with the FRRT.

Another possibility is to reduce the SF thermal noise, SF $1/f$ noise, column circuit noise, and ADC quantization noise themselves. The SF thermal noise, column circuit noise, and ADC quantization

noise could be reduced by circuit technologies. Although various methods have been reported to reduce the SF $1/f$ noise, additional improvements are needed to perform the electron counting with a 3-Tr scheme.

8. Conclusions

To discuss the reset noise generated by a slow subthreshold current, intuitive and simple analytical forms are derived in spite of the subthreshold current nonlinearity, which characterize the time evolution of the reset noise during the reset operation.

For soft reset, the reset noise limit when $t \rightarrow \infty$, $\sqrt{\langle v_{PD}(\infty)^2 \rangle}$, is given by $\sqrt{mkT/2C_{PD}}$, which agrees with previous published works. The asymptotic time dependence of the noise, $\sqrt{\langle v_{PD}(t)^2 \rangle}$, decreases with t^{-1} , even though the asymptotic time dependence of the average PD voltage, $V_{PDa}(t)$, is as slow as $\log t$. The flush reset method is effective because the hard reset part eliminates the image lag, and soft reset part reduces the noise to the soft reset noise level.

The tapered reset method achieves exponential convergence, but the reset noise reduction is insufficient.

Finally, the FRRT shows both a fast convergence and a good reset noise reduction. When A is large, even small values of C_P/C_{PD} can drastically decrease the reset noise. If the feedback is sufficiently fast, the reset noise limit when $t \rightarrow \infty$, $\sqrt{\langle n_{PD}(\infty)^2 \rangle}$, becomes $\frac{mkT(C_{PD}+C_{P1})^2}{2q^2A(C_{PD}+(1+A)C_P)}$. Assuming that $C_{PD} = 10$ fF, $C_P/C_{PD} = 0.01$ and $A = 2700$, $\sqrt{\langle n_{PD}(\infty)^2 \rangle}$ becomes $0.10 e^- \cdot \text{rms}$ according to this simple model. Achieving an electron counting capability with this architecture requires a challenging $1/f$ noise reduction, even if the reset noise can be decreased.

Conflicts of Interest: The authors declare no conflict of interest.

Abbreviations

The following abbreviations are used in this manuscript:

| | |
|--------|---|
| Tr | Transistor |
| CMOS | Complementary metal oxide semiconductor |
| PPD | Pinned photodiode |
| CDS | Correlated double sampling |
| AEC | Auto exposure control |
| PD | Photodiode |
| UV | Ultraviolet |
| IR | Infrared |
| Ge | Germanium |
| InGaAs | Indium-gallium-arsenide |
| RST | Reset transistor |
| SF | Source follower |
| SEL | Row select transistor |
| k | Boltzmann constant |
| T | Absolute temperature |
| C | Capacitance |
| e^- | Electron |
| CCD | Charge coupled devices |
| RD | Reset transistor drain |
| FRRT | Feedback reset with reverse taper control |
| FB | Feedback |
| Ref | Reference |
| SPAD | Single photon avalanche diode |
| FLIM | Fluorescence lifetime imaging microscopy |
| rms | Root mean square |

References

1. Mendis, S.K.; Kemeny, S.E.; Fossum, E.R. A 128×128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems. In Proceedings of the IEEE International Electron Devices Meeting (IEDM), Washington, DC, USA, 5–8 December 1993; pp. 583–586.
2. Teranishi, N.; Kohono, A.; Ishihara, Y.; Oda, E.; Arai, K. No image Lag Photodiode Structure in the Interline CCD Image Sensor. In Proceedings of the IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, 13–15 December 1982; pp. 324–327.
3. Lee, P.P.K.; Gee, R.C.; Guidash, R.M.; Lee, T.; Fossum, E.R. An Active Pixel Sensor Fabricated Using CMOS/CCD Process Technology. In Proceedings of the of IEEE Workshop on CCD and Advanced Image Sensors, Dana Point, CA, USA, 20–22 April 1995.
4. Inoue, I.; Nozaki, H.; Yamashita, H.; Yamaguchi, T.; Ishiwata, H.; Ihara, H.; Miyagawa, R.; Miura, H.; Nakamura, N.; Egawa, Y.; *et al.* New LV-BPD (Low Voltage Buried Photo-Diode) for CMOS Imager. In Proceedings of the Technical Digest International Electron Devices Meeting (IEDM), Washington, DC, USA, 5–8 December 1999; pp. 883–886.
5. Yonemoto, K.; Sumi, H.; Suzuki, R.; Ueno, T. A CMOS Image Sensor with a Simple FPN Reduction Technology and Hole Accumulated Diode. In Proceedings of the IEEE International Solid-State Circuits Conference (ISSCC) Digest of Technical Papers, San Francisco, CA, USA, 9 February 2000; pp. 102–103.
6. Fossum, E.R.; Hondongwa, D.B. A Review of the Pinned Photodiode for CCD and CMOS Image Sensors. *IEEE J. Electron Devices Soc.* **2014**, *2*, 33–43. [[CrossRef](#)]
7. White, M.H.; Lampe, D.R.; Blaha, F.C.; Mack, I.A. Characterization of Surface Channel CCD Image Arrays at Low Light Levels. *IEEE J. Solid State Circuit* **1974**, *SC-9*, 1–13. [[CrossRef](#)]
8. Takahashi, H.; Kinoshit, M.; Morita, K.; Shirai, T.; Sato, T.; Kimura, T.; Yuzurihara, H.; Inoue, S. A $3.9 \mu\text{m}$ pixel pitch VGA format 10 b digital image sensor with 1.5-transistor/pixel. In Proceedings of the IEEE International Solid-State Circuits Conference (ISSCC) Digest of Technical Papers, San Francisco, CA, USA, 15–19 February 2004; pp. 108–109.
9. Mori, M.; Katsuno, M.; Kasuga, S.; Murata, T.; Yamaguchi, T. A $1/4$ in 2M pixel CMOS Image Sensor with 1.75 Transistor/Pixel. In Proceedings of the IEEE International Solid-State Circuits Conference (ISSCC) Digest of Technical Papers, San Francisco, CA, USA, 15–19 February 2004; pp. 110–111.
10. Itonaga, K.; Mizuta, K.; Kataoka, T.; Yanagita, M.; Ikeda, H.; Ishiwata, H.; Tanaka, Y.; Wakano, T.; Matoba, Y.; Oishi, T.; *et al.* Extremely-Low-Noise CMOS Image Sensor with High Saturation Capacity. In Proceedings of the IEEE International Electron Devices Meeting (IEDM), Washington, DC, USA, 5–7 December 2011; pp. 171–174.
11. Korthout, L.; Verbugt, D.; Timpert, J.; Mierop, A.; de Haan, W.; Maes, W.; de Meulmeester, J.; Muhammad, W.; Dillen, B.; Stoldt, H.; *et al.* A wafer-scale CMOS APS imager for medical X-ray applications. In Proceedings of the International Image Sensors Workshop, Bergen, Norway, 22–28 June 2009.
12. Ihama, M.; Inomata, H.; Asano, H.; Imai, S.; Mitsui, T.; Imada, Y.; Hayashi, M.; Gotou, T.; Suzuki, H.; Sawaki, D.; *et al.* CMOS Image Sensor with an Overlaid Organic Photoelectric Conversion Layer: Optical Advantages of Capturing Slanting Rays of Light. In Proceedings of the International Image Sensors Workshop, Hokkaido, Japan, 8–11 June 2011; pp. 153–156.
13. Ishii, M.; Kasuga, S.; Yazawa, K.; Sakata, Y.; Okino, T.; Sato, Y.; Hirase, J.; Hirose, Y.; Tamaki, T.; Matsunaga, Y.; *et al.* An Ultra-low Noise Photoconductive Film Image Sensor with a High-speed Column Feedback Amplifier Noise Canceller. In Proceedings of 2013 Symposium on VLSI Circuits Digest of Technical Papers, Kyoto, Japan, 10–13 June 2013; pp. C8–C9.
14. Imura, S.; Kikuchi, K.; Miyakawa, K.; Ohtake, H.; Kubota, M.; Okino, T.; Hirose, Y.; Kato, Y.; Teranishi, N. High Sensitivity Image Sensor Overlaid with Thin-Film Crystalline-Selenium-based Heterojunction Photodiode. In Proceedings of the 2014 IEEE International Electron Devices Meeting, San Francisco, CA, USA, 15–17 December 2014; pp. 88–91.
15. Aberg, I.; Ackland, B.; Beach, J.V.; Godek, C.; Johnson, R.; King, C.A.; Lattes, A.; O'Neill, J.; Pappas, S.; Sriram, T.S.; *et al.* A Low Dark Current and High Quantum Efficiency Monolithic Germanium-on-Silicon CMOS Imager Technology for Day and Night Imaging Applications. In Proceedings of the 2010 IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, 6–8 December 2010; pp. 344–347.

16. De Borniol, E.; Guellec, F.; Castelein, P.; Rouvié, A.; Robo, J.; Reverchon, J. High-Performance 640×512 Pixel Hybrid InGaAs Image Sensor for Night Vision. In Proceedings of the Infrared Technology and Applications XXXVII, SPIE, Baltimore, MD, USA, 23 April 2012; Volume 8353. [[CrossRef](#)]
17. Carnes, J.E.; Kosonocky, W.F. Noise Source in Charge-coupled Devices. *RCA Rev.* **1972**, *33*, 327–343.
18. Barbe, D.F. Imaging Devices Using the Charge-Coupled Concept. *IEEE Proc.* **1975**, *63*, 38–67. [[CrossRef](#)]
19. Pain, B.; Yang, G.; Ortiz, M.; Wrigley, C.; Hancock, B.; Cunningham, T.J. Analysis and Enhancement of Low-Light-Level Performance of Photodiode Type CMOS Active Pixel Imagers Operated with Subthreshold Reset. In Proceedings of the IEEE Workshop on Charge Coupled Devices and Advanced Image Sensors, Karuizawa, Japan, 9–11 June 1999; pp. 140–143.
20. Tian, H.; Fowler, B.; el Gamal, A. Analysis of Temporal Noise in CMOS Photodiode Active Pixel Sensor. *IEEE J. Solid State Circuits* **2001**, *36*, 92–101. [[CrossRef](#)]
21. Takayanagi, I.; Fukunaga, Y.; Yoshida, T.; Nakamura, J. A Four-Transistor Capacitive Feedback Reset Active Pixel and Its Reset Noise Reduction Capability. In Proceedings of the IEEE Workshop on Charge Coupled Devices and Advanced Image Sensors, Lake Tahoe, CA, USA, 7–9 June 2001.
22. Pain, B.; Yang, G.; Cunningham, T.J.; Wrigley, C.; Hancock, B. An Enhanced-Performance CMOS Imager with a Flushed-Reset Photodiode Pixel. *IEEE Trans. Electron Devices* **2003**, *50*, 48–56. [[CrossRef](#)]
23. Fowler, B.; Godfrey, M.D.; Balicki, J.; Canfield, J. Low Noise Readout Using Active Reset for CMOS APS. *SPIE Proc.* **2000**, *3965*, 126–135.
24. Pain, B.; Cunningham, T.J.; Hancock, B.; Yang, G.; Seshadri, S.; Ortiz, M. Reset Noise Suppression in Two-Dimensional CMOS Photodiode Pixels through Column-Based Feedback-Reset. In Proceedings of the IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, 8–11 December 2002; pp. 809–812.
25. Fowler, B.; Godfrey, M.D.; Mims, S. Analysis of Reset Noise Suppression via Stochastic Differential Equations. In Proceedings of the 2005 IEEE Workshop on Charge Coupled Devices and Advanced Image Sensors, Karuizawa, Japan, 9–11 June 2005; pp. 19–22.
26. Kozlowski, L.J.; Rossi, G.; Blanquart, L.; Marchesini, R.; Huang, Y.; Chow, G.; Richardson, J.; Standley, D. Pixel Noise Suppression via SoC Management of Tapered Reset in a 1920×1080 CMOS Image Sensor. *IEEE J. Solid State Circuits* **2005**, *40*, 2766–2776. [[CrossRef](#)]
27. Yang, J.; Fife, K.G.; Brooks, L.; Sodini, C.G.; Mudunuru, A.B.P.; Lee, H.-S. A 3Mpixel Low-Noise Flexible Architecture CMOS Image Sensor. In Proceedings of the 2006 IEEE International Solid State Circuits Conference—Digest of Technical Papers, San Francisco, CA, USA, 6–9 February 2006.
28. Lotto, C. Energy-Sensitive Single-photon X-ray and Particle Imaging. In *Single-Photon Imaging*; Seitz, P., Theuwissen, A.J.P., Eds.; Springer-Verlag: Berlin, Germany; Heidelberg, Germany, 2011; pp. 255–271.
29. Merrill, R.B. kTC Noise Cancellation Pixel. In Proceedings of the 2001 IEEE Workshop on Charge-Coupled Devices and Advanced Image Sensors, Lake Tahoe, CA, USA, 7–9 June 2001; pp. 118–121.
30. Thornber, K.K. Theory of Noise in Charge-Transfer Devices. *Bell Syst. Tech. J.* **1974**, *53*, 1211–1262. [[CrossRef](#)]
31. Emmons, S.P.; Buss, D.D. Noise Measurements on the Floating Diffusion Input for Charge-Coupled Devices. *J. Appl. Phys.* **1974**, *45*, 5303–5306. [[CrossRef](#)]
32. Shioyama, Y.; Hatori, F.; Matsunaga, Y. Analysis of Random Noise in STACK-CCD. *ITE Techn. Rep.* **1993**, *17*, 1–6. (In Japanese)
33. Nakamura, N.; Shioyama, Y.; Ohsawa, S.; Sugiki, T.; Matsunaga, Y. Random Noise Generation Mechanism for a CCD Imager with an Incomplete Transfer-Type Storage Diode. *IEEE Trans. Electron Devices* **1996**, *43*, 1883–1889. [[CrossRef](#)]
34. Teranishi, N.; Mutoh, N. Partition Noise in CCD Signal Detection. *IEEE Trans. Electron Devices* **1986**, *ED-33*, 1696–1701. [[CrossRef](#)]
35. Teranishi, N. Required Conditions for Photon Counting Image Sensors. *IEEE Trans. Electron Devices* **2012**, *59*, 2199–2205. [[CrossRef](#)]
36. Charbon, E.; Fishburn, M.W. Monolithic Single-Photon Avalanche Diodes: SPADs. In *Single-Photon Imaging*; Seitz, P., Theuwissen, A.J.P., Eds.; Springer-Verlag: Berlin, Germany; Heidelberg, Germany, 2011; pp. 123–157.
37. Dutton, N.A.W.; Gyongy, I.; Parmesan, L.; Gneccchi, S.; Calder, N.; Rae, B.R.; Pellegrini, S.; Grant, L.A.; Henderson, R.K. A SPAD-Based QVGA Image Sensor for Single-Photon Counting and Quanta Imaging. *IEEE Trans. Electron Devices* **2016**, *63*, 189–196. [[CrossRef](#)]

38. Kusuvara, F.; Wakashima, S.; Nasuno, S.; Kuroda, R.; Sugawa, S. Analysis and Reduction of Floating Diffusion Capacitance Components of CMOS Image Sensor for Photon-Countable Sensitivity. In Proceedings of the International Image Sensor Workshop, Vaals, the Netherlands, 8–11 June 2015; pp. 120–123.
39. Boukhayma, A.; Peizerat, A.; Enz, C. A 0.4 e⁻ rms Temporal Readout Noise, 7.5 μm Pitch and a 66% Fill Factor Pixel for Low Light CMOS Image Sensors. In Proceedings of the International Image Sensor Workshop, Vaals, the Netherlands, 8–11 June 2015; pp. 365–368.
40. Yao, Q.; Dierickx, B.; Dupont, B. CMOS Image Sensor Reaching 0.34 e⁻ RMS Read Noise by Inversion-Accumulation Cycling. In Proceedings of the International Image Sensor Workshop, Vaals, the Netherlands, 8–11 June 2015; pp. 369–372.
41. Ma, J.; Fossum, E.R. Quanta Image Sensor Jot with Sub 0.3 e⁻ r.m.s. Read Noise and Photon Counting Capability. *IEEE Electron Device Lett.* **2015**, *36*, 926–928. [[CrossRef](#)]
42. Seo, M.-W.; Kawahito, S.; Kagawa, K.; Yasutomi, K. A 0.27 e⁻ rms Read Noise 220-μV/e⁻ Conversion Gain Reset-Gate-Less CMOS Image Sensor with 0.11- CIS Process. *IEEE Electron Device Lett.* **2015**, *36*, 1344–1347.



© 2016 by the author; licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC-BY) license (<http://creativecommons.org/licenses/by/4.0/>).