

Article **A Frequency Signature RFID Chipless Tag for Wearable Applications**

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Received: 21 December 2018; Accepted: 22 January 2019; Published: 25 January 2019

Abstract: In this paper, a frequency-signature Radio-Frequency Identification (RFID) chipless tag for wearable applications is presented. The results achieved for a fully-textile solution guaranteeing a seamless integration in clothes are reported and discussed. The proposed tag consists of two planar monopole antennas and a 50 Ω microstrip line loaded with multiple resonators. In order to achieve a compact size, the resonators are slotted on the ground plane of the microstrip line. As for the antennas, the same geometry was exploited for both the TX and the RX tag antenna. In particular, it consists of a proximity fed planar monopole on a ground plane. The selected geometry guarantees easy integration with the multi-resonator structure. Numerical and experimental data referring to a 2-bit implementation are presented and discussed. For fabricating all the prototypes, a layer of pile was used as a substrate, while an adhesive non-woven conductive fabric was exploited for the fabrication of the conductive parts. Experimental tests demonstrate that although the performance of the final device strongly depends on the properties of the used materials and on the imperfections of the fabrication process, the proposed frequency-signature RFID chipless tag is suitable for wearable applications, such as anti-counterfeiting systems and laundry labels.

Keywords: chipless tag; fully-textile; RFID; wearable

1. Introduction

So far, the success of wearable electronics has been hampered by technological limits and, in particular, by the lack of materials and manufacturing techniques that would enable a seamless integration of the electronic parts in the garments. In fact, although several conductive fabrics are available on the market [\[1](#page-9-0)[–3\]](#page-9-1) and different low-cost fabrication techniques have been proposed in literature [\[3–](#page-9-1)[15\]](#page-10-0), the wearability of smart clothes and their robustness in operations such as washing, drying and ironing are still strongly limited by the use of chips and ICs requiring tin soldering [\[5,](#page-10-1)[6\]](#page-10-2). This problem is overcome by chipless devices, such as chipless Radio Frequency Identification (RFID) tags [\[16](#page-10-3)[–34\]](#page-11-0).

The use of chipless tags guarantees important practical advantages, such as seamless integration with the clothes, real-time operations, potentially infinite "service-life", low environmental impact, and low cost [\[16–](#page-10-3)[18\]](#page-10-4). Furthermore, chipless tags are able to work in harsh scenarios where the correct operation of conventional RFID technology could be compromised by the exposure of ICs to high temperatures or hazardous environments [\[16\]](#page-10-3). On the other hand, encoding data without the presence of ICs represents a major challenge. For this reason, industrial and academic efforts are mainly focused on the investigation of new approaches guaranteeing efficient encoding [\[16\]](#page-10-3).

More recently, the chipless RFID technology has also been exploited for the fabrication of sensors [\[16](#page-10-3)[,18,](#page-10-4)[23,](#page-10-5)[25–](#page-10-6)[28\]](#page-11-1). In this regard, numerous chipless sensors have been proposed in the literature, but for the majority of these devices, reliability and reproducibility are still critical issues [\[18\]](#page-10-4).

In terms of the approach adopted for encoding data, the following classification can be used for RFID chipless tags: spectral signature [\[19\]](#page-10-7), phase encoding [\[29\]](#page-11-2), polarization diversity [\[30\]](#page-11-3), and time domain signature [\[31\]](#page-11-4). However, aside from the adopted approach, the operating mechanism is the same: there is a reader that sends an interrogation signal to the transponders; the transponders reflect back a signal encapsulating the encoded data (see Figure [1\)](#page-2-0).

As for wearable applications, both simple chipless tags and sensors have been proposed in the literature [\[23,](#page-10-5)[24](#page-10-8)[,32](#page-11-5)[–34\]](#page-11-0). Among these, in [\[23\]](#page-10-5), the results achieved for a sewn chipless RFID sensor tag are presented. Numerical and experimental results reported in the paper demonstrate the correct operation of the chipless tag in the frequency range, 3–6 GHz. In more detail, the sewn stretchable sensor consists of a rectangular loop sewn on to a stretchable fabric. A conductive thread is used for fabricating the loop. The achieved results confirm that the proposed device is suitable to be used as a strain sensor.

In Reference [\[24\]](#page-10-8), an RFID chipless tag for people identification is presented. The proposed device consists of L-shaped scatterers able to depolarize the incident wave and to generate a cross-polarized signal. The capability of depolarizing the interrogation signal allows for a robust decoding even when the tag is placed in direct contact with the human body. However, numerical and experimental results demonstrate that the performance of the proposed tag is strongly affected by bending. In particular, a wrong ID decoding occurs if the tag is placed on a curved region of the human body.

In Reference [\[33\]](#page-11-6), the authors have proposed a fully textile frequency-signature chipless tag consisting of a microstrip line loaded with multiple resonators. Each resonator corresponds to a single bit. Numerical and experimental results achieved for a 3-bit prototype have demonstrated the suitability of the proposed device for RFID wearable applications requiring a low number of bits, such as anti-counterfeiting systems and laundry labels [\[23\]](#page-10-5). In this paper, new numerical and experimental results are reported and discussed. It is shown that the proposed device is a low-cost solution which allows a seamless integration in garments.

The paper is structured as follows: in Section [2,](#page-1-0) details on the geometry and numerical results achieved for both the single components (adjustable multi-stopband structure and monopole antennas) and the tag connected with antennas are given, experimental results are reported in Section [3,](#page-6-0) and finally, a brief discussion and some conclusions are reported in Section [4.](#page-9-2)

2. Geometry and Numerical Results

The proposed wearable frequency-signature chipless tag consists of a 50 Ω microstrip line loaded with compact resonators and two wideband antennas. In particular, the microstrip line acts as a stopband structure.

As shown in Figure [1,](#page-2-0) a generic RFID system based on a multi-resonator tag also comprises a reader equipped with two antennas. The operating principle is very simple: (1) the reader sends an interrogation signal to the tag and (2) the tag replies, sending a backscattered signal whose frequency spectrum contains the encoded data. Both the reader and the transponder antennas are cross-polarized in order to minimize possible interferences between the interrogation signal and the backscattered encoded signal [\[19\]](#page-10-7). In this paper, the attention is focused on the tag system.

In the following subsections, details on the geometry and numerical results achieved for both the single components (adjustable multi-stopband structure and wideband antennas) and the overall tag system will be given.

Figure 1. Schematic representation of a generic multi-resonator chipless tag Radio Frequency
Hartification (PEID) system [10] Identification (RFID) system [19]. Identification (RFID) system [\[19\]](#page-10-7). Identification (RFID) system [19].

2.1. Multi-Resonator Structure 2.1. Multi-Resonator Structure

2.1. Multi-Resonator Structure the ground plane of the microstrip line. The front view and the back view of a 2-bit multi-stopband structure are shown in Figure [2a](#page-2-1),b, respectively; the single resonator is illustrated in Figure 2c. The proposed multi-resonator structure is illustrated in Figure [2.](#page-2-1) The resonators are slotted on

structure; (b) back view of a 2-bit multi-stopband structure; (c) geometry of the single resonator. **Figure 2.** Geometry of the proposed multi-stopband structure: (**a**) front view of a 2-bit multi-stopband

The design of the multi-stopband structure was performed with the commercial full-wave The design of the multi-stopband structure was performed with the commercial full-wave
simulator CST Microwave Studio [35]. The design process consisted of two main steps: 1) first, the resonator operating at the lowest frequency was optimized; 2) second, the resonators operating at the resonator operating at the lowest frequency was optimized; 2) second, the resonators operating at
higher frequencies were obtained through a uniform scaling of the first resonator on the x–y plane. The adopted approach can be summarized through the following equation, which describes the relationship between the total area of the first resonator and the total area of the nth resonator:

$$
A_n = A_1 \cdot (SF_n)^2, \ n > 1 \tag{1}
$$

where A_1 is the area of the first resonator (on the x–y plane), A_n is the area of the nth-resonator (on the x-y plane), and SF_n is the nth scale factor. Each resonator introduces a transmission zero (i.e., a relative $\frac{1}{n}$ minimum of the transmission coefficient of the microstrip line), whose central frequency is controlled by the parameter ϵ by the parameter SF_n . Consequently, each resonator corresponds to a single bit and the presence of a minimum amplitude (and a phase variation) of the S_{21} parameter represents a logic 1; conversely, the absence is encoded as a "0" bit. To encode a logic 0, the layout of the corresponding resonator is modified as illustrated in Figure [3.](#page-3-0) where π_1 is the area of the nth scale factor factor. Each resonator internsity and π_2 the new conduction π_1 $\frac{1}{2}$ to constant (on the x-v plane) Λ is the area of the pth-resension (or where α is the first resonance of the first resonance α is the α is the area of the number of the nu x –y plane), and $\overline{S}F$ is the number of $\overline{S}F$. Comes we set the negative same seed to a simple hit and the numerous of $\overline{S}F$ relative minimum of the transmission coefficient of the ϵ measurements of the minimum of the microstrip line ϵ central frequency is the minimum of the ϵ measurements of the ϵ measurements of the ϵ measureme controlled by the parameter $\frac{d}{dx}$ is the parameter $\frac{d}{dx}$. Consequently, each resonator corresponds to a single bit and the sequence of the and the sequence of the sequence of the sequence is an except of the seque presence of a minimum amplitude (and a phase variation) of the S21 parameter representation $\frac{1}{2}$; and $\frac{1}{2}$ $\frac{1}{\sqrt{2}}$ for absence is encoded as a $\frac{1}{\sqrt{2}}$ bit. To encode a logic $\frac{1}{\sqrt{2}}$ the corresponding to the c

Figure 3. To encode a 0 bit, five short-circuits are used to modify the layout of the single resonator. **Figure 3.** To encode a 0 bit, five short-circuits are used to modify the layout of the single resonator.

 '0' bit

obviously, for a given bandwidth, higher scale factors allow a higher number of bits to be encoded; on the other hand, excessively high scale factors lead to a complex decoding. The frequency shift between two consecutive resonant peaks depends on the scale factor and,

A comparison between numerical results achieved for multi-resonator structures having a different number of bits is illustrated in Figure 4a. different number of bits is illustrated in Figure 4a.

Numerical results obtained for four different configurations of a 3-bit tag are reported in Figure 4b,c. These results demonstrate that the data encapsulated in the frequency spectrum [c](#page-4-0)an be decoded by analyzing both the amplitude (Figure [4a](#page-4-0),b) and the phase (Figure [4c](#page-4-0)) of the S_{21} parameter.

Figure 4. *Cont*.

Figure 4. Numerical results achieved for different multi-resonator structures: (**a**) amplitude of the S_{21} achieved for multi-stopband structures with 3, 4, and 5 bits; amplitude (b) and phase (c) of the S_{21} calculated from full-wave simulations for different configurations of a 3-bit structure. calculated from full-wave simulations for different configurations of a 3-bit structure.

As it can be seen from Figure 4b,c, f[or](#page-4-0) the different configurations, the position of the minima of the $|S_{21}|$ and of the phase variations of the S_{21} are both stable in frequency. This means that when the geometry of one or more resonators is modified to achieve a logic 0, the resonances corresponding to the other resonators are not significantly influenced.

\mathbf{A} is the different from Figure 4b,c, for the different configurations, the position of the minima of t $\frac{1}{2}$ is the phase variations of the S21 are both stable in frequency. The stable in frequency. *2.2. TX and RX tag Antennas*

As shown in Figure 1, a multi-resonator chipless tag also includes TX and RX antennas. Hence, to complete the geometry of the proposed device, two planar monopole antennas were also designed. for the first time in [36]. Each antenna is a proximity-fed planar monopole on a ground plane, optimized to operate in the frequency range 2–4 GHz. This solution guarantees a compact size and simplifies the to complete the geometry of the proposed device, two planar monopole antennas were also integration with the multi-stopband structure. As required by any multi-resonator chipless tag RFID system (see Figure [1\)](#page-2-0), the two antennas are cross-polarized; in particular, the RX antenna is y-polarized in Figure 5, and the TX antennas are cross-polarized (see Figure [5a](#page-4-1)), while the TX antenna is x-polarized (see Figure [5b](#page-4-1)). The final dimensions of the two ground plane, optimized to optimize in the frequency range 2–4 GHz. This solution guarantees and the frequency The geometry adopted for the TX and the RX antennas, which is illustrated in Figure [5,](#page-4-1) was proposed antennas are summarized in Table [1.](#page-5-0)

Numerical results achieved for the reflection coefficient are reported in Figure [6a](#page-5-1). As for the RX antenna, values of the $|S_{11}|$ lower than –10 dB were achieved in the frequency range 1.97–3.93 GHz, corresponding to a relative bandwidth of about 66.4 %. Meanwhile, the TX antenna exhibits an $|S_{11}|$ lower than –10 dB in the range 1.86–4.20 GHz, corresponding to a relative bandwidth of about 80.7 %.

Figure 5. Geometries and dimensions of the wideband antennas proposed as RX tag antenna (**a**) and **Figure 5.** Geometries and dimensions of the wideband antennas proposed as RX tag antenna (**a**) and TX tag antenna (**b**). TX tag antenna (**b**).

Table 1. Dimensions of the antennas illustrated in Figure [5.](#page-4-1) **Table 1.** Dimensions of the antennas illustrated in Figure 5.

 $|S_{11}|$ of the two monopoles; (**b**) 3D radiation patterns of the RX tag antenna calculated at 2.1 GHz and |S11| of the two monopoles; (**b**) 3D radiation patterns of the RX tag antenna calculated at 2.1 GHz and 2.4 GHz (b); (**c**) 3D radiation patterns of the TX antenna calculated at 2.1 GHz and 2.4 GHz. **Figure 6.** Numerical results achieved for the RX and the TX tag antennas: (**a**) comparison between the

As for the radiation properties, values of directivity higher than 2 dBi and a dipole-like radiation pattern were achieved in the –10 dB relative bandwidth. 3D radiation patterns calculated at 2.1 GHz and 2.4 GHz are illustrated in Figure [6b](#page-5-1),c. Finally, all of the achieved numerical results are summarized in Table [2.](#page-5-2)

Table 2. Numerical results achieved for the RX and the TX tag antennas.

	$BW_{-10\text{ dB}}$ (GHz)		$ S_{11} $ (dB)		Directivity (dBi)			
		@ 2 GHz	@ 3 GHz	@ 4 GHz	@ 2 GHz	@ 3 GHz	@ 4 GHz	
RX	1.97-3.93	-10.1	-10.9	-9.14	3.40	3.99	3.44	
TХ	1.86–4.2	-13.3	-11.03	-17.9	2.75	4.06	4.17	

2.3. Overall System Tag

Full-wave simulations were also performed in order to evaluate the performance of the overall tag system (i.e., the multi-stopband structure integrated with the TX and the RX tag antennas) in the case of a 2-bit configuration. The whole device is illustrated in Figure [7a](#page-6-1). A plane wave was used as a source.

According to the polarization of the RX tag antenna, the E-field was y-polarized. The results achieved for the Radar Cross Section (RCS) are illustrated in Figure [7b](#page-6-1). In this case, a relative maximum of the notice RCS corresponds to a logic 1. As can be noticed, good results were obtained for the second bit, i.e., the one corresponding to the resonator having a higher frequency of resonance. In fact, for the second bit, it can be seen that the frequency at which the RCS maximum is positioned is stable (the same frequency was obtained for the configurations 11 and 01), and a difference higher than 10 dB can be observed $\frac{1}{2}$ between the RCS amplitudes corresponding to the two logical states. As per the first bit, it can be seen that the first bit, it can be seen that the first relative maximum of the RCS is positioned at a different frequency for the configuration 11 and 10. However, also in this case, the two logical states can be easily recognized; in fact, a difference of about 10 dB was obtained for the amplitude of the RCS corresponding to the two states. corresponding to the two states. source. According to the polarization of the RX tag antenna, the E-field was y-polarized. The results According to the Polarization of the RA tag america, the E-Herd was y-polarized. The results active

Figure 7. Numerical results achieved for a 2-bit overall system tag: (a) setup used during full-wave simulations; (**b**) numerical results achieved for the RCS of the device reported in Figure 7a. simulations; (**b**) numerical results achieved for the RCS of the device reported in Figure [7a](#page-6-1).

3. Experimental Results 3. Experimental Results

In order to verify the numerical data presented in the previous Section, some prototypes were In order to verify the numerical data presented in the previous Section, some prototypes were realized and characterized. In more detail, three different prototypes were fabricated: 1) a prototype of the stop-band microstrip line in a 2-bit configuration; 2) a prototype of the TX and the RX antennas; 3) a prototype of the stopband microstrip line integrated with the RX antenna. The achieved results are described in the following. All the results refer to an implementation on a layer of pile with 0.5 mm thickness, and by using an adhesive non-woven conductive fabric for all the conductive parts.

3.1. Stop-Band Microstrip Line

3.1. Stop-Band Microst[rip](#page-11-6) Line In Reference [33] the authors presented the results referring to a stopband microstrip line in a b on comiguration (i.e., the comiguration naving three resonances sioned on the ground piane)
The achieved experimental results were slightly different to the numerical ones. From the foregoing 3-bit configuration (i.e., the configuration having the configuration of the ground plane in the ground plane of the ground plane in the ground plane of the ground plane of the ground plane of the ground plane of the groun analysis, it was concluded that the mismatch between numerical and experimental results obtained for
. From the foregoing and the foregoing of the 3-bit configuration was likely due to some imperfections of the fabrication process. In particular, two layers, 0.5 mm thick. By analyzing the prototype, it was verified that the hand-stitching led to a the was verified that the hand-stitching led to a a 3-bit configuration (i.e., the configuration having three resonators slotted on the ground plane). the prototype presented in Reference [\[33\]](#page-11-6) was fabricated on a 1 mm layer of pile achieved by sewing non-uniform thickness of the pile. Additionally, further imperfections were introduced by the cutting plotter used for shaping the non-woven conductive fabric; in fact, the adopted plotter is more suitable for processing small areas.

Taking into account these considerations, to avoid the hand-sticking and to have a smaller area to be processed, the prototype presented in this paper is a stopband microstrip line in a 2-bit configuration fabricated on a single layer of pile (thickness 0.5 mm, dielectric permittivity equal to 1.18). As in Reference [\[33\]](#page-11-6), an adhesive non-woven conductive fabric produced by Saint Gobain was used for all the conductive parts and a cutting plotter was used for the fabrication [\[9,](#page-10-9)[10\]](#page-10-10).

The final geometrical parameters of the prototype are reported in Table [3.](#page-7-0) The scale factor (see Equation (1)) was optimized for achieving a resonance shift of about 300 MHz. In particular, the two resonators were designed to introduce two stopbands centered at 2.1 GHz and 2.4 GHz. used for all the conductive parts and a cutting parts and a cutting plotter was used for the fabrication Γ The final geometrical parameters of the prototype are reported in Table 3. The scale factor

A picture of the 2-bit prototype is reported in Figure [8a](#page-7-1). Experimental tests were performed A picture of the 2-bit prototype is reported in Figure 8a. Experimental tests were performed through a VNA R&S ZVA50. The results for both the amplitude and the phase of the S_{21} parameter are illustrated in Figure [8b](#page-7-1),c, respectively. A good agreement between the numerical data and experimental results was achieved. In particular, the first minimum of the $|S_{21}|$ is centered at 2.08 GHz and the second one at 2.40 GHz. Hence, with respect to the numerical data, there is a shift of about 20 MHz for the resonance corresponding to the first resonator; while the resonance of the second resonator is perfectly tuned. These results confirm that, as supposed by the authors, the disagreement between numerical data and experimental results achieved in Reference [\[33\]](#page-11-6) was mainly due to some imperfections in the manufacturing process such as irregular thickness of the pile and cutting plotter, unsuitable to process large area. pile and cutting plotter, unsuitable to process large area.

Figure 8. Experimental results achieved for a 2-bit prototype: (a) 2-bit prototype under test; (**b**) $|S_{21}|$ corresponding to all possible configurations; (**c**) phase of the S_{21} corresponding to all the possible configurations. possible configurations.

Table 3. Geometrical parameters of the 2-bit prototype shown in Figure [8a](#page-7-1).

2-bits Prototype												
W		W_{feed}	W_{res}	L_{res}	t_{res}	a_{res}	gres	S_{F2}				
40	60	3.7	19.1	18.3								

All parameters are in millimeters.

3.2. TX and RX Tag Antennas 3.2. TX and RX Tag Antennas

In order to verify the numerical data of the antennas presented in the previous section, a prototype In order to verify the numerical data of the antennas presented in the previous section, a of both the TX and the RX antenna using the dimensions report[ed](#page-5-0) in Table 1 was fabricated (see Figure [9a](#page-8-0),b). The experimental results achieved for the reflection coefficients are reported in Figure [9c](#page-8-0). In both cases, a good agreement between the numerical and experimental results was obtained. From Figure [9c](#page-8-0), it can be seen that both of the antennas exhibit values of the reflection coefficient lower than -10 dB in the frequency range of interest (i.e., 2–3 GHz).

Figure 9. Experimental results achieved for the TX tag antenna and the RX tag antenna: (a) front view and back view of the RX antenna prototype; (b) front view and back view of the TX antenna prototype; (**c**) measured reflection coefficients.

3.3. 2-Bit Tag 3.3. 2-Bit Tag

Finally, a prototype of the stopband microstrip line integrated with the RX antenna was Finally, a prototype of the stopband microstrip line integrated with the RX antenna was realized (Figure [10a](#page-9-3),b). The overall dimensions of the prototype are 85.9 mm X 110 mm. The expected performance was verified by adopting the setup illustrated in Figure [10c](#page-9-3).

In more detail, two-port measurements were performed with the antenna illustrated in In more detail, two-port measurements were performed with the antenna illustrated in Figure [9b](#page-8-0) connected to Port 1 and the microstrip line integrated with the RX antenna connected to Port 2.

Figure [10d](#page-9-3). As can be noticed, the results are very promising; in fact, even though the positions of the relative minima of the transmission coefficient are slightly different for different configurations (see, for instance, the position of the first minimum for the configurations 11 and 10), the presence of a logic d or a logic 1 can be easily recognized. The transmission coefficient measured for all of the configurations of interest is reported in

It would probably be possible to further improve the results by optimizing the geometry of the antennas and the transition between the stopband structure and the antenna feed-line. However, the results shown in Figure [10d](#page-9-3) fully demonstrate the feasibility of the proposed design approach and of its fully textile implementation.

microstrip line integrated with the RX antenna; (c) experimental setup adopted for measurements; (d) measured transmission coefficients for the four configurations of interest. **Figure 10.** Experimental results achieved for the 2-bit tag. (**a**) Front and (**b**) back view of the stopband

4. Conclusions

4. Conclusions In this paper, a fully textile frequency-signature RFID chipless tag was presented. The proposed tag encodes the desired information by exploiting a microstrip line loaded with multiple resonators slotted on the ground plane. Each resonator introduces at its frequency of resonance a relative minimum on the transmission coefficient of the microstrip line. Accordingly, by designing the resonators to have different frequencies of resonance, it is possible to encapsulate the binary code in the frequency signature of the microstrip line.

As for the antennas, the same geometry of a planar broadband monopole was applied to both the transmitting and the receiving tag antenna.

Experimental data demonstrate the feasibility and good perspectives of the proposed design approach and of its fully textile implementation.

Author Contributions: Conceptualization, L.C., G.M. and L.T.; Methodology, L.C, G.M. and L.T..; Validation, L.C.; Formal Analysis, L.C., G.M and L.T.; Investigation, L.C., G.M and L.T.; Data Curation, L.C.; Writing-Original Draft Preparation, L.C.; Writing-Review & Editing, G.M. and L.T.; Visualization, L.C.; Supervision, G.M. and L.T.

Funding: This research received no external funding.

Conflicts of Interest: The authors declare no conflict of interest.

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