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A New Adaptive Approach to Control Circulating and Output Current of Modular Multilevel Converter

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Abstract: This paper addresses the output current and circulating current control of the modular multi-level converter (MMC). The challenging task of MMCs is the control of output current and circulating current. Existing control structures for output and circulating current achieve control objectives with comparatively complex controllers and the designed parameters for the controller is also difficult. In this paper, an adaptive proportional integral (API) controller is designed to control the output current and the circulating current. The output current is regulated in $\alpha\beta$ axes while the circulating current is regulated in the *abc* stationary frame to enhance MMC performance. The output and circulating current control results using an API controller are compared with the conventional proportional resonant (PR) controller in terms of transient response, stability, optimal performance, and reference tracking for results verification. The API control architecture significantly improve transient response, stability, and have excellent reference tracking capability. Moreover, it controls output current and converges the circulating current to a desired value. The control structure is designed for a three-phase MMC system, simulated and analyzed in MATLAB-Simulink.

Keywords: circulating current; fuzzy; proportional integral; proportional resonant; MMC

1. Introduction

The MMC is becoming an increasingly attractive, competitive, and highly applied technology for medium and high voltage power applications due to its advantages, such as low harmonics, modular structure, simple scaling, loss reduction in switches, and satisfactory fault management [1–3]. Currently, MMCs have been expanded to many applications [4] such as motor drivers [5], solar/wind power systems [6–8], energy transmission and distribution systems [9], and static synchronous compensators (STATCOM) [10]. Recently published literature related to MMC targeted current control, voltage control, loss analysis and modulation, dynamic and steady-state models, modeling, and simulation methods [11–16].

The first structure of the MMC was presented in [17]. Currently, many configurations and control designed approaches have been introduced by many researchers for the control objectives, such as balancing and averaging of SM capacitor voltage, circulating current ripples injection or suppression, and output voltage regulation [18–22]. Figure 1 shows the configuration of a three-phase six arm MMC.





Every arm of the MMC is equipped with a series connected sub module (SM) configuration. The SM, consists of two switches connected with a capacitor, is also depicted in Figure 1.

Figure 1. General MMC 3 phase structure.

For the optimal performance of MMC, the control system plays a vital role. The output current is controlled to transfer the power to AC grid while the circulating current does not affect the output of the converter. However, uncontrolled circulating current results in higher losses. The circulating current problem in each arm of MMC arises due to potential differences in every SM arm and DC link. Reference [23] studied the effects of arm inductance and SM capacitance on the magnitude of circulating current. Increases in arm inductance decrease the circulating current, but the circulating currents are not eliminated. The negative sequence component of circulating current rotates at twice line frequency. A control scheme is proposed in [24] by translating the a-b-c sequence components into *dq* rotational frame. But this control structure has a limitation because the circulating current also have positive and zero sequence components along with negative sequence components.

The most common control approach was introduced by Hagiwara and Akagi in [25]. The control method is used to regulate the output current and internal dynamics, i.e., circulating current and internal energy. However, the ripples in circulating current could not be eliminated effectively by this control method. Over the past few years, many control schemes for circulating current were proposed such as dq or rotating frame control for the suppression of 2nd order harmonics [26–28], resonant controllers to eliminate harmonics in unbalanced circumstances [29] and to eliminate integral harmonics in circulating currents plug in repetitive controllers are used [30,31]. But the above-mentioned control method is complicated in structure and the controllers are difficult to tune for system stability and performance.

Some researchers have proposed control methods for controlling both circulating current and output current. A PR control strategy is used to control an output current to get the desired output power. The output current is 1st harmonic frequency sinusoid [32]. Since after all PR control strategy gives an infinite gain at the resonant frequency, any small frequency changes may point to an enormous tracking error from the reference signal. A PI controller in [24] is used to control circulating current

and a PR controller is also proposed to suppress the circulating current in [33]. However, the design and tuning parameters of the controllers mentioned above may be complicated as obtaining excellent control operation and stability may be a difficult task.

This paper covers two main issues of the MMC—circulating current control and output current control during normal grid condition. An adaptive PI controller has been used to control both the circulating and output current control in *dq* and *abc* stationary frames. Compared to other control methods, the API control scheme has many advantages such as good tracking, transient response, optimal stability performance, wide range operation capability, and adequate control for the MMC during normal grid condition.

The sections of this paper are organized as Section 2, which describes the operation and modeling of the MMC. Section 3 deals with the control structure design for output and circulating current. The results are discussed in Section 4. While the conclusion is detailed in Section 5.

2. Operation and System Mathematical Modeling

Every SM of an MMC has a capacitor and half bridge connected switches, which gives output voltage according to switch ON or OFF position as depicted in Figure 2.

2.1. Switching Operation of the MMC

The insertion or bypassing of *N*-SM will give, (M + 1) levels of output waveform at every phase of the MMC. Where *N* represents No of SM and *M* represents the levels of output wave form. The switching operation of a single phase MMC is illustrated in Figure 2, using a single line schematic diagram.

A DC voltage " U_{dc} " is applied at the input of the MMC. The switches are operating in reciprocal fashion in each SM relying on the position ON or OFF. The solid line shows the capacitor is inserted and the dim line shows a capacitor is bypassed in every SM. The insertion and bypassing of the capacitor will define different levels of voltage at its terminal shown in Figure 2. The capacitor state that is inserted or bypassed are summarized in Table 1.

Scenario	Capacitors States in the Upper Arm	Capacitors States in the Lower Arm	Output Voltage
1	All capacitors are inserted	All capacitor bypassed	$-\frac{U_{dc}}{2}$
2	One capacitor bypassed and three capacitors inserted	One capacitor inserted and three capacitors bypassed	$-\frac{U_{dc}}{4}$
3	Two capacitors bypassed and two capacitors inserted	Two capacitors inserted and two capacitors bypassed	0
4	Three capacitors bypassed and one capacitor inserted	Three capacitors inserted and one capacitor bypassed	$rac{U_{dc}}{4}$
5	Four capacitors bypassed and zero capacitors inserted	Four capacitors inserted and zero capacitors bypassed	$\frac{U_{dc}}{2}$

2.2. System Mathematical Modeling

The mathematical modeling of the MMC is derived from a single-phase equivalent circuit as shown in Figure 3.



Figure 2. Switching operation of the single phase MMC.

By applying the Kirchhoff loop law, the relations for the arm voltages, output current, and circulating current of the converter are obtained as follow [34]. Where $j \in \{a, b, c\}$

$$-\frac{U_{dc}}{2} + u_{m,j} + R_{arm}i_{m,j} + L_{arm}\frac{di_{m,j}}{dt} + v_{u,j} = 0$$
(1)

$$\frac{U_{dc}}{2} - u_{n,j} - L_{arm} \frac{di_{n,j}}{dt} - R_{arm} i_{n,j} + v_{u,j} = 0$$
⁽²⁾

where L_{arm} and R_{arm} denote arm inductance and arm resistance respectively. U_{dc} represent the entire DC bus terminal voltage. $v_{u,j}$ represent output voltage of converter at point Z. $u_{m,j}$ and $u_{n,j}$ represents submodule arm voltages. The upper arm is represented by subscript *m* while a lower arm is represented by subscript *n*. Different currents of the MMC are given as

$$i_{m,j} = i_{diff,j} + \frac{i_{u,j}}{2} \tag{3}$$

$$i_{n,j} = i_{diff,j} - \frac{i_{u,j}}{2} \tag{4}$$

$$i_{u,j} = i_{m,j} - i_{n,j}$$
 (5)

$$i_{diff,j} = \frac{1}{2} (i_{m,j} + i_{n,j})$$
 (6)

where $i_{m,j}$ and $i_{n,j}$ represents the upper arm current and lower arm current. $i_{u,j}$ represent the output current. $i_{diff,j}$ represents an internal circulating current. Adding and subtracting (1) and (2) we obtain the following relation.

$$v_{u,j} = \underbrace{\left(\frac{u_{n,j} - u_{m,j}}{2}\right)}_{e_{v,j}} - \frac{R_{arm}}{2}i_{u,j} - \frac{L_{arm}}{2}\frac{di_{u,j}}{dt}$$
(7)

$$v_{diff,j} = \frac{U_{dc}}{2} - R_{arm}i_{diff,j} - \frac{L_{arm}di_{diff,j}}{dt}$$
(8)

The $e_{v,j}$ is internal voltage in (7), while $v_{diff,j}$ in (8) is the internal unbalanced voltage of an MMC, and the internal $i_{diff,j}$ over phase $j \in \{a, b, c\}$ can be express as

$$i_{diff,j} = \frac{i_{m,j} + i_{n,j}}{2} = \frac{I_{dc}}{3} + i_{cir,j}$$
(9)

where I_{dc} is the dc link current and supposed to be distributed equally in all phases. As a result, the circulating current described in (6) contains 1/3 of the DC link current $\frac{I_{dc}}{3}$ and $i_{cir,j}$ is the AC component of circulating current. The dynamics of an MMC are characterized in Equations (7) and (8). By evaluating Equation (7), the output current $i_{u,j}$ can be controlled by $e_{v,j}$. Similarly, by evaluating Equation (8), the internal circulating current $i_{diff,j}$ can be controlled by $v_{diff,j}$. Furthermore, the capacitor SM internal dynamics with regards to arm voltages and currents are expressed as

$$\frac{C}{N}\frac{dv_{cm,j}^{L}}{dt} = n_{m,j}^{i}i_{m,j}$$
(10)

$$\frac{C}{N}\frac{dv_{cn,j}^{\Sigma}}{dt} = n_{n,j}^{i}i_{n,j}$$
(11)



Figure 3. Single phase equivalent circuit.

The capacitance of an SM capacitor is expressed by *C*, the number of SM in each half arm is represented by *N*, while capacitor sum voltages in upper and lower arms are represented by $v_{cm,j}^{\Sigma}$ and $v_{cn,j}^{\Sigma}$. Insertion indices of upper and lower arms are represented by $n_{m,j}^{i}$ and $n_{n,j}^{i}$. Insertion indices $n_{m,j}^{i}$

and $n_{n,j}^i$ equal to one means the SM capacitor is inserted while insertion indices $n_{m,j}^i$ and $n_{n,j}^i$ equal to zero means the SM capacitor is bypassed in corresponding arms. As stated by Equations (8) and (9), the reference calculated for the arm voltages can be expressed as

$$u_{m,j_ref} = \frac{U_{dc}}{2} - e_{v,j} - v_{diff,j} + R_{arm}i_{diff,j} + \frac{L_{arm}di_{diff,j}}{dt}$$
(12)

$$u_{n,j_ref} = \frac{U_{dc}}{2} + e_{v,j} - v_{diff,j} + R_{arm}i_{diff,j} + \frac{L_{arm}di_{diff,j}}{dt}$$
(13)

In (13) the $e_{v,j}$ is calculated from the internal loop current controllers [35], while $v_{diff,j}$ is calculated from the circulating current eliminating controller, which can be used to minimize the three-phase circulating current in an MMC. It can be concluded from Equations (1), (2), (12), and (13) that $i_{u,j}$ and $i_{diff,j}$ could be controlled by manipulating $e_{v,j}$ and $v_{diff,j}$, respectively.

3. Control Scheme of the MMC

3.1. Adaptive PI Controller Design

The adaptive PI is a hybrid controller in which fuzzy logic (FL) is used to update the gains of PI for a wide range of operation. The FL makes the processing operation of a PI controller easy in a non-linear system where the system is ill-defined and mathematically poorly designed. The FL pre-processes the controller input (I/P) signal and PI post-process error signal to get the desired output response from the plant.

3.1.1. Fuzzy Controller Architecture

In the real world, problems related to a control system are largely complex, particularly when implementation problems are considered. The promising aspect of the fuzzy logic controller (FLC) is that they incorporate expert knowledge instead of entirely depending upon the exact mathematical model. Moreover, heuristics and intuition knowledge are also incorporated into the control system. Thus, FLC is suitable in applications, where models are ill-defined, not reliable enough, and complex. Essentially, the FLC is an artificial decision maker who works in a closed-loop fashion in real-time as shown in Figure 4. It collects plant O/P information y(t), matches it with the reference I/P x(t), and decides what the plant I/P v(t) would be to assure performance objectives. The FLC is mainly composed of four main phases: rule-base, inference mechanism, fuzzification, and de-fuzzification.

- 1. The "rule-base" which contains the information of controlling output variables are created in FLC, in the shape of IF-THEN rules, with condition and conclusion.
- 2. The rules are evaluated in the inference mechanism according to the error. During inference mechanism, it is concluded which control rules are appropriate at the current situation. Moreover, the choice of I/P to the plant are also enabled in this phase.
- 3. During the fuzzification, a "crisp" (which are real numbers, not fuzzy sets) or actual time information are collected and reshaped to a fuzzy set using fuzzy expressive terms, expressive variables, and membership functions. Moreover, I/P is modified and interpreted which are then compared with the rules defined in the rule-base.
- 4. The defuzzification transforms the fuzzy outcomes obtained by inference mechanism to real-time or crisp output utilizing membership functions. There are several available methods for defuzzification, i.e., mean of maximum, center of minimum, and center of gravity [36,37].



Figure 4. Fuzzy controller architecture to controlled plant.

3.1.2. Fuzzy PI Controller

The PI controller has tuning gains parameter k_p and k_i , respectively. The desired performance of PI control structure is improved by updating the gains, according to error u(t). The adaptability is attained by employing fuzzy rules, as given below and graphically illustrated in Figure 5.

- If error |u(t)| is zero, then proportional gain k_p is large and integral gain k_i is small
- If error |u(t)| is small, then proportional gain k_p is large and integral gain k_i is zero.
- If error |u(t)| is large, then proportional gain k_p is large and integral gain k_i is large.

The "crisp inputs" in the form circulating current and output current are fuzzified and shaped to a fuzzy set using membership function. The fuzzy set is evaluated in the rules-base with condition and conclusion, and concludes which rules are appropriate at the current time. The defuzzification process involves the conversion of the fuzzy outcomes to circulating and output voltages utilizing the membership function. The fuzzy rule selected from the set is defuzzified by using the center of gravity method. While the Gaussian membership function (GMF) is used for fuzzification, the GMF uses two variables, variance σ_i or standard deviation and center d_i as:

$$\mu(x) = \exp\left(-0.5\left(\frac{x_i - d_i}{\sigma_i}\right)^2\right) \tag{14}$$



Figure 5. Output-input membership functions.

The defuzzified output of fuzzy is fed as a gain (K_p , K_i) to PI. Thus, the PI controller for controlling the $i_{u,i}$ is mathematically defined as:

$$e_{v\alpha\beta}^{*}(PI) = K_{p}u(t) + K_{i}\int u(t)dt$$
(15)

The PI controller for controlling the $i_{diff,i}$ is mathematically defined as:

$$v_{diff,j}^*(PI) = K_p u(t) + K_i \int u(t) dt$$
(16)

Whereas u(t) is controller input, $e_{v\alpha\beta}^*$ and $v_{diff,j}^*$, controller outputs, k_p and k_i are controller gains respectively. However, the gain's parameters of a conventional PI controller are constant in (15) and (16), which needs adaptation according to system load disturbance, parameter uncertainties, electrical faults, and load variations. To achieve adaptation, the designed controller use u(t) signal as an input for self-tuning of gain parameters, i.e., k_p and k_i , respectively. Fuzzy IF-THEN rules are made as presented in Table 2. Based on these rules, a decision is performed by the inference engine. The defuzzified output is obtained by the center of gravity method for U_1 and U_2 .

Table 2. IF-THEN rules employed for gain adaptation.

Input Membership Functions		IF-TH	EN Rules	Output Membership Functions		
S.No	S.No Linguistic Ra Terms Ra		If Input $ u(t) $	Then Output (k_p, k_i)	Linguistic Terms	Range
(1)	Zero	[0, 0.2]	Zero	Zero. Large	Zero	[0, 0.2]
(2)	Small	[0.3, 0.7]	Small	Large. Small	Small	[0.3, 0.7]
(3)	Large	[0.8, 1.0]	Large	Large. Large	Large	[0.8 <i>,</i> 1.0]

During fuzzification, the error signal, u(t), information is collected and converted/fuzzified to fuzzy set using fuzzy linguistic terms and membership functions and compared with the IF-THEN rules defined in the rule-base. The rules are analyzed in the inference mechanism that decides the firing of the dominant rule. The fuzzy outcomes in the shape of U_1 and U_2 are obtained during defuzzification using the center of gravity method and update the gain parameters K_1 and K_2 , respectively. The rules employed in rule-base can be graphically illustrated in Figure 6a–c and shows the various scenarios in which U_1 and U_2 are actually computed by the fuzzy controller based on the absolute error. The line indices which corresponds to inputs can be moved to adjust gains k_p and k_i , as shown in Figure 6a–c.

So, the adaptation of (15) using the API controller for controlling $i_{u,i}$ is mathematically defined as

$$e_{v\alpha\beta}^{*}(API) = U_{1}K_{1}u(t) + U_{2}K_{2}\int u(t)dt$$
(17)

The adaptation of (16) using the API controller for controlling $i_{diff,i}$ is mathematically defined as

$$v_{diff,j}^{*}(API) = U_1 K_1 u(t) + U_2 K_2 \int u(t) dt$$
(18)

Whereas U_1 and U_2 are the fuzzy controller output for the gains k_p and k_i respectively. While K_1 and K_2 are used for the representation of updated gains k_p and k_i as shown in Figure 7.



Figure 6. Adaptation of gain values where the: (a) error is zero, (b) error is small, and (c) error is large.



Figure 7. Control structure for error tracking.

3.2. Circulating Current Control

The $i_{diff,j}$ can be regulated by $v_{diff,j}$, therefore, a control scheme is needed to regulate $v_{diff,j}$ for controlling $i_{diff,j}$ to overcome the converter losses. The variable $v_{diff,j}$ is obtained through (8). A feedforward part is added to compensate the voltage drop. Filtering of U_{dc} is not required due to the lower number of harmonics in DC voltage compared to AC voltage. The control law we obtain

$$v_{diff,j}^* = \frac{U_{dc}}{2} - Ri_{diff} - \left(U_1 K_1 u(t) + U_2 K_2 \int u(t) dt\right) \left(i_{diff,j}^* - i_{diff,j}\right)$$
(19)

The reference is forced to get the desired value of circulating current, i.e., P/MU_{dc} . The control structure using the API controller is shown in Figure 8.



Figure 8. Circulating current control.

3.3. Output Current Control

The output current can be regulated according to the following equation.

$$v_{u,j} = e_{v,j} - \frac{R_{arm}}{2} i_{u,j} - \frac{L_{arm}}{2} \frac{di_{u,j}}{dt}$$
(20)

By evaluating (20), $e_{v,j}$ is the variable which can be regulated to control the output current $i_{u,j}$. The Equation (20) can be rewritten as

$$\begin{bmatrix} e_{v,a} \\ e_{v,b} \\ e_{v,c} \end{bmatrix} = \frac{1}{2(sL_{arm} + R_{arm})} \begin{bmatrix} i_{u,a} \\ i_{u,b} \\ i_{u,c} \end{bmatrix} + \begin{bmatrix} v_{u,a} \\ v_{u,b} \\ v_{u,c} \end{bmatrix}$$
(21)

Using the Clarke transformation Equation (20) can be transformed into $\alpha\beta$ axis from *a-b-c* frame $(T_{abc \Rightarrow \alpha\beta})$.

$$T_{abc \Rightarrow \alpha\beta} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\ \sqrt{\frac{1}{2}} & \sqrt{\frac{1}{2}} & \sqrt{\frac{1}{2}} \end{bmatrix}$$
(22)

For a three-phase balance system Equation (21) can be written

$$\begin{bmatrix} e_{v,\alpha} \\ e_{v,\beta} \end{bmatrix} = \frac{1}{2(sL_{arm} + R_{arm})} \begin{bmatrix} i_{u,\alpha} \\ i_{u,\beta} \end{bmatrix} + \begin{bmatrix} v_{u,\alpha} \\ v_{u,\beta} \end{bmatrix}$$
(23)

In control engineering usage $v_{u,\alpha\beta}$ is load disturbance which will vary inversely according to short circuit ratio [32]. Since $v_{u,\alpha\beta}$ is measurable by introducing a feedforward term, the dynamics can be improved. The control law obtained

$$e_{v,j}^* = \left(U_1 K_1 u(t) + U_2 K_2 \int u(t) dt \right) \left(i_{u,j}^* - i_{u,j} \right) + v_{u,\alpha\beta} + i_{u,\alpha\beta} \frac{R}{2}$$
(24)

where $(i_{u,j}^* - i_{u,j})$ error control and the reference for voltage is computed using Equation (24), to control the output current. The output current control structure is shown in Figure 9.



Figure 9. Output Current control.

The overall control scheme for controlling the circulating current and output current is shown in Figure 10.



Figure 10. Overall control design for output and circulating currents.

4. Results and Discussions

A control design architecture for a three-phase MMC model has been implemented with the help of MATLAB-Simulink (2017a, Natick, Massachusetts, MA, USA). The time for simulation is considered as 2 s. For better understanding, all the results are magnified. A comparative analysis of the results between the proposed API controller and a conventional PR controller has been made. The capacitor sum voltage and circulating current are the variables which need to be controlled, to reduce MMC losses. Ideally, the circulating current value i_{diff} should converge to DC input power, i.e.,

 $\left(i_{diff} = \frac{P}{MU_{dc}}\right)$. The results of circulating current in Figure 11a,b is compared using the API and a PR controller. For a better understanding, the results are magnified for time t = 0.6 s to t = 0.8 s using the magnification tool. The API controller result shows that the circulating current value is converging to the desired value, i.e., 250 A with no oscillations during 0.2 s, showing good result compared to

the PR controller. While in the case of PR controller the circulating current value is not converging to the desired value i.e., 250 A and also shows oscillations up to 0.2 s. Which means that the circulating current is not converged properly. It is also noted that circulating current one phase is exceeding the desired value, i.e., 250 A throughout the simulation as depicted in Figure 11b. The capacitor sum voltage average value should converge to DC link voltage mean value, i.e., $\frac{U_{dec}}{2}$ to get a balance distribution of capacitor sum voltage over desired SM. The capacitor sum voltage of both an upper and a lower arm using the API and a PR controller are analyzed in Figures 12a,b and 13a,b. For a better explanation, the results are magnified for time t = 0.58 s to t = 0.68 s using the magnification tool. The API controller display proper convergence of capacitor sum voltage to a desired DC link mean value for both upper and lower arm. All phase voltages are balanced in magnitude, which shows the balance distribution of capacitor sum voltage for the required SM in both upper and lower arm. As opposed to the API controller, the capacitor sum voltage of both upper and lower arm. As an imbalance in magnitude and created a trajectory path from 0.2 s to 0.8 s, which meant that the capacitor sum value was not totally converged to the desired DC link mean value for a balance distribution of capacitor voltage in the required SM.



Figure 11. (a) Circulating current using the API controller; (b) circulating current using the PR controller.



Figure 12. (**a**) Capacitor voltage of an upper arm using the API controller; (**b**) capacitor voltage of an upper arm using the PR controller.



Figure 13. (a) Capacitor voltage of a lower arm using the API controller; (b) capacitor voltage of a lower arm using the PR controller.

The dynamics of sum energy for both controllers are shown in Figure 14a,b. For better analysis, the results are magnified for time t = 0.6 s to t = 1 s using the magnification tool. At the start, the API controller shows overreaction. The rules defined in the rule-base are called and the decision is carried out to fire the dominant rule. The API controller takes only 0.024 s to clear transient, and the reference is completely tracked at 0.75 s completely without further overshoot, undershoot, steady state error, and ripples. However, in the case of the PR controller, the reference is completely tracked at 1 s. Moreover, the API controller shows a fast and robust response during the transient state. While the sum energy using a PR controller shows slower, oscillatory, and steady-state error. Which means that the PR controller response is slower than the proposed API controller. The energy difference mean value should be regulated correctly and converged to the zero reference value properly. The energy difference dynamics of the MMC for both controllers are shown in Figure 15a,b. For the result analysis, the results are magnified using the magnification tool for t = 0.65 s to t = 0.8 s. At the start, the proposed API controller shows an overreaction for 0.012 s, because the gains parameters are not updated. The rules defined in the rule-base are recalled and update the gain parameters of the PI controller. Once the parameters are updated, the proposed controller start tracking the reference without any further overshoot and converges the delta energy properly to the desired mean value. However, in the PR controller, the delta energy mean value is not converged properly, or showing disturbance, during time interval 0.65 s to 0.8 s. Moreover, the energy difference of both controllers is nearly close to zero. However, the API controller shows perfectly close convergence to zero and oscillating between positive and negative values giving an average value of zero, which means that the energy difference is regulated correctly and converged to zero reference value properly. In comparison to the API, the PR controller in the zoomed window shows that the desired value is showing some disturbance from 0.6 s to 0.8 s, which means that value is not converging to zero correctly during the mentioned time frame or showing a steady state error.



Figure 14. (a) Sum energy of arms using the API controller; (b) sum energy of arms using the PR controller.



Figure 15. (**a**) Energy difference of arms using the API controller; (**b**) energy difference of arms using the PR controller.

The output current is transformed in the $\alpha\beta$ axis for both controllers shown in Figure 16a,b. For the explanation of results, the results are magnified using the magnification tool for *t* = 0.6 s to t = 0.8 s. For both controllers, the comparison is made on the basis of the performance indices, reference tracking, and transient and steady-state response. The API controller shows better performance indices, reference tracking, and transient and steady-state response compared to the PR controller. The performance indices are summarized in Table A2 of Appendix A, while converter and grid output phase voltages are shown in Figures 17a,b and 18a,b for both controllers. For an explanation of the results, they are magnified using the magnification tool for t = 0.8 s to t = 0.88 s.



Figure 16. (a) Output current using the API controller; (b) output current using the PR controller.



Figure 17. (a) Converter phase voltages using the API controller; (b) converter phase volatges using the PR controller.



Figure 18. (a) Grid phase voltage using the API controller; (b) grid voltage using the PR controller.

The control structure designed with the API controller is compared with conventional PR controller. The comparative analysis of a conventional PR and proposed API controller for controlling the output and circulating currents shows that the designed controller can track the references. The designed controller is also capable of converging the circulating current to the desired value. With the proposed control structure, the designed controller can converge the DC link voltage mean value to $\frac{U_{dc}}{2}$ for the balance distribution of the capacitor sum voltage in the desired SM. The energy difference in both arms is well regulated by the proposed controller. The proposed API controller shows an improved transient response compared to the PR controller.

5. Conclusions and Future Work

This paper presents a control structure for controlling the circulating current and output current of the MMC during normal grid conditions. The control structure is implemented with an API controller. The control designed structure enables reference tracking, controlled circulating current, output current, the convergence of sum and delta energies to proper values. For validation of the proposed control structure, the results of the designed API controller are compared with a conventional PR control for verification.

MATLAB-Simulink results for both PR and API controllers shows that the API controller has a superior performance over the PR controller. The API controller can converge the circulating current to the desired value. The proposed API controller also shows improved transient response compared to the PR controller. In addition, the controller assures the reference tracking for sum energy and delta energy during the transient and steady response ensuring a stable three-phase output voltage. Moreover, the optimal performance achieved for the MMC parameters using the API controller are shown in Table A1 of Appendix A.

However, the designed control structure for circulating current and output current will be validated using FPGA or DSP TMS3200F28xx Kits (Texas Instruments, Dallas, TX, USA), Intel 8Xc196KC microcontroller (Intel corporation, Santa Clara, CA, USA) and dSPACE Digital Signal Controllers boards (DS1104, dSPACE Inc, Michigan, MI, USA). Feedback linearization and optimal control strategies will be designed and compared with conventionally tuned PR control structure.

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Appendix A

Parameters	Values	Symbols	Units
D.C voltage	200	U_{dc}	kV
Grid voltage	100	v_{u}	kV
Output current	1	i_u	kA
Frequency	50	f	Hz
No of SM	12	N	-
Inductance	50	Larm	mH
Resistance	1.57	R _{arm}	Ω
Capacitance	0.45	С	mF

Table A1. Converter parameters.

Table A2. Performance indices evaluation.

	API Controller			PR Controller		
Performance Indices Elevation For	¹ ISE	² IAE	³ IATE	ISE	IAE	ITAE
Circulating current	3.714 3.665 3.694	0.2237 0.2105 0.2200	0.1834 0.1836 0.1832	1696 2247 2483	23.47 25.46 25.31	14.63 14.47 25.31
Output current	400.56 20.86	5.968 5.658	2.915 2.997	1022 50.5	13.5 13.1	8.913 9.009

¹ISE: Integral Square Error, ²IAE: Integral Absolute Error, ³ITAE: Integral of time Absolute Error.

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