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Equivalent Phase Current Harmonic Elimination in Quadruple Three-Phase Drives Based on Carrier Phase Shift Method

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Abstract: Multiphase drives are entering the spotlight of the research community for transportation applications with their high power density and the possibility of high fault tolerance. The multi three-phase drive is one of the main types of multiphase drives that allows for the direct adoption of commercial three-phase converters and high control flexibility. The elimination of high-frequency current harmonics will reduce the flux linkage harmonics, torque ripple, vibration and noise in machine drives. Therefore, this work introduces a new method to the modelling of equivalent phase current in multi three-phase drives with the double integral Fourier analysis method. A new carrier-based pulse-width modulation (CPWM) method is introduced to reduce the equivalent phase drives. The proposed angles of carrier signals are analyzed for quadruple three-phase drives, and the corresponding experimental results confirm the significance of the proposed phase-shifted CPWM method to eliminate the equivalent phase current harmonics.

Keywords: multiphase drives; pulse width modulation; current harmonics

1. Introduction

Three-phase machines were universally used at the beginning of the 20th century as they present better torque performance compared with the single-phase and the two-phase machines, which produce the twice pulsating torque ripple [1]. The increasing phase number of the machine will not produce the twice-pulsating torque ripple. The development of power electronics in the 1980s enabled the adoption of machines with more than three phases. The machine can be connected to the power converters rather than directly connected to the three-phase power supply. Therefore, an arbitrary phase machine can be used as long as the phase number of the machine matches the phase number of the power converter, especially for the application of electric propulsion [2–5]. There are several reasons why the multiphase machine has good fault tolerance performance compared with the conventional three-phase machine [6,7]. Taking a 15-phase machine as an example, the machine can be operated at over 90% of the rated power with the breakdown of one phase. Secondly, the adoption of a multiphase machine greatly reduces the voltage sharing on power converters of each phase leg, which offers the feasibility of the multiphase machine used for high-power applications [8–12]. Additionally, the control of a multiphase drive system becomes more flexible, which gives a possibility to cancel the time-domain harmonics and



space-domain harmonics in the drive system. Therefore, the efficiency and torque performance in the multiphase is improved with respect to the traditional single three-phase drives [13–15].

Recently, multi three-phase machines with separate neutral points controlled by parallel converters have become an increasing concern, which is shown in Figure 1[8,16]. The adoption of parallel converters make it possible to control very high-power and high-speed machines with commercial products, since the power requirement on each inverter module is reduced. Moreover, this topology has high fault tolerance, as any inverter module is independent without any electrical connections to the other modules [6]. Besides, the adoption of the parallel converters increases the flexibility of the control algorithm. The torque ripple, noise and vibration of the machine, and the direct current (dc) link voltage ripple are possibly to be reduced by applying a proper control scheme [17,18].



Figure 1. Multi three-phase drives system.

The carrier-based pulse width modulation (CPWM) is known as the earliest and most straightforward modulation technique [19]. The working principle of the CPWM is to compare the fundamental signal with the carrier signal (high frequency) to determine the states of switching devices on power converters. When the fundamental waveform is lower than the carrier with waveform, the switch at the bottom of the converter leg ($T_2 T_4$ and T_6 in Figure 1) will turn on; when the fundamental waveform is larger than the carrier waveform, the switch on the top of the converter leg (T_1 T_3 and T_5 in Figure 1) will turn on. Compared with the space vector modulation, the CPWM has lower dc link voltage utilization [20]. This drawback can be avoided by the injection of the zero-sequence harmonics [21]. The implementation of CPWM is straightforward and it can be extended to the control of the multiphase multilevel converters without greatly increasing the complexity of the control algorithm. The phase-shifted CPWM is the commonly used CPWM for the control of multilevel converters, as it has the same switching frequency and conducting periods, which means even power distribution on the power switches of the converter [22,23]. Recent research works have verified that the pulse width modulation (PWM) technique results in high-frequency torque ripple in multiphase drives [24,25]. For instance, [25] shows that the high-frequency torque harmonics are mitigated by choosing appropriate carrier angles in dual three-phase drive systems. The high-frequency PWM-related current harmonics are known as the source of the high-frequency flux linkage harmonics, torque ripple, vibration and noise [26,27]. Therefore, it is of importance to reduce the equivalent current harmonics of the multiphase drive systems by considering the effect the CPWM technique.

This article exploits the degrees of control freedom in multi three-phase drives, by applying the concept phase-shifted CPWM to multi three-phase drives to reduce total equivalent current harmonics.

The remaining part of the article includes the following sections: Section 2 introduces the double Fourier integral analysis method of pulse width modulation. Section 3 presents the mathematical modelling of the equivalent current harmonics in multi three-phase drives. Section 4 shows the comparative experiment with and without applying the proposed phase-shift CPWM. Section 5 draws the conclusion of this article.

2. Double Fourier Integral Analysis Method of Pulse Width Modulation

According to [19], if the function of both x(t) and y(t) are periodic functions, the function f(x, y) is the summation of sinusoidal harmonics. Assuming the periods of both the functions x(t) and y(t) are 2π , and f(x, y) is represented by:

$$f(x,y) = \frac{A_{00}}{2} + \sum_{n=1}^{\infty} [A_{0n} \cos ny + B_{0n} \sin ny] + \sum_{m=1}^{\infty} [A_{m0} \cos mx + B_{m0} \sin mx] + \sum_{m=1}^{\infty} \sum_{n=-\infty}^{\infty} [A_{mn} \cos(mx + ny) + B_{mn} \sin(mx + ny)],$$
(1)

with:

$$A_{mn} = \frac{1}{2\pi^2} \int_{-\pi}^{\pi} \int_{-\pi}^{\pi} f(x, y) \cos(mx + ny) dx dy,$$
(2)

$$B_{mn} = \frac{1}{2\pi^2} \int_{-\pi}^{\pi} \int_{-\pi}^{\pi} f(x, y) \sin(mx + ny) dx dy.$$
(3)

where *m* and *n* are the positive integer numbers.

Sine-triangle modulation is one of the most commonly used CPWMs. Figure 2 describes the x-y plane of the sine-triangle modulation, which is represented by f(x, y). The blue area represents the parts when the switch at the top of the converter leg is turned on, while the white area represents the parts when the switch at the bottom of the converter leg is turned on [28]. The *x* and *y* axes are considered as the carrier signal and the modulating signal time-varying angles respectively. y(x) is described with the straight line in Figure 2a. The slope of y(x) is the quotient between the frequencies of the modulating waveform and the carrier waveform. The intersection points between the function y(x) and the changing boundaries of the function f(x, y) are the switching time instants of the converter. The corresponding pulse width modulation (PWM) output voltage waveform (from $-\frac{V_{dc}}{2}$ to $\frac{V_{dc}}{2}$) is shown in Figure 2b.



Figure 2. (a) The x-y plane for the sine-triangle modulation (b) The x-y plane for the corresponding pulse width modulation (PWM) output voltage [28].

The time varying modulating signal $V_m(t)$ can be presented as:

$$V_m(t) = M \cos y, \tag{4}$$

with respect to Equation (4), the intersection points in Figure 2a can be given as the following two cases. Case 1: for the function f(x, y) switches from $-\frac{V_{dc}}{2}$ to $\frac{V_{dc}}{2}$, the switching instants happens at:

$$x = 2\pi p + \frac{\pi}{2}(1 + M\cos y), \ p = 0, \ 1, \ 2, \ 3, \ \dots$$
 (5)

Case 2: for the function f(x, y) converts from $\frac{V_{dc}}{2}$ to $-\frac{V_{dc}}{2}$, the switching time happens at:

$$x = 2\pi p - \frac{\pi}{2}(1 + M\cos y), \ p = 0, \ 1, \ 2, \ 3, \ \dots$$
(6)

The switching time instants in Equations (5) and (6) are the upper and the lower integral limits in Equations (2) and (3), respectively. The function of $f(x, y) = \frac{V_{dc}}{2}$ between the upper and the lower limits. Substituting Equations (5) and (6) into Equations (2) and (3), A_{mn} and B_{mn} can be rewritten as:

$$A_{mn} = \frac{1}{2\pi^2} \int_{-\pi}^{\pi} \int_{-\frac{\pi}{2}(1+M\cos y)}^{\frac{\pi}{2}(1+M\cos y)} \frac{V_{dc}}{2} \cos(mx+ny) dx dy,$$
(7)

$$B_{mn} = \frac{1}{2\pi^2} \int_{-\pi}^{\pi} \int_{-\frac{\pi}{2}(1+M\cos y)}^{\frac{\pi}{2}(1+M\cos y)} \frac{V_{dc}}{2} \sin(mx+ny) dx dy.$$
(8)

Substituting Equations (7) and (8) to Equation (1), and replacing *x* and *y* with $\omega_c t + \theta_c$ and $\omega_o t + \theta_o$ respectively, the time-varying output voltage of terminal a with respect to the ground in Figure 2 can be represented by the function of f(t), which can be expressed as:

$$f(t) = \underbrace{\frac{V_{dc}}{2}}_{DC \ offset} + \underbrace{\frac{V_{dc}}{2}Mcos(\omega_{o}t + \theta_{o})}_{fundamental \ component} + \underbrace{\frac{4V_{dc}}{\pi}\sum_{m=1}^{\infty}\frac{1}{m}J_{0}\left(m\frac{\pi}{2}M\right)\sin m\frac{\pi}{2}cos(m[\omega_{c}t + \theta_{c}])}_{carrier \ harmonics} + \underbrace{\frac{4V_{dc}}{\pi}\sum_{m=1}^{\infty}\sum_{n=-\infty}^{\infty}\frac{1}{m}J_{n}\left(m\frac{\pi}{2}M\right)\sin\left([m+n]\frac{\pi}{2}\right)\cos(m[\omega_{c}t + \theta_{c}] + n[\omega_{o}t + \theta_{o}].$$

$$\underbrace{sideband \ harmonics}$$
(9)

By using the double Fourier integral method, the time-varying phase leg voltage of f(t) can be expressed with the addition of the dc offset, the fundamental component and all of the sinusoidal harmonic components (carrier harmonics and sideband harmonics).

3. Modelling of Equivalent Current Harmonics in Multi Three-Phase Drives

According to Equation (9), assuming the modulating signal start angle as the reference ($\theta_o = 0$), the phase leg voltage (phase leg voltage is defined as the voltage drop between a, b, c and z, which is displayed in Figure 1) of the p^{th} , $p \in \{1, ..., N\}$ three-phase subsystem u_{a_pz} (phase a) is represented as:

$$u_{a_p z}(t) = \frac{V_{\rm dc}}{2} M \cos \omega_0 t + \sum_{m=1}^{\infty} \sum_{n=-\infty}^{\infty} A_{mn} \cos\{m x_p(t) + n \omega_0 t\},\tag{10}$$

with:

$$A_{mn} = \frac{2V_{\rm dc}}{m\pi} J_n \left(m\frac{\pi}{2} M \right) \sin\left[(m+n)\frac{\pi}{2} \right],\tag{11}$$

$$x_p(t) = \omega_c t + \theta_{c,p},\tag{12}$$

where $x_p(t)$ is the carrier phase angle in the p^{th} three-phase subsystem and $\theta_{c,p}$ is the p^{th} three-phase subsystem carrier phase start angle (t = 0). Referring to Equation (10), the equivalent phase voltage $u_{total}(t)$ generated by all the subsystems (shown in Figure 1) can be represented as:

$$u_{total}(t) = \frac{NV_{dc}}{2} M \cos \omega_{o} t + \sum_{p=1}^{N} \sum_{m=1}^{\infty} \sum_{n=-\infty}^{\infty} A_{mn} \cos\{mx_{p}(t) + n\omega_{o}t\},$$
(13)

As can be seen from Equation (13), the total equivalent harmonic voltage will be decreased with different carrier angles in different three-phase systems. Applying $\theta_{c,p} = \frac{2\pi(p-1)}{N}$ to each three-phase system, the multi three-phase drive system will gain an effective switching frequency of Nf_c . With the proposed phase-shifted CPWM in each three-phase drive, the total equivalent phase voltage is expressed by Equation (14):

$$u_{total}(t) = \frac{NV_{dc}}{2} M \cos \omega_{o} t + \sum_{p=1}^{N} \sum_{m=1}^{\infty} \sum_{n=-\infty}^{\infty} A_{mn} \cos\{Nmx_{p}(t) + n\omega_{o}t\},$$
(14)

Comparing Equations (13) and (14), the equivalent voltage harmonic components are significantly mitigated, while the remaining harmonics only exist around the multiples of $N f_c$.

Figure 3 shows the Fast Fourier Transformation (FFT) spectra of total equivalent voltage applying the phase-shifted CPWM with normalized V_{dc} with three modulation indexes (M = 0.9, M = 0.5, and M = 0.1). In Figure 3, the total equivalent voltage is supplied to the machine with the same rated power for any number of split three-phase systems, which means the fundamental voltage components are the same for different values of N. Figure 3 presents the FFT spectra of the total equivalent voltage without the proposed CPWM (for any number of split three-phase systems) at the layer of N = 1. Conforming to Equation (13), the total equivalent voltage is combined with the fundamental component and the groups of harmonics around the multiples of switching frequency. Figure 3 displays the FFT spectra of total equivalent voltage with proposed CPWM at the layers of $N \ge 2$. Consistent with Equation (14), the equivalent voltage harmonic components are significantly mitigated, while the only remaining harmonics exist at Nf_c . The larger value of N will result in higher order remaining harmonics, and thus the effect of total equivalent voltage harmonics cancellation is more dominant with the increasing value of subsystem number N. Comparing Figure 3a-c, it can be observed that a different modulation index affects the harmonic amplitudes to a different extent. However, the same groups of the harmonic components will be eliminated despite the modulation indexes, and thus the proposed phase-shifted CPWM method can be applied into different working conditions. Taking the case N = 4 as an example, with phase-shifted CPWM, the equivalent voltage harmonics around f_c, 2f_c, 3f_c, 5f_c, 6f_c, 7f_c, 9f_c, 10f_c are cancelled out under all the modulation indexes.

In [24], it indicates that the relationship between voltage harmonics u_h and current harmonics i_h can be drawn as:

$$u_h = i_h \cdot Z(h),\tag{15}$$

where Z(h) is the impedance of the h^{th} harmonic component. According to Equations (13) and (14), the equivalent phase current $i_{total}(t)$ can be represented as:

$$i_{total}(t) = \frac{NV_{dc}M}{2z(0)}\cos\omega_{0}t + \frac{1}{z(h)}\sum_{p=1}^{N}\sum_{m=1}^{\infty}\sum_{m=-\infty}^{\infty}A_{mn}\cos\{mx_{p}(t) + n\omega_{0}t\}.$$
 (16)

Applying the proposed phase-shifted CPWM in each three-phase system, the equivalent phase current can be represented as:

$$i_{total}(t) = \frac{NV_{dc}M}{2z(0)}\cos\omega_{0}t + \frac{1}{z(h)}\sum_{p=1}^{N}\sum_{m=1}^{\infty}\sum_{m=-\infty}^{\infty}A_{mn}\cos\{Nmx_{p}(t) + n\omega_{0}t\}.$$
 (17)

Referring to Equations (14) and (17), it is concluded that applying the proposed phase-shifted CPWM will effectively reduce the equivalent voltage harmonics as well as their corresponding current harmonics.



Figure 3. Multi three-phase drive FFT spectra with and without the proposed phase-shifted carrier-based pulse-width modulation (CPWM) under different *M*. (a) M = 0.9, (b) M = 0.5, (c) M = 0.1.

4. Experimental Results

A quadruple three-phase permanent magnet synchronous machine driven by quadruple independent three-phase modular inverters platform was set up in the laboratory, which is shown in Figure 4, to validate the proposed phase-shifted CPWM method analyzed in Section 3. The PLECS RT lab box is set as a real time controller to generate synchronized gate signals to drive the quadruple three-phase modular converters. The control strategy of this experimental test consists of two closed loops (i.e., the outer speed loop and inner current loop with proportional-integral (PI) controllers). The main experimental parameters are shown in Table 1. In order to better show the PWM harmonics, the fundamental components in Figures 5 and 6 are saturated.



Figure 4. Quadruple three-phase drive system experimental setup.

Table 1. Experimental parameters.

Parameter	Value
Direct current (DC) link voltage (V_{dc})	40 V
Switching frequency (f_c)	2 kHz
Machine pole pair number	4
Load torque	3 Nm
Machine mechanical speed	200 rpm



Figure 5. (**a**,**b**) Experimental result of phase currents without phase-shift CPWM. (**a**) Two fundamental period range. (**b**) The cursor range of Figure 5a,c,d. Equivalent phase current without phase-shift CPWM. (**c**) Time-domain current waveform. (**d**) FFT spectrum of Figure 5c.



Figure 6. (**a**,**b**) Experimental result of phase currents with phase-shift CPWM. (**a**) Two fundamental period range. (**b**) The cursor range of Figure 6a. (**c**,**d**) Equivalent phase current with phase-shift CPWM. (**c**) Time-domain current waveform. (**d**) FFT spectrum of Figure 6c.

The phase currents experimental results without the proposed phase-shift CPWM are displayed in Figure 5. The carrier phase angles in each subsystem were set to be $\theta_{c1} = \theta_{c2} = \theta_{c3} = \theta_{c4} = 0$. Figure 5a shows the time-varying current waveforms (phase A) from all the four subsystems, while Figure 5b shows the cursor range of Figure 5a, which is the two-period range of the carrier signal. Figure 5a,b show that the phase currents are in phase in terms of both modulating the signal period range and carrier signal period. Figure 5c,d show the equivalent phase current and its FFT spectrum without phase-shifted CPWM.

The equivalent phase current waveform in Figure 5c is the same as the phase current waveforms in Figure 5a, and the current total harmonic distortion (THD) of both Figure 5a, c is 28.5%. This result matches with Equation (16) shown in Section 3, which indicates that the equivalent phase current magnitudes of both fundamental component and high-order current harmonics will be four times the phase currents' magnitudes in a quadruple three-phase drive. As shown in Figure 5d, the equivalent phase current harmonics exist at the multiples of the switching frequency (2 kHz), which exist around 2 kHz, 4 kHz, 6 kHz, 8 kHz, 10 kHz, 12 kHz, 14 kHz, 16 kHz, 18 kHz, 20 kHz. The harmonic FFT spectra are consistent with the analytical results shown in Figure 3 (the layer of N = 1).

Figure 6 describes the experimental results of phase currents applying the proposed phase-shift CPWM. According to the analytical models in Section 3, the carrier phase angles $\theta_{c1} = 0$, $\theta_{c2} = \frac{\pi}{2}$, $\theta_{c3} = \pi$ and $\theta_{c4} = \frac{3\pi}{2}$ are applied to the four subsystems accordingly. Similar to Figure 5, Figure 6a shows the four subsystem phase current waveforms, while Figure 6b presents the cursor range of Figure 6a. According to Figure 6b, it is can be seen that the phase currents in the carrier signal period range are effectively shifted with proposed phase-shifted CPWM, while they are synchronized in Figure 5b. Figure 6c,d show the equivalent phase currents waveform and its FFT spectrum applying the proposed phase-shift CPWM.

The current THD of Figure 6a,c are 36.1% and 12.1%, respectively. In terms of Figures 5a and 6a, it can be observed that the phase current THD is slightly increased with phase-shifted CPWM. This is mainly caused by the mutual coupling effect among machine stator windings, and the increase of phase current THD will result in a higher copper loss of the machine. Different machine stator layouts will result in different mutual coupling effect, which is worthwhile for further analysis in the future. However, this work is mainly aimed at the elimination of the equivalent phase currents to reduce the flux linkage harmonics, torque ripple, vibration and noise in the multi three-phase drives. Figure 5c, and the current THD is reduced from 28.5% in Figure 5c to 12.1% in Figure 6c. Comparing Figure 5d,c, the majority of phase current harmonic components in Figure 6d are eliminated, except the harmonics around 8 kHz and 16 kHz, which are four times and eight times the switching frequency, respectively (m = 1 and m = 2). This result matches the analytical Equations (16) and (17) in Section 3 and the FFT

spectra in Figure 3 (the layer of N = 4). Therefore, the experimental results have validated that the total equivalent phase currents can be effectively mitigated with the phase-shifted CPWM.

5. Conclusions

This work proposed a phase-shifted CPWM approach to eliminate the equivalent phase current harmonics in quadruple three-phase drives. The optimal carrier phase shift angles are proposed referring to the mathematical equivalent phase current models introduced in Section 3. There is more dominant phase current harmonics elimination with the increasing number of subsystems. The quadruple multi three-phase drive system with the proposed carrier phase angles of $\theta_{c1} = 0$, $\theta_{c2} = \frac{\pi}{2}$, $\theta_{c3} = \pi$ and $\theta_{c4} = \frac{3\pi}{2}$ is verified with experimental tests. The experimental results shows that, applying the proposed CPWM, the equivalent phase current harmonic components are effectively eliminated at the multiples of switching frequency, except the harmonic components around four times and eight times of the switching frequency. The equivalent phase current THD is reduced from 28.5% to 12.1% with the proposed phase-shifted CPWM. The main advantages of the proposed phase-shifted CPWM are listed as follows:

- (1) The phase-shifted CPWM can be applied into arbitrary multi three-phase drives.
- (2) The implementation of the proposed method is simple by changing the initial phase angles of carrier signals in the software.
- (3) Compared with previous research works targeted at torque ripple reduction, this work aims to reduce the total equivalent phase current harmonics, which makes the analytical models much easier. Additionally, the elimination of total equivalent phase current harmonics will result in significant machine performance improvement in terms of torque, vibration and noise.

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Nomenclature

- *V*_{dc} Converter dc-link voltage
- *M* Modulation index.
- $\omega_{\rm o}, \omega_{\rm c}$ Frequencies of the modulating and the carrier signals respectively.
- θ_o, θ_c Initial phase angles of the modulating and the carrier signals respectively.
- *x*, y Time-varying angle of the carrier and the modulating signals respectively
- *J_n* Bessel function
- f_c Switching frequency
- Z(h) The h^{th} harmonic impedance

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