

Overview of Inertia Enhancement Methods in DC System

Feng Wang *, Lizheng Sun, Zhang Wen and Fang Zhuo

State Key Laboratory of Electrical Insulation and Power Equipment, Xi'an Jiaotong University, Xi'an 710049, China

* Correspondence: fengwangee@xjtu.edu.cn

Abstract: The modern power system is experiencing transformation from the rotational-generation-equipment-dominated system to a power-electronics-converter-dominated system, with the increasing penetration of renewable energy resources such as wind and photovoltaic. The power-electronics-based renewable generation, as well as energy storage system, can lead to the reduction of system inertia. As dc systems such as dc microgrids are attracting more attention, the low-inertia issues will challenge their stability. In this paper, a comprehensive review of inertia-enhancement methods in dc power systems is presented. The concept and significance of the inertia in dc systems is firstly introduced, and then the types of inertia-providing sources in dc systems are discussed. After that, the different virtual inertia control strategies applied in power electronics converters are classified and investigated. These virtual inertia control methods are proven to have a great ability to enhance the inertia of a dc system. The challenges and future research direction are discussed at the end of the article. In this paper, the previous research work on the inertia of dc power systems is summarized in detail, the inertia-enhancement methods of DC systems are comprehensively introduced, and the future research directions are prospected.

Keywords: renewable energy sources; dc microgrids; inertia; virtual inertia control; energy storage system



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1. Introduction

The overexploitation and utilization of fossil energy has given rise to troublesome issues, such as energy shortage and environmental pollution [1–4]. The development of renewable energy sources (RESs) can be an environmentally friendly solution to alleviate the problems of the energy crisis and carbon emissions [5,6]. According to the Global Status Report of Renewable Energy in 2021 [7], in spite of the impact of the COVID-19 pandemic, more than 256 GW of RESs capacity was added globally during 2020, still surpassing the previous record by around 30%. Moreover, it was the only type of electricity generation that kept net growth.

Considering that the conventional power system is not suitable for large-scale RESs' access, microgrids (MGs) were proposed and have been deeply studied [8,9]. Integrating the RESs, an energy storage system (ESS), and loads, MGs have merits in RESs accommodation and providing stable power supply for users. Depending on the type of bus voltage in MGs, the microgrids can be classified into ac MGs, dc MGs, and ac/dc hybrid MGs [10,11]. Research, as well as the literature, focuses on ac microgrids, since the utility grid relies on an ac system [12,13]. However, owing to the inherent dc nature of various RESs, ESS, and loads, such as photovoltaic panels, battery energy storage, LEDs, and electrical vehicles, dc MGs are more suitable for those dc characteristic components' access. Compared to ac MGs, neither reactive power nor harmonics exists in dc MGs, so higher efficiency and power quality can be achieved [14–16]. Therefore, dc systems such as dc distribution networks and dc MGs have developed rapidly in recent years.

However, the rapid growth of RESs is raising new problems in regard to the new-type electrical system. On the one hand, the intermittent and fluctuation of sustainable energy

power generation brings difficulty to the power system in real-time power balance. This problem is particularly prominent for MGs with distributed RESs as the main energy sources. The power mismatch between power generation and load demand will cause the frequency excursion and fluctuation in ac MGs [17], while in dc MGs, the unbalanced power manifests as the voltage deviation and the voltage fluctuation [10]. On the other hand, RESs are connected to the power grid through power electronics devices, which have no ability to provide mechanical inertial response as the rotational power generation equipment [18]. With the transformation from the traditional generator-based system to the power electronics converter-based system, the modern power system, especially distribution network or MG composed of distributed RESs connected by power electronic converters, is presenting the characteristic of low inertia and weak damping [19,20]. Low inertia and weak damping may cause system stability problems. Among them, the more serious problem of the ac system is the frequency stability, while the main problem of the dc system is the voltage stability.

The frequency stability is of great significance and has the priority to be concerned in an ac power grid [21,22]. Normally the system should be operating at the nominal frequency. When the power fluctuations occur on the RESs generation side or loading side, the grid frequency may deviate from its nominal value. Once the frequency deviation exceeds the allowable limits, it will induce the tripping of the frequency relays in the system, and then it may lead to serious grid fault, and even collapse in the worst case [20]. In order to keep the balance between power generation and load consumption, a multi-timescale coordinated frequency control strategy is proposed and implemented in the power system, which is illustrated in Figure 1. It can be seen from Figure 1 that inertia control plays an important role in hindering the sudden change of frequency within the first few seconds (1–10 s). The system inertia is usually provided by the rotational generator, such as a synchronous generator (SG), which can offset the imbalance power by absorbing or releasing the kinetic energy stored in the rotors [23]. There have been many research studies aiming at enhancing the inertia levels of power-electronics-based ac MGs, and virtual synchronous generator (VSG) control is the most potential and widely used method [24–27].

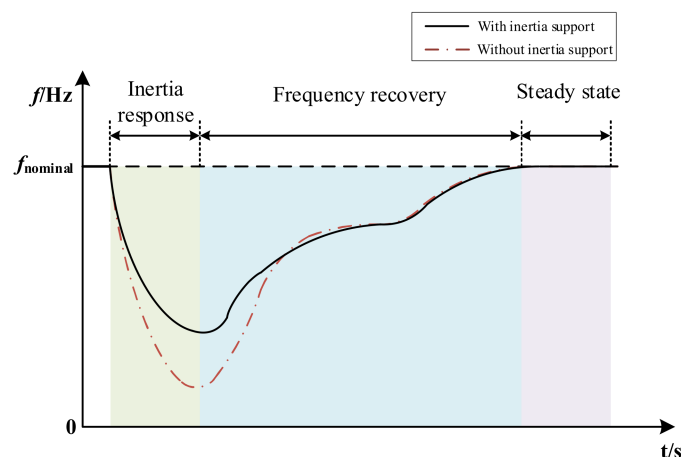


Figure 1. The multi-timescale coordinated frequency response process.

The penetration of a high proportion of RESs and power-electronics-based equipment also reduces the inertia of the dc system. In dc system, the frequent switching loads and intermittent RES generation (e.g., photovoltaic source and wind resource) can make the dc bus voltage fluctuate greatly and reduce the efficiency and stability of the system. Reference [28] analyzes the reasons for the voltage instability in the dc MG system and gives the method for voltage stability control. In Reference [29], the relationship between the damping and inertia of the dc system is studied. A unified concept is proposed by connecting the oscillation-related stability issues raised by weak damping with the sudden change in voltage that originates from low inertia. However, these works from the literature

mainly focus on the study of the mechanistic reasons for the low inertia of the dc system; the research on the dc inertia enhancement methods has only begun to rise with the application and promotion of dc MGs in recent years. The dc bus voltage stability is the only concern in dc MGs, and the methods of emulating the inertia of the dc system are different from those in the ac system. Appropriate inertia control can mitigate the voltage deviation, as well as the voltage oscillation, and maintain the dc voltage stability [29–31].

At present, many scholars have summarized and prospected the research work on the low-inertia problem of power system. In Reference [20], current state-of-the-art of virtual inertia implementation techniques are summarized, and potential research directions and challenges about low-inertia issues are also explored. Reference [23] presents a comprehensive review of inertia-enhancement methods, including both proven technologies and emerging ones, and also discusses the impact of inertia on frequency control. However, these review articles are all focused on inertia-enhancement methods for ac systems, and the research work on dc inertia problems is rarely summarized. This paper presents an overview of the inertia-enhancement methods in the dc system. By summarizing the past research work on the inertia of the dc power system, this article generalizes the definition and physical meaning of the inertia in the dc power system and sorts out the inertia-supply sources. This paper also highlights the state-of-the-art inertia-control methods for power electronic converters, and future research directions are also prospected.

The remaining part of this paper is organized as follows: The concept of the inertia in dc system and the classification of inertia enhancement methods based on inertia-providing sources are introduced in Section 2. In addition, the necessity of inertia is discussed. Then various virtual inertia control algorithms and design processes are detailed in Section 3. The advantages and disadvantages of each method are also presented. Finally, the technical challenges and possible future research directions are discussed in Section 4. The last part is the conclusions in Section 5.

2. Concept and Classification of Inertia Enhancement in DC System

2.1. Concept of Inertia in DC System

In an ac system, the inertia is represented by the ability to impede the frequency abruptly, which is generally provided by SG in traditional system [20]. The power-balance equation of SG is expressed as follows:

$$P_m - P_e - D_d(\omega - \omega_n) = J\omega \frac{d\omega}{dt} \approx J\omega_n \frac{d\omega}{dt} \quad (1)$$

where P_m , P_e , D_d , ω , and ω_n are the mechanical power, the electromagnetic power, the damping coefficient, the angular frequency of SG, and the nominal frequency of the system, respectively; and J is the moment of inertia.

As the frequency decreases, the SGs automatically slow down and release the kinetic energy stored in the rotors; therefore, the rate of change of frequency (RoCoF) is reduced, and the frequency nadir is improved. The kinetic energy of the rotors is expressed as follows [20]:

$$W_{rotor} = \frac{1}{2} J\omega^2 \quad (2)$$

The existence of system inertia can provide SGs with sufficient time to adjust power output and re-establish the power balance. The time constant of inertia is described as follows:

$$H_{ac} = \frac{W_{rotor}}{S_N} = \frac{1}{2} \frac{J\omega^2}{S_N} \quad (3)$$

where S_N denotes the rated capacity of SG.

Correspondingly, inertia in dc systems such as dc MGs manifests the ability to mitigate the sudden change of the dc voltage, and the dc-link capacitors are the components which provide the inertia of dc MGs [32]. To illustrate the principle of how capacitors play roles

in inertia response to the voltage abrupt, the relationship between voltage and current on both sides of the dc-link capacitor is shown in Figure 2, and it is described as follows:

$$i_{in} - i_{out} = C \frac{du_{dc}}{dt} \quad (4)$$

where i_{in} denotes the output current of the converter, i_{out} denotes the current flows to the dc bus, i_c denotes the capacitor current, C denotes the capacitor of the dc bus, and u_{dc} denotes the dc bus voltage.

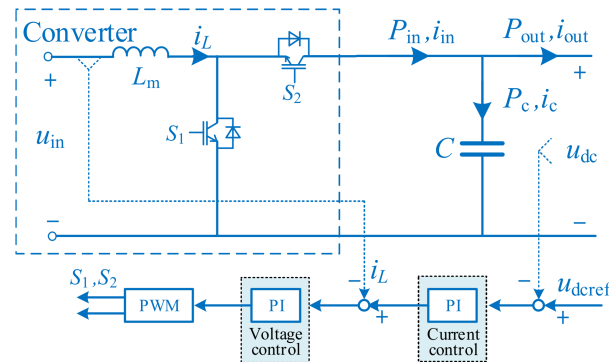


Figure 2. Power relationship of dc-link capacitor side.

Multiply both sides of the Equation (4) by u_{dc} , and the formula of power balance can be obtained:

$$P_{in} - P_{out} = C \cdot u_{dc} \cdot \frac{du_{dc}}{dt} \quad (5)$$

where P_{in} denotes the output power of the converter, P_{out} denotes the power flows to the dc bus, and P_c denotes the power charging of the capacitor. It can be known from (4) and (5) that the unbalanced power between P_{in} and P_{out} is compensated by the dc-link capacitor. When the dc bus voltage decreases, the capacitor releases its saved energy and provides active power support; therefore, the rate of change of voltage (RoCoV) is reduced, and the voltage nadir is improved, and vice versa. As the power balance in dc MGs is achieved, the dc-link capacitor stops charging or discharging, indicating that the inertia has no effect on the steady state.

Similarly, the energy stored in capacitors and the time constant of inertia in dc MGs can be described as follows:

$$W_{cap} = \frac{1}{2} C u_{dc}^2 \quad (6)$$

$$H_{dc} = \frac{W_{cap}}{S_{Nc}} = \frac{1}{2} \frac{C u_{dc}^2}{S_{Nc}} \quad (7)$$

where S_{Nc} denotes the rated capacity of capacitors.

According to the previous analysis, the larger the dc-link capacitance, the larger the inertia time constant and the system inertia. However, the dc-link capacitance in dc MGs is usually limited and small, so the system inertia supplied by parallel capacitors is too insufficient to suppress the dc bus voltage abruptly, and so it consequently needs to be enhanced. Note that the topology of non-isolated bi-directional dc/dc converter shown in Figure 2 is just an example; the above analysis can also apply to other topologies, such as dual active bridge (DAB) dc/dc converters [33–36], LLC converters [37], CLLC converters [38], and so on.

2.2. Classification of Inertia-Providing Sources

To further enhance the system inertia, it is necessary to fully discuss the possible inertia-providing sources in the dc system. A typical topology of dc MG is depicted in Figure 3. It is usually composed of RESs (e.g., solar energy and wind energy), energy

storage units, and loads [39,40]. These elements are connected to the dc bus through power electronic converters, which lack the capability of inertia support [41–43]. Therefore, the dc MG is a classical low-inertia system.

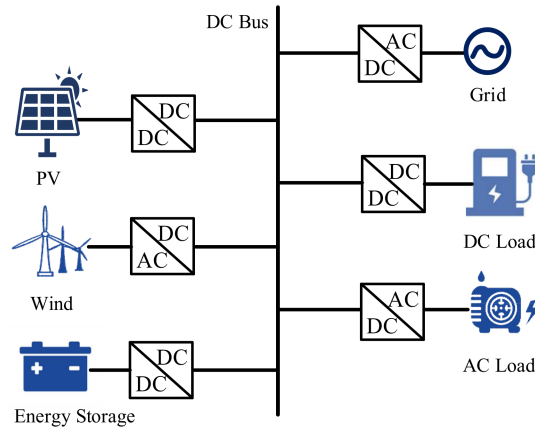


Figure 3. The typical topology of a dc microgrid.

In a dc MG, the sources capable of providing the system inertia can be mainly divided into three types: support by wind turbines, support by energy storage units, and support by photovoltaic (PV) arrays. The classification is shown in Figure 4.

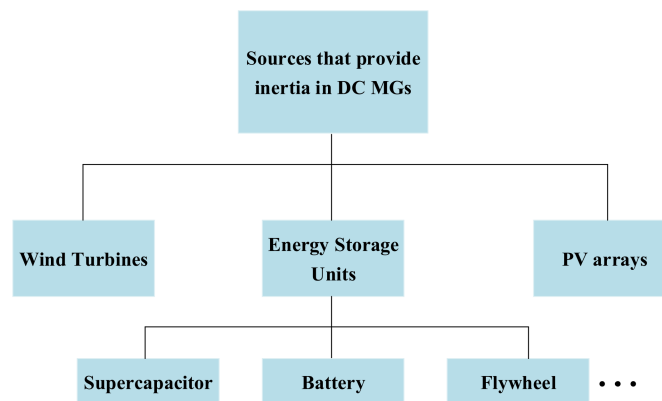


Figure 4. The classification of inertia-providing sources in dc MGs.

2.2.1. Inertia Support by Wind Turbines

Wind energy is one of the most promising sustainable energy resources, and wind turbines have the potential to emulate inertia by adjusting the rotating speed and releasing/absorbing the rotor’s kinetic energy, similar to SGs [44–46]. The scheme of permanent magnetic synchronous generator (PMSG)-based wind-generation system in dc MGs is depicted in Figure 5. It can be seen that the ac/dc converter connects the stator windings of the wind turbine with dc bus, and the wind turbine can achieve maximum wind-energy capture through the maximum power point tracking (MPPT) control of the converter. However, the isolation effect of the converter decouples the mechanical and electrical parts of the wind-generation system, so there is no direct coupling relationship between wind turbine’s rotating speed and dc bus voltage. The wind turbine thence cannot directly respond to the voltage change of the dc bus, and its large amounts of rotational kinetic energy may hardly contribute to support the system power deficit or surplus, resulting in low inertia in dc MGs.

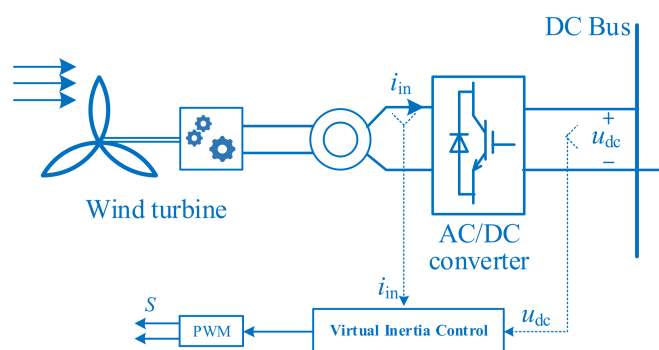


Figure 5. Schematic diagram of PMSG-based wind-generation system in dc MGs.

Many research studies focus on how to take full advantage of the mechanical inertia potential of wind turbines in dc MGs [47–52]. Reference [47] establishes the relationship between the kinetic energy of wind turbine and the capacitor and then introduces the virtual capacitor into the dc MG with wind/energy storage, which improves the system inertia by releasing the kinetic energy on the rotor of the wind turbine. The system stability after introducing virtual inertia is also judged. The detailed virtual inertia control method is detailed in Section 3. In References [48,49], the connection between dc bus voltage fluctuation and the rotor speed change is built, so that the wind turbine can provide virtual inertial power from the kinetic energy stored in the rotor when the DC bus voltage fluctuates. To solve the contradiction between inertia support and maximum power point operation, the MPPT curve is designed to smoothly switched during the inertia-response period, but this method is too complicated. These methods essentially change the power output reference of wind turbines through controlling the converter, so the original operation point of the wind turbine is inevitably changed, maybe resulting in stability issues. The technical difficulties of utilizing wind power to provide system inertia are in the following aspects: fast and accurate response, smooth transition of power, electrical and mechanical system safety, and overspeed/underspeed shutdown. These challenges require further study.

2.2.2. Inertia Support by Energy Storage Units

Energy storage units (ESUs) interfaced with power electronic devices have become an effective means of solving the power fluctuation of RES generation due to their rapid regulation capabilities [53,54]. Moreover, ESUs can provide inertia support by fast charging or discharging. The dc-link capacitor is a typical energy storage element, and its mechanism that provides inertia was discussed above. Currently, ESUs applied in dc MGs include battery energy storage, supercapacitor, flywheel, and so on, and among them, battery energy storage (BES) and supercapacitor (SC) have mature techniques and widespread application prospects.

References [32,55] introduced the RoCoV into the droop coefficient and proposed an improved droop control method. This method is applied in the accumulator battery-connected converter, and then the inertia enhancement is implemented. Although battery has the advantages of high-energy density, its charging and discharging efficiency is relatively low, which is not suitable for frequent charging and discharging to stabilize the power fluctuation of the system. In addition, when using the inertial control method, the slow dynamic response of battery has also become the bottleneck for the function of inertia control. Because of its high-power density, long cycle life, and high charge and discharge efficiency, the supercapacitor is suitable for suppressing high-frequency power fluctuation [56]. However, the costs of supercapacitors are relatively high. Moreover, when the dc system is in the steady-state operation, these supercapacitors are idle, which causes resource waste. Therefore, the hybrid energy storage system (HESS) composed of a battery and supercapacitor is proposed, as it combines the advantages of both the battery and supercapacitor and has been adopted and utilized for inertia support.

The common ways to access HESS in dc MGs includes parallel connection and three-port converter connection [55,57,58]. In Reference [55], a HESS-based inertia control method is given, and it adopts the parallel connection, as illustrated in Figure 6a. In terms of control strategy, except for the power-distribution scheme, the control method of the supercapacitor converter is the same as that of the battery energy storage converter. According to the difference of the charging/discharging characteristics between batteries and supercapacitors, the reference current absorbed or released by HESS is divided into two parts through a low-pass filter (LPF): low-frequency part and high-frequency part. Supercapacitors are intended to mitigate the high-frequency power fluctuation induced by RESs or load mutation and provide inertial support for the dc bus. As a long-term power-balance device, batteries absorb or release low-frequency power to keep the steady-state operation in dc MGs. The way of parallel connection for HESS has a simple structure and ease to implement, but it needs more power electronics converters, resulting in inevitable power loss.

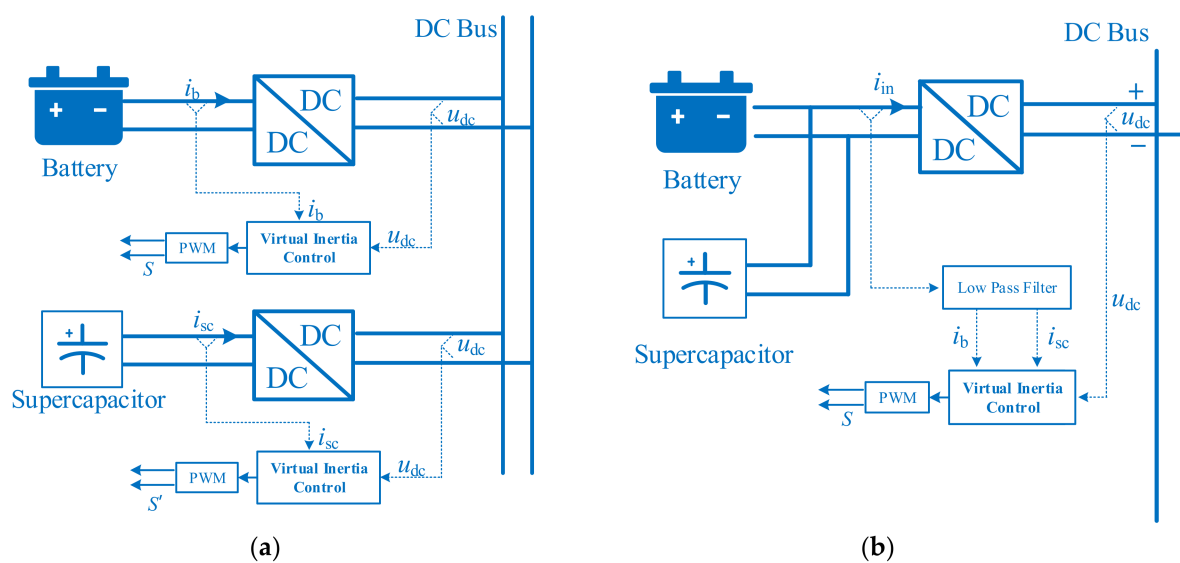


Figure 6. HESS topology in dc MGs: (a) a multi-converter parallel topology and (b) a multi-ports converter topology.

In order to further improve the power density and conversion efficiency of the HESS converter, some scholars propose to use a three-port converter as a hybrid energy storage converter [59,60]. Moreover, the three-port converter is also beneficial to the modular design of the HESS. However, the control strategies for batteries and supercapacitors have become more complex and need further research.

2.2.3. Inertia Support by PV Arrays

In general, PV arrays are integrated into the dc MGs through a boost converter, which MPPT control is applied in order to guarantee the maximum PV power output. Moreover, PV arrays belong to non-rotating stationary element and have no inherent inertia. Therefore, PV arrays seldom take the responsibility of system inertia support. It is feasible for PV to provide inertia by adjusting its operation point apart from maximum power point [61,62], but this method could lead to the reduction of PV generation efficiency [63–65]. Reference [66] proposed a control strategy of PV system based on the virtual dc generator to eliminate the effect of load and light-intensity change on PV system. The proposed topology of PV system is illustrated in Figure 7, where u_{PV} and i_{PV} denote the output voltage and current of PV arrays, respectively; u_b is the output voltage of the first stage boost converter; and u_o and i_o are the output voltage and current of the second stage boost converter, respectively. As shown in Figure 7, a two-stage boost converter interconnects the PV arrays to the dc bus. The first-stage boost converter is intended to track the maximum power point of PV, while the second-stage boost converter is intended to regulate the dc

bus voltage. By the way of emulating the dc machine, the PV generation system has the similar output characteristic, and the PV output and dc bus voltage change more gently. The control method of virtual dc machine is introduced in Section 3. However, it needs to be emphasized that the way of providing inertia by adjusting the PV output power may cause the PV output to deviate from the maximum power point, resulting in energy waste.

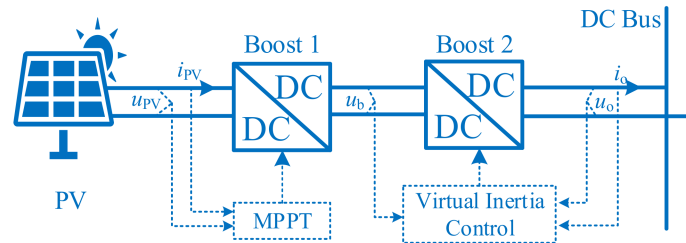


Figure 7. Schematic diagram of PV system in dc MGs.

In addition to the abovementioned inertia-providing sources, the dc MGs working at the grid-connected mode can also obtain inertia support from the utility grid. Considering that frequent charging/discharging may damage the life cycle of a battery, a power-management strategy based on virtual inertia is presented [67]. In Reference [67], the ac-grid-connected inverter is designed to maintain the dc bus voltage, and a utility grid, instead of a battery, provides the inertia support. This method can reduce the burden of the battery and prolong its life cycle. Moreover, some motor loads can also provide rotational inertia, but related research is limited. We discuss it in Section 4.

As noticed, all the inertia enhancement methods rely on the control strategies of the connected power electronics converters (e.g., dc/dc converter and dc/ac converter); hence, the principle and classification of inertia control is discussed next.

2.3. Classification: According to Inertia Control Method

The inertia-control method can be mainly divided into two types: direct inertia control (DIC) and virtual inertia control (VIC). The detailed classification is shown in Figure 8.

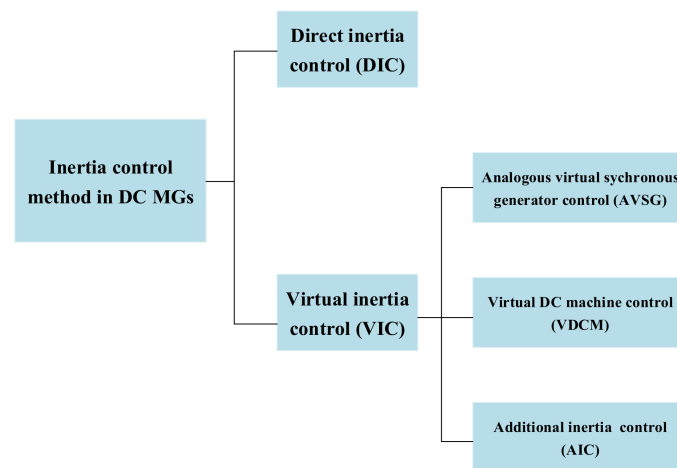


Figure 8. Classification of inertia control methods.

DIC refers to providing inertia by controlling traditional rotating-power-generation equipment such as SGs [47]. However, the modern power system is experiencing the transition from a rotational-generator-dominated system to a power-electronics-converter-dominated system, and SGs is gradually replaced by RESs. Thus, DIC is not the main research focus in this article.

VIC refers to providing virtual inertia by controlling power electronics converters connected to RESs or ESS, which belong to non-rotating stationary equipment and have no ability to directly provide inertia. VIC is the main focus of this article.

To further explain the meaning of virtual inertia control, the dc-side equivalent circuit of the converter in dc MGs is depicted in Figure 9. Similar to the previous one in Figure 2, P_{in} denotes the output power of the converter, P_{out} denotes the power flowing to the dc bus, P_c denotes the power charging of the capacitor, and C denotes the dc-link capacitance. Different from the voltage and current double-loop control used in the converter in Figure 2, the converter in Figure 9 adopts virtual inertia control, so ΔP_v denotes the extra output power by the converter using virtual inertia control.

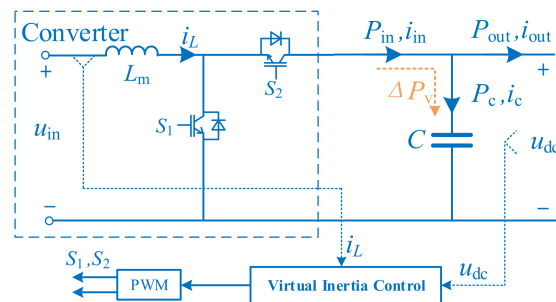


Figure 9. Power relationship of dc-link capacitor side, considering the virtual inertia control.

The formula of power balance on both sides of the dc-link capacitor based on Equation (5) can be obtained as follows:

$$P_{in} + \Delta P_v - P_{out} = (C + C_v) \cdot u_{dc} \cdot \frac{du_{dc}}{dt} \quad (8)$$

where C_v is the value of virtual capacitance. It can be known from (8) that extra power output by controlling the converter is equivalent to virtualizing a larger capacitance in the dc-link, and the time constant of inertia in dc MGs which characterizes the inertia of the system is correspondingly enhanced, just as (9) expresses:

$$H_{dc} = \frac{W_{cap}}{S_{Nc}} = \frac{1}{2} \frac{(C + C_{vir})u_{dc}^2}{S_{Nc}} \quad (9)$$

To sum up, the virtual inertia control essentially allows converters to generate extra power output and virtualizes a larger dc-link capacitance, thus enhancing the system inertia. The detailed classification and introduction of each type of VIC is give in the next section.

3. Virtual Inertia Control Methods

In this section, the various VIC methods are introduced, and the strengths and weaknesses of these methods are also discussed and compared.

3.1. Additional Inertia Control

The additional inertia control (AIC) mainly refers to improving the traditional converter control method to output additional virtual power. More specifically, the AIC enables the converter to deliver additional power by coupling the virtual capacitor into the conventional converter control loop [32,48,49,55,68,69]. Therefore, the converter has the ability to provide inertia, while still retaining the original output characteristics.

Currently, the control architecture in dc distribution network (DN) or MGs mainly can be divided into two types: the centralized control structure and the decentralized one [41–43]. For the centralized control structure, all the RESs and ESS interface converters receive the unified dispatch from the central controller through communications. In Reference [70], a dc microgrid for data centers, using the centralized control framework, is

proposed, and the central control system coordinates the operation of converter used in the dc microgrid and ensures the stable operation of the system. However, as the core device for the centralized control structure, the central controller is vulnerable to physical attacks or cyber-attacks, leading to the communication breakdown of the entire dc system.

For the above considerations, the decentralized control is presented in which the converters in a dc system make actions via local voltage or current information. The droop control is the typical decentralized control scheme [42]. In this scheme, the dc bus voltage deviation is used to automatically achieve power sharing among converters. The droop control equation in dc network can be expressed as follows:

$$u_{dci} = u_{dc}^* - k_{di} \cdot i_{oi} \quad (10)$$

where u_{dci} denotes the output voltage of the i th converter ($i = 1, 2, 3, \dots$), u_{dc}^* denotes the reference dc bus voltage, k_{di} refers to the droop coefficient of the i th converter, and i_{oi} denotes the output current of the i th converter.

However, the droop control in dc systems just adjusts the deviation of the dc bus voltage from the primary regulation point of view and is not sensitive to the rate of change of voltage (RoCoV) and does not provide inertial support for the dc system. In order to enhance the system inertia, Reference [32] introduces the RoCoV into the droop control equation, and an improved droop control equation can be obtained as follows:

$$u_{dci} = u_{dc}^* - \frac{1}{k\left(\frac{du_{dc}}{dt}\right)} \cdot i_{oi} \quad (11)$$

where $k\left(\frac{du_{dc}}{dt}\right)$ indicates the function of RoCoV, and it is expressed as follows:

$$k\left(\frac{du_{dc}}{dt}\right) = k_1 \frac{du_{dc}}{dt} + k_2 \quad (12)$$

The reason for introducing RoCoV into the droop coefficient is that rapid power disturbances in dc systems manifest themselves as a large dc-bus-voltage change rate. It can be seen from (11) and (12) that the improved droop coefficient is negatively correlated with RoCoV. When the dc bus voltage remains constant, the RoCoV is equal to zero, and when the DC bus voltage fluctuates due to source-load fluctuations, the RoCoV (du_{dc}/dt) becomes large, and the droop coefficient becomes smaller; therefore, the interface converter will output a higher current to suppress the voltage fluctuation. Consequently, the interface converter adopting the improved droop-control strategy has the inertia-support capabilities.

Figure 10 presents the proposed improved droop-control architecture. It can be found that the battery energy storage is the inertia source, and its interface converter adopts the AIC. In the control architecture, the rate of change of the dc bus voltage is obtained through the high-pass filter (HPF) and used to calculate the droop coefficient; then the reference of the battery output current can be calculated by using the improved droop control. Finally, after the PI controller of inner current loop and PWM part, the switching-gate signals are generated and sent to the DC/DC converter.

Practically, the function of RoCoV is not linear as (11), because the inertia power is limited by the battery's energy-storage capacity, and correspondingly, the droop coefficient also has the limitations. In Reference [32], the actual droop coefficient is designed to achieve the following:

$$k\left(\frac{du_{dc}}{dt}\right) = \begin{cases} \frac{k_{2max}}{0.5\pi \arctan\left(1 - \frac{du_{dc}}{dt}\right)^2} & \frac{du_{dc}}{dt} \geq 0 \\ \frac{k_2 - k_{min}}{0.5\pi \arctan\left(1 - \frac{du_{dc}}{dt}\right)^2} & \frac{du_{dc}}{dt} < 0 \end{cases} \quad (13)$$

where k_{max} and k_{min} are the limits of the droop coefficient. By designing the droop coefficient as an inverse tangent function about RoCoV, it prevents the droop coefficient from crossing the limit and ensures the stability of the system; at the same time, it makes full

and reasonable use of the inertia margin of the converter, i.e., the variation margin of the droop coefficient, so that the battery can provide a large inertia for the system whether the load fluctuation is large or small. The advantage of this architecture is that the control principle is simple and inertia support can be provided quickly, but the disadvantage is that the voltage differential term is introduced, resulting in a high-frequency disturbance to the control system.

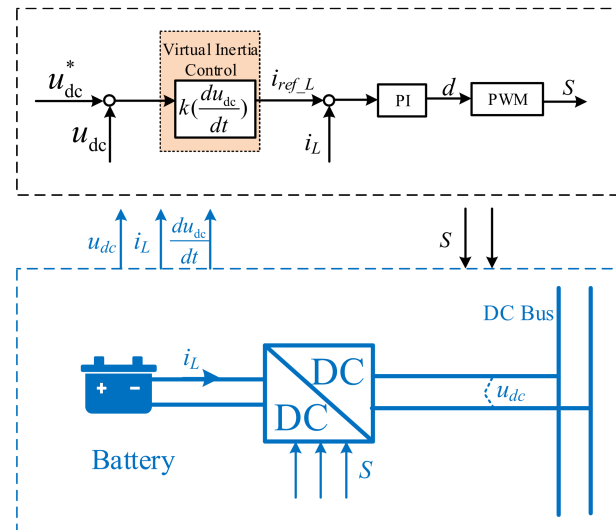


Figure 10. Control architecture for proposed AIC in Reference [32].

In addition to designing the droop factor as the reciprocal of the inverse tangent function, Reference [55] gives another form. It is designed as a power function:

$$u_{dci} = u_{dc}^* - k'_{di} \cdot i_{oi} \tag{14}$$

$$k'_{di} = \begin{cases} k_{di} - m_1 \left(\left| \frac{du_{dc}}{dt} \right| \right)^{m_2}, & \left| \frac{du_{dc}}{dt} \right| \geq C_{set} \\ k_{di}, & \left| \frac{du_{dc}}{dt} \right| < C_{set} \end{cases} \tag{15}$$

where k_{di} refers to the initial droop coefficient, m_1 and m_2 are the constants controlling the strength of inertia, and C_{set} is the threshold for deciding whether to activate AIC. The meaning of Equation (15) is that, when the dc-bus voltage-change rate exceeds the set threshold, the droop coefficient of the converter’s droop control will be correspondingly reduced according to Equation (15), so that the converter can output more power to provide inertia support. It can be seen that values of parameters m_1 and m_2 in Equation (15) can affect the system inertia. The simulation results of the droop coefficients for different values of m_1 and m_2 show that the larger value of m_1 and smaller value of m_2 can enhance the inertia support capacity of the converter. Similarly, the range of values for m_1 and m_2 is determined according to the converter’s capacity and system’s stability margins.

Figure 11 presents the proposed control architecture in Reference [55]. The control scheme is similar to that of Reference [32], but the difference is that a model predictive controller is used to substitute the PI controller of inner current loop. It can effectively avoid the hysteresis of traditional PI-based internal loop current regulation and is suitable for applications requiring fast control, such as virtual inertia control. However, this method still needs to introduce the voltage change rate, so there is still the problem of high-frequency disturbance.

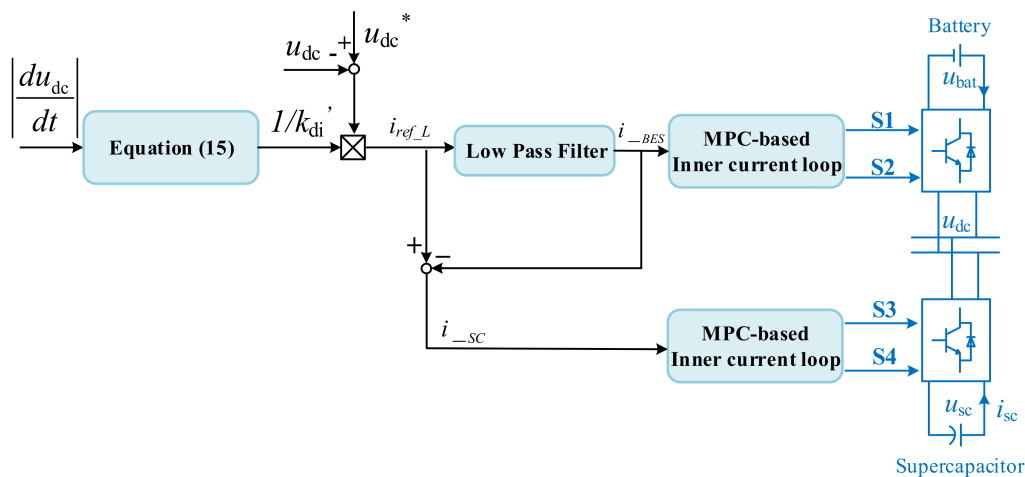


Figure 11. Control architecture for proposed AIC in Reference [55].

Moreover, a hybrid energy storage system (HES) is used. The HES is composed of battery energy storage (BES) and supercapacitor (SC). In terms of control strategy, except for the power-distribution scheme, the SC interface converter is controlled in the same way as the BES interface converter. The control block diagram is also depicted in Figure 11. The filtered high-frequency current reference is sent to the model predictive control (MPC)-based inner loop of SC interface converter, while the low-frequency current reference is sent to the MPC-based inner loop of BES interface converter.

Although the above methods of changing the droop coefficient improve the stability of the dc bus voltage, they still have drawbacks, such as complex control algorithms, output power that tends to exceed limits, transient response times that are too long, etc. In addition to changing the droop coefficient, virtual inertial control can also be achieved by adjusting the droop curve intercept [68]. The expression for the adjustment of the longitudinal intercept of the droop curve consists of a nested power function and inverse tangent function. The proposed method has a simpler control structure and reduces transient response time, but it does not comprehensively consider the operational boundaries of constraints such as stability, dynamic performance, and feasibility.

Instead of adjusting the droop curve to enhance the inertia, Reference [69] proposes a new control scheme to output extra power. The voltage fluctuation signal is introduced into the control loop of the BES interface converter, and the current reference value is compensated and changed by additional flexible virtual inertia control, so that the output power of the BES changes rapidly, thereby giving the power grid a certain inertia support. In Reference [48], a similar approach is applied to the grid-connected inverter in dc MGs, considering that the utility grid, as a large power source, can quickly provide sufficient inertial power. Reference [49] connects the maximum power-tracking coefficient of wind turbines and the virtual capacitance and adds the AIC part in to the MPPT control of wind turbines' interface converter.

The AIC essentially couples the virtual capacitor into the original control loop of converters, changing the current reference value of the inner current loop. In general, these AIC methods have simple principles and are easy to implement. However, these methods all inevitably depend on the differential term of the dc voltage, thereby introducing additional high-frequency disturbances and coupling terms to the control loop, weakening the ability to release virtual inertia, and making the overall control structure complex and lacking in generality.

3.2. Analogous Virtual Synchronous Generator Control

In the ac grids, the virtual synchronous generator (VSG) control has been widely studied and applied. The VSG technology changes the inverter control strategy by introducing virtual inertia and damping into the control system, so that the output characteristics of

the inverter are similar to those of the synchronous generator; thereby, the system inertia can be enhanced. Zhong et al. [26] first proposed the concept of synchronverters; the mechanical and electromagnetic properties of the synchronous machine are emulated in the well-established converter control algorithm. Furthermore, Zhong et al. [71] improved the control strategy and developed a self-synchronized synchronverter without a dedicated synchronization unit, such as a phase-locked loop (PLL). References [72,73] compare and analyze the dynamic characteristics of both the traditional droop control and VSG control, and Reference [72] proposes a generalized droop control for the grid-supporting inverter, which combines the advantages of both and can adapt to different requirements.

At present, the VSG technology in the ac system has been widely and deeply studied, and many scholars have begun to apply it to the dc system. The design of the VSG topology used in dc grids mimics the one in the ac system, whose swing equation [73] is expressed as follows:

$$\begin{cases} P_{in} - P_o - P_D = J \cdot \omega_0 \cdot \frac{d(\omega - \omega_0)}{dt} \\ P_D = D \cdot (\omega - \omega_0) \end{cases} \quad (16)$$

where P_{in} represents the active power reference of the inverter, P_o represents the output power of the inverter, P_D describes the damping effect of the VSG, D is the damping factor, ω is the reference angular frequency generated by VSG, ω_0 is the nominal angular frequency of the ac grids, and J is the moment of inertia.

As discussed in Section 2.1, the inertia in the dc system manifests the ability to mitigate the sudden change of the dc voltage, and there are many variables that correspond to each other between both the VSG topology in the ac system and dc system. The similarities [74,75] are listed in Table 1.

Table 1. VSG analogy between ac system and dc system.

Variables	VSG in AC System	VSG in DC System
Droop relation	$\omega - P_o$	$u_{dc} - i_o$
Control objects	ω	u_{dc}
Output	P_o	i_o
Inertia	J	C_{vir}
Storage energy	$\frac{1}{2}J\omega^2$	$\frac{1}{2}C_{vir}u_{dc}^2$

According to Table 1, Reference [74] substituted the dc bus voltage for angular frequency in ac grids and proposed an analogous virtual synchronous generator (AVSG) control for a bi-directional grid-connected inverter (BGI) in the dc MG. The dc virtual inertia control equation is expressed as follows:

$$\begin{cases} i_{in} - i_o - i_D = C_{vir} \cdot u_{dc}^* \cdot \frac{d(u_{dc} - u_{dc}^*)}{dt} \\ i_D = D_d \cdot (u_{dc} - u_{dc}^*) \end{cases} \quad (17)$$

where i_{in} represents the output current reference of the inverter; i_o represents the actual output current of the inverter; i_D describes the damping effect of the AVSG; D_d is the damping factor; u_{dc} is the reference dc bus voltage generated by AVSG; u_{dc}^* is the nominal dc bus voltage; and C_{vir} is the virtual capacitance, analogizing with J in (16).

Figure 12 presents the proposed AVSG control architecture in Reference [75] that consists of the AVSG control, the voltage and current dual-loop control, and the current feed-forward control. The AVSG control part mimics the dynamic characteristic of the synchronous generator and analogizes the VSG in the ac system. The output of this part is the error between dc bus voltage and its reference value, $(u_{dc} - u_{dc}^*)$. The voltage and current dual loop receive the error signal between the dc-voltage reference value and actual value and output the duty cycle. The current outer loop is designed with a PI controller to track the dc-bus voltage reference, and the current inner loop is designed with decoupling of grid-connected current components in the d -axis and q -axis. Moreover, i_d^* is the grid-

connected current reference in the d -axis, which is set to zero because the BGI only transmits active current; i_q^* is the grid-connected current reference in q -axis; and i_{dq} and u_{dq} are the grid-connected current and voltage in the d - q coordinate system, respectively.

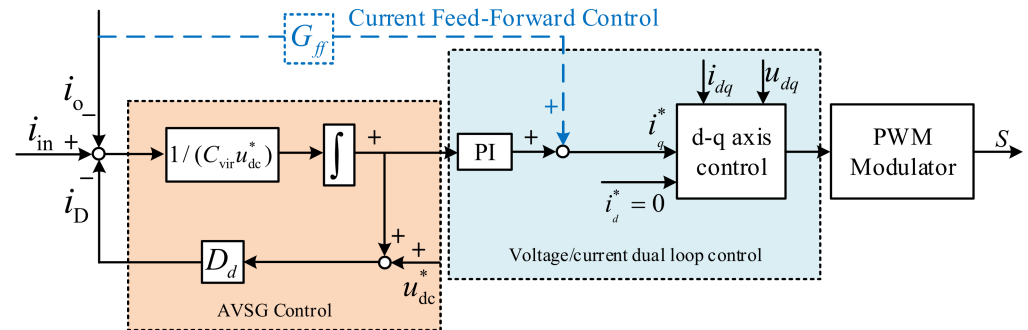


Figure 12. Control architecture for proposed AVSG in Reference [75].

In References [74,75], the small signal model of the proposed AVSG control is established, and the transfer function of output voltage to output current, as well as the step response, can be obtained. However, it was found that the dc bus voltage at the initial stage of the step response will experience an impulsive process of change after the sudden change of system power demand. An output current feed-forward control part is developed to compensate for the impulse component of the step response; thereby, the dynamic characteristics of the control system are improved.

However, there are limitations in the research work in References [74,75]. The proposed VIC for the BGI can be used only when dc MG is working in the grid-connected mode. When the dc MG operates in off-grid mode, the BGI will not be able to provide inertial support. In this case, it is difficult to ensure the safety and stability of the dc MG. In order to enhance the inertia of the dc MGs in both the grid-connected and off-grid mode, References [76,77] propose a similar AVSG control, which is used in the bi-directional dc/dc converter for the BES. The energy storage could provide inertia support regardless of the operating conditions of the dc MGs. The control block diagram has the same AVSG and current feed-forward parts as that in Reference [74], while in the voltage/current dual-loop control part, the inner current loop uses a PI controller.

By analogy with the VSG method used in ac system, the above AVSG methods applied to dc system exhibit good suppression of voltage surges. Zhu et al. [78] compares the AVSG control in Reference [76] with the AIC method in Reference [69]. The results show that, compared to the AIC method that introduces a voltage differential link, the AVSG control has better dynamic characteristics, and its stability is less affected by changes in control parameters. The AVSG control has good robustness.

Moreover, the influence of different types of VIC on dc MGs is also analyzed. The equivalent impedance model of the dc MGs is established, and the influence of changes in parameters such as virtual inertia coefficient (C_{vir}), dc bus capacitance, and load power on the system stability is studied according to the Middlebrook criterion. The conclusion is that, although the VIC method, similar to AVSG, can enhance the system inertia and improve the voltage quality, a large C_{vir} will reduce the system's stability margin. The stability analysis provides a basis for the selection of controller and system parameters.

Aiming at further improving the inertial response capability of the system, a flexible virtual inertia control method is presented by Reference [79]. Based on the AVSG control structure, the virtual capacitance can be flexibly adjusted and expressed as follows:

$$C_{vir} = \begin{cases} C_{v0} & \left| \frac{du_{dc}}{dt} \right| < M \\ C_{v0} + k_1 \left(\left| \frac{du_{dc}}{dt} \right| \right)^{k_2} & \left| \frac{du_{dc}}{dt} \right| \geq M \end{cases} \quad (18)$$

where C_{v0} is the virtual inertia coefficient when the system is in steady state, M is the set critical value of the dc bus voltage change rate, and k_1 and k_2 are the relevant parameters to adjust the virtual inertia coefficient. Theoretically, the larger the virtual inertia coefficient, the stronger the system inertia, so once the voltage mutation is detected and the dc bus voltage change rate becomes larger than M , the virtual inertia coefficient will be changed to a larger value; thus, the proposed AVSG control is more flexible.

The problem with this approach is that the introduction of the voltage differential term will bring the interference of high-frequency noise to the control system. In addition, an excessive virtual inertia coefficient is known to affect the system stability; thus, the setting of k_1 and k_2 is particularly important, adding difficulty to the design of the control system. Although the above AVSG control methods can mitigate the voltage mutation, the speed of the voltage recovery stage is also slowed down. Therefore, Reference [80] improves the approach for the above problems and proposes an adaptive AVSG method. In Reference [80], C_{vir} is designed as a function in relation to the change rate of dc bus voltage and the voltage deviation. The design principle of C_{vir} can be illustrated in Figure 13.

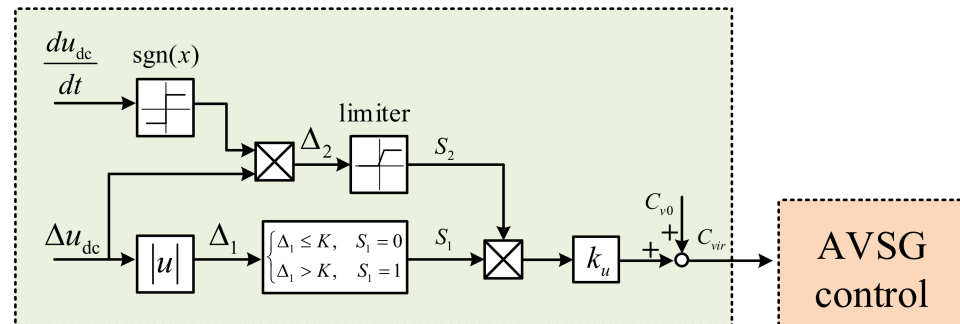


Figure 13. Control architecture for improved AVSG proposed in Reference [80].

In Figure 13, the $\text{sgn}(x)$ is a symbolic function, the output 1 means the input is greater than zero, and the output 0 means the input is less than zero. The symbolic function can reduce the amount of computation and also avoid system noise brought by the differential term. Based on Figure 13, the expression of C_{vir} is as follows:

$$C_{vir} = \begin{cases} C_{v0} & \Delta_1 \leq K, \Delta_1 > K \& \Delta_2 \leq 0 \\ C_{v0} + k_u S_2 & \Delta_1 > K \& \Delta_2 > 0 \end{cases} \quad (19)$$

where K is the set critical value of the dc bus voltage deviation, and k_u is the relevant parameters to adjust C_{vir} . According to (18), when the sudden voltage change occurs, the voltage-change rate and the deviation have the same mathematics symbol, so C_{vir} is changed from its initial value of C_{v0} to a large value, meaning that system inertia is enhanced; when the dc bus voltage begins to recover, the voltage-change rate and the deviation have the opposite mathematics symbol, so C_{vir} is changed to its initial value of C_{v0} , meaning that system inertia is weakened, and the voltage recovers faster.

In recent research, some scholars combine the intelligent control, the intelligent optimization algorithm, with AVSG control to improve its performance. In Reference [81], a new adaptive virtual inertia control method is proposed that uses a fuzzy logic control (FLC) algorithm to flexibly adjust C_{vir} . Compared to the method in References [74,76], the state of charge (SOC) of the battery energy storage is also considered in the FLC algorithm to ensure the safety of BES. The feasibility is verified by simulation. References [82,83] propose an MPC-controlled AVSG approach. By comprehensively considering the allowed range rate of the dc-link voltage as the constraint and considering the output of VIC and voltage deviations as the optimization objectives, a prediction model is established. The predictive model can predict the optimal compensation current to change the input current reference of the AVSG controller; therefore, the system inertia is greatly enhanced. Mean-

while, some research has begun to focus on the design and simplification of the AVSG control models. More in-depth research for AVSG control is still needed.

3.3. Virtual DC Machine Control

Virtual dc machine (VDCM) control is a relatively emerging virtual inertial control method which mimics the characteristics of the dc machines to strengthen the dc system's inertia. References [84–86] propose the VDCM concept, which is used in the bidirectional buck/boost converter. Reference [66] proposes the VDMC approach and applies it to boost the converter on the PV side. References [87,88] use VSG control and VDMC control on the ac and ac sides of the energy router, respectively, and a similar idea also appears in Reference [89], where the control method is applied to the cascaded power electronic converters. It is proved that VDCM can simulate the inertia and damping of the traditional ac power grid in the dc system. However, there are few works from the literature that involve a detailed analysis of the working principle and mechanism of VDCM, and none of the abovementioned works analyze the dynamic response of VDMC and the influence of parameter design on system stability.

An analogy is made to the key parameters of dc machine and VDCM in References [90–92]. Accordingly, the dc machine model is shown in Figure 14a.

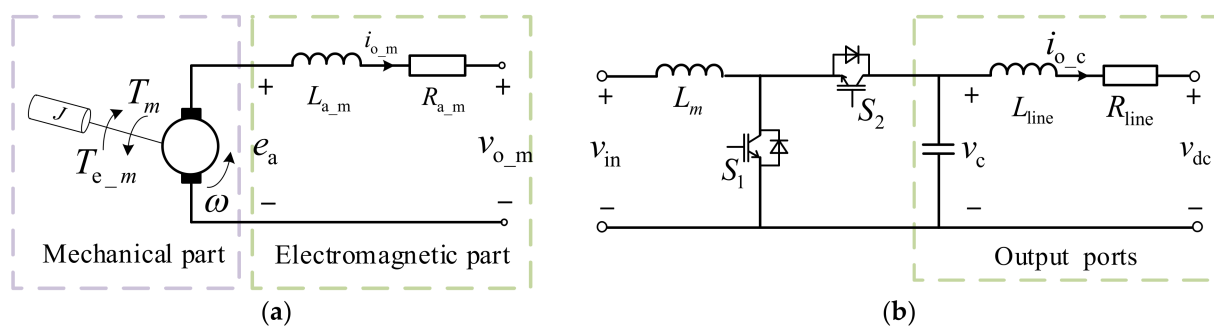


Figure 14. Analogy between dc machine and dc/dc converter: (a) The dc machine equivalent circuit; (b) The non-isolated bi-directional dc/dc converter circuit.

From the equivalent circuit of the dc machine in Figure 14a, the equivalent equation in the electromagnetic part is calculated as follows:

$$\begin{cases} v_{o_m} = e_a - R_{a_m} \cdot i_{o_m} - L_{a_m} \cdot \frac{di_{o_m}}{dt} \\ e_a = C_T \phi \omega \end{cases} \quad (20)$$

where e_a is the induced electric potential of the dc machine; v_{o_m} and i_{o_m} are the output voltage and output current of the armature winding, respectively; R_{a_m} and L_{a_m} are the armature resistance and armature inductance, respectively; C_T and ϕ are the torque coefficient and magnetic flux, respectively; and ω is the rotor speed.

As the typical dc/dc converter topology, the non-isolated bi-directional dc/dc converter is shown in Figure 14b, and the equivalent circuit equation can be expressed as follows:

$$v_{dc} = v_c - R_{line} \cdot i_{o_c} - L_{line} \cdot \frac{di_{o_c}}{dt} \quad (21)$$

where v_c is the voltage on the output capacitor of the dc/dc converter; v_{dc} and i_{o_c} are the dc bus voltage and the output current of the converter, respectively; and R_{line} and L_{line} are the line resistance and line inductance, respectively.

It can be known from (20) and (21) that the output circuit of the dc/dc converter can be completely equivalent to the armature of the dc machine. Specifically, the line impedance in the output ports of the converter corresponds to the armature winding of the dc machine, and the output voltage of the converter corresponds to the induced electric potential of the

dc machine, so as to realize the complete equivalent process of the output circuit of the dc/dc converter and the electromagnetic part of the dc machine. Note that the topology of the non-isolated bi-directional dc/dc converter is selected to be analogous to the dc machine. The equivalent model of the output ports is not affected by changes in the internal topology of the converter. Therefore, the equivalent model can be also suitable for other converter topologies, such as the dual active bridge (DAB) dc/dc converter, LLC converter, CLLC converter, and so on.

Moreover, considering that the converter does not have inertial support capability, the mechanical motion equation of the dc machine should also be added to the control system. The power-balance equation for the dc machine’s mechanical part [90] is as follows:

$$\begin{cases} P_m - P_{e_m} - \frac{\omega_{o_m} D_m}{C_T \varphi} (e_a - v_{o_m}^*) = \frac{J_m \omega_{o_m}}{C_T \varphi} \frac{d(e_a - v_{o_m}^*)}{dt} \\ P_{dm} = \frac{\omega_{o_m} D_m}{C_T \varphi} (e_a - v_{o_m}^*) \\ P_{iner_m} = \frac{J_m \omega_{o_m}}{C_T \varphi} \frac{d(e_a - v_{o_m}^*)}{dt} \\ P_{e_m} = \omega T_{e_m} \approx \omega_{o_m} T_{e_m} \end{cases} \quad (22)$$

where P_m represents the mechanical power; P_{e_m} is the electromagnetic power of the dc machine, which can be expressed by the product of angular velocity, ω , and torque, T_{e_m} ; P_{dm} describes the damping effect of the dc machine, in which D_m is the damping coefficient and ω_{o_m} and $v_{o_m}^*$ are the nominal rotor speed and the nominal output voltage of the dc machine, respectively; P_{iner_m} is the inertia power; and J_m is the moment of inertia.

By combining the previous analogy, the VDCM control equation can be obtained as follows:

$$\begin{cases} P_{vm} - P_e - \frac{\omega_{on} D_{damp}}{C_T \varphi} (v_{vir} - v_{dc}^*) = \frac{J_{vir} \omega_{on}}{C_T \varphi} \frac{d(v_{vir} - v_{dc}^*)}{dt} \\ P_{vd} = \frac{\omega_{on} D_{damp}}{C_T \varphi} (v_{vir} - v_{dc}^*) \\ P_{vi} = \frac{J_{vir} \omega_{on}}{C_T \varphi} \frac{d(v_{vir} - v_{dc}^*)}{dt} \end{cases} \quad (23)$$

$$\begin{cases} P_e = \omega_{vir} T_e = v_{dc} i_{o_c} \\ v_{dc} = v_{vir} - R_a i_{o_c} \\ v_{vir} = C_T \varphi \omega_{vir} \end{cases} \quad (24)$$

where P_{vm} , P_e , P_{vd} , and P_{vi} represent the input power of the converter, the output electromagnetic power of the converter, the virtual damping power, and virtual inertia power of the converter, respectively; and v_{vir} denotes the virtual induced electric potential in VDCM control, which is also the output voltage v_c in Figure 14a, emulating e_a in the dc machine. Moreover, v_{dc}^* is the nominal dc voltage, ω_{vir} is the virtual angular frequency, J_{vir} is the virtual moment of inertia, and R_a is an adjustable parameter to mimic the armature winding resistance of the dc machine. The relationship between the mechanical motion equation in (23) and the output equivalent circuit in (21) manifests in (24). The detailed analogy between dc machine and VDCM control is listed in Table 2.

Table 2. Analogy between dc machine and VDCM control.

Variables	DC Machine	VDCM Control
Mechanical power	Primary motor	ESS/RES (PV, Wind turbine)
Inertia	J_m from rotor	J_{vir} from virtual inertia
Induced electric potential	e_a	v_{vir}
Output voltage	v_{o_m}	v_{dc}
Angular frequency	ω	ω_{vir}
Armature winding	R_{a_m}, L_{a_m}	R_a, L_a
Storage energy	$\frac{1}{2} J_m \omega^2$	$\frac{1}{2} J_{vir} \omega_{vir}^2$

Combine (22) and (23) and Table 2, and the virtual capacitance can be calculated by using the following equation:

$$\frac{1}{2}C_{vir}v_{vir}^2 = \frac{1}{2}J_{vir}\omega_{vir}^2 = \frac{1}{2}J_{vir}\left(\frac{v_{vir}}{C_T\phi}\right)^2 \tag{25}$$

$$C_{vir} = \frac{J_{vir}}{(C_T\phi)^2} \tag{26}$$

The control block diagram of VDCM is shown in Figure 15. The control structure proposed in Reference [90] can be mainly divided into three parts: the voltage outer loop, the VDCM control loop, and the current inner loop. In the voltage loop, a PI controller, $G_v(s)$, is utilized to track the reference dc voltage so that the steady-state voltage error can be eliminated. The purpose of the PI controller $G_i(s)$ in current loop is the same and to track the input current of the converter. The VDCM controller consists of the mechanical part and the electromagnetic part. By emulating the dynamic characteristics of dc machine, the dc/dc converter with VDCM control can improve the dynamic stability of dc system voltages.

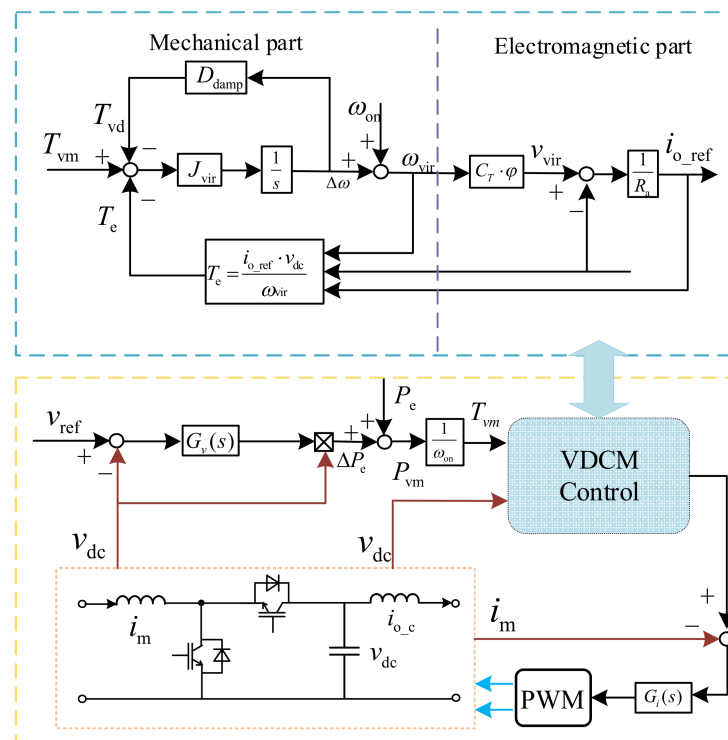


Figure 15. Control architecture for proposed VDCM control in Reference [90].

In Reference [90], the small signal model of the VDCM control is established, and its dynamic characteristic is also analyzed. The original small signal model is a fifth-order system, so it is hard to select the parameters such as J_{vir} and D_{damp} . To facilitate the analysis of the established model, a second-order simplified model is introduced by using the dominant poles. The second-order improved system can simplify the parameters' design process, and the parameters in VDCM control are also optimized.

The proposed VDCM can mimic the dynamic characteristics of the dc machine well, and the response speed is fast. However, the adopted VDCM control model in Reference [90] is not perfect, and its dynamic response should be improved. Reference [93] compares a non-isolated bidirectional dc/c converter to a dc machine, and a new VDCM control block diagram is designed. Since the topology of the dc/dc converter is specifically considered in the modeling process, the small signal model is further simplified into a first-order system, and the improved VDCM control has more superior voltage dynamic

performance and inertial support capability. Additionally, the problem of low sensitivity of the designed control system parameters has also been solved. The limitation of this method is that it relies on the internal topology of the converter, so it is not universal.

In addition, some scholars combine VDCM control with other traditional control methods, such as droop control. A VDCM control strategy based on the P-U droop characteristics is proposed in Reference [91]. The proposed method makes an analogy to the typical P-U droop control equation in the dc system and the mechanical inertia characteristics of the dc machine, by which the P-U droop control loop with VDCM is designed. Reference [92] adds the SOC of the distributed ESS to the armature resistance and proposes an improved VDCM control strategy, in which the dynamic power sharing, as well as the SOC balance, can be achieved. Reference [94] proposes a similar control method for SOC balance, which is also adapted to the dc MGs with multiple ESSs of different capacities. According to the current research, VDCM control has great potential to provide inertia support for dc system, but the current VDMC has not yet entered the stage of promotion and use, and its control structure needs to be further improved.

3.4. Comprehensive Comparison

In this section, the virtual inertia control methods for power electronics converters are classified and detailed. The respective advantages and disadvantages of these different types of control methods are summarized in Table 3. It can be seen that each type of virtual inertia control has its strengths and weakness. The AIC control is easy to implement, but the introduced high-frequency disturbance may lead to stability issues. At present, AVSG is more mature in terms of technical implementation, so it has the most potential in large-scale application. In theory, VDCM has better characteristics and is more suitable for a dc system, but it needs to be further simplified in terms of its modeling and control-system design. Therefore, a reasonable virtual inertial control method should be selected according to the actual needs.

Table 3. Summary of virtual inertia control methods.

Control Technique	Advantages	Disadvantages
AIC	<ul style="list-style-type: none"> • Simple control system design • Communication-less (for droop-based method) 	<ul style="list-style-type: none"> • High-frequency disturbance • Difficulty in parameter tuning • Slow transient response and power-sharing problems
AVSG	<ul style="list-style-type: none"> • Emulating synchronous generator characteristic • Good dynamic performance • Quick inertia support 	<ul style="list-style-type: none"> • Poor robustness to topology internal parameter changes • Complex model design
VDCM	<ul style="list-style-type: none"> • Emulating dc machine characteristic • Better dynamic performance than AVSG • Quicker inertia support 	<ul style="list-style-type: none"> • More complex model and design • Difficulty in controlling • High requirements for hardware

4. Challenges and Future Research Direction

4.1. Improved Modeling of Virtual Inertia Control Methods

The current research on converter control methods for virtual inertia enhancement mainly focus on the design of the proposed control algorithms and specific implementation. However, these few works from the literature focus on the accurate system modeling analysis after applying these methods. In fact, refined modeling plays an important role in the transient process issues of the system. In addition to the value-range setting of the control system parameters [74], a refined modeling of the control system can be used to reveal the energy change of the system during the transient process. In Reference [95],

a systematical modeling method is proposed to calculate the grid frequency support effect and the transient energy demand of VSG. Based on the modeling and calculation algorithm, a VSG parameter optimization method is presented to minimize the transient energy demand of VSG, while meeting the frequency support requirements of the grid. This research idea can also be extended to the VIC control of dc MGs. Moreover, the improvement of the dynamic characteristics of the control strategy also depends on the small-signal modeling analysis of the algorithm and the system.

Therefore, more accurate modeling and transient process analysis of VIC methods will be one of the future research hotspots.

4.2. Influence of VIC on Multi-Machine Operation and System Stability

With the rise of RES penetration in the power system, the scale and number of RES units, as well as the ESS units, are also increasing, and this brings challenges to the multi-machine coordinated control and stable operation of the distribution network and microgrid. At present, the research on the system inertia enhancement method in the literature mostly takes a single converter as the control object to study its control strategy. However, the inertia of the current dc system is very low, and only relying on the virtual inertial control of a single inverter cannot provide sufficient inertial support for the dc system. Therefore, in the actual dc distribution network or dc MG scenarios, multiple converters are often involved in inertial support.

The advantage of using VIC for multi-machine control is that the characteristics of different types of inertial sources can be fully utilized to realize the complementarity of inertial supports. However, its shortcomings are also obvious: multi-machine operation greatly increases the complexity of system modeling and control system. The common issues caused by multi-parallel operation include the power-sharing and circulating-current problems. References [96–99] propose an adaptive virtual impedance-based VSG control approach to achieve the reactive proportional power sharing among multiple converters. However, most research on power sharing and circulating suppression focuses on ac systems, and such issues are rarely mentioned in articles on the virtual inertia control of dc systems. Moreover, the VIC for multi-parallel operation also adds difficulty to system-stability analyses [100,101]. In future research, it is necessary to further explore the detailed interaction mechanism of different types and quantities of inertia sources, including RES and ESS units; optimize the multi-machine cooperative control strategy; and ensure the stable operation of the system.

4.3. Consideration about Influence of Load Side on System Inertia

Whether it is an ac system or a dc system, the previous research on system inertia enhancement methods has revolved around the source side, while ignoring the possible inertia in the load side. Actually, the dc machine is one of the dc loads, which could provide inertia support by mechanical energy on its rotor. The electric-vehicle-charging station likewise has the similar inertia support capability [102]. As the type and quantity of loads with inertia characteristics increase, it is necessary to deeply analyze the internal relationship between different types of loads and the inertia, so as to design suitable control strategies according to types of loads and improve the stability of the system.

5. Conclusions

This paper presented a comprehensive review of the inertia enhancement method on dc system under high-RES penetration. First, this paper introduced the definition and function of inertia in the dc system, and then the different types of inertia sources, including wind turbines, photovoltaic arrays, and energy storage were introduced. The principle by which these inertia sources provide inertia and the scenarios in which they are used were also analyzed. Since these inertial sources have to provide inertia by controlling the interface converter, the virtual inertial control methods were classified and discussed in detail. Among these control strategies, the AIC methods have simple principles and are

easy to implement, but they are susceptible to high-frequency noise. The AVSG control mimics the dynamic characteristic of the synchronous generator and has a good application prospect. The VDCM control method is an emerging technique, and it has great potential in inertia support. In the future, more research needs to be performed on improved schemes, refined modeling, multi-machine operation, and various load-side inertia controls. This review summarizes the research results of the inertia problem in the dc system and looks forward to the future research directions. It is expected to provide useful help and contribution to the development of the inertia emulating and enhancement technology in the dc power system.

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