

Article Highly Stable Inverted Organic Solar Cell Structure Using Three Efficient Electron Transport Layers

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Abstract: The efficiency of organic solar cells (OSCs) is influenced by various factors, among which environmental temperature plays a significant role. Previous studies have shown that the thermal stability of these cells can be enhanced by incorporating a third component into their structure. Ternary organic solar cells, particularly, have shown promising results in improving thermal stability. A well-designed electron transport layer (ETL) can significantly bolster thermal stability by facilitating efficient charge transport and reducing charge recombination. In this study, we investigated the effect of temperature, ranging from 300 K to 400 K, on the efficiency of inverted ternary structures by using a one-dimension optoelectronic model on "Oghma-Nano 8.0.034" software. The structures examined include (S1) "FTO/SnO₂/PM6:D18:L8-BO/PEDOT: PSS/Ag", (S2): "FTO/C₆₀/PM6:D18:L8-BO/PEDOT: PSS/Ag", and (S3): "FTO/PC₆₀BM/PM6:D18:L8-BO/ PEDOT: PSS/Ag". Simulations using three different ETLs—SnO₂, C₆₀, and PC₆₀BM—at 340 K (66.85 °C) resulted in a main effect on open circuit voltage (V_{oc}) and fill factor (FF) values, in addition to an important I_{sc} value in terms of thermally stable devices. However, these structures retained 92% of their initial ~20% efficiency observed at 300 K, demonstrating significant thermal stability under high power conversion efficiency (PCE) conditions.

Keywords: organic solar cells; ternary structures; operating temperature; electron transport layer; power conversion efficiency

1. Introduction

Currently, the international community faces a multifaceted global energy challenge, closely intertwined with the issue of global warming, primarily induced by greenhouse gasses, especially CO_2 , in the atmosphere [1–6]. Fossil fuels, as the principal sources of approximately 90% of CO_2 emissions and over 70% of all greenhouse gas emissions worldwide, are significant drivers of climate change [7–12]. These gasses trap solar heat, contributing to a rise in global temperatures [13–16].

Addressing global warming necessitates sustainable interventions, including reducing air pollution, which can be achieved through adopting renewable energy sources, minimizing waste, and conserving natural resources [13,17–19]. In this context, photovoltaic energy emerges as a sustainable and long-lasting solution that could mitigate the impacts of global warming [20–22]. OSCs, in particular, represent a promising alternative to traditional silicon-based panels due to their environmental benefits and cost efficiency in production [23]. Their widespread adoption could significantly enhance global solar energy utilization and reduce the ecological impacts of human activities [20]. Particularly, the role of OSCs in reducing global warming effects is increasingly recognized [23–25]. Although



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Copyright: © 2025 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https://creativecommons.org/ licenses/by/4.0/). their potential benefits are substantial, further research and development are critical to improve their efficiency and thermal stability [26–30].

The ETL is essential for determining the thermal stability of inverted ternary OSCs due to multiple factors [31]. An ETL that is well designed and has a consistent surface morphology can efficiently inhibit the penetration of moisture and prevent deterioration in high-humidity circumstances [31]. Effective engineering of the ETL can mitigate surface flaws, which have a negative impact on the performance and stability of the device.

Optimizing the ETL can improve the interfacial contact between the ETL and the organic active layer, leading to enhanced charge extraction and less recombination, hence enhancing thermal stability [32]. Modifying the ETL can drastically reduce the energy barrier at the ETL/organic contact, further increasing charge transmission and thermal stability [32].

Therefore, solution-processed ETLs, such as SnO₂ (Tin(IV)), have been studied for constructing high-performance inverted ternary OSCs with better thermal stability [33]. In addition, SnO₂ has an advantage in terms of high electron mobility of 2×10^{-9} m² v⁻¹ s⁻¹ and a high glass transition temperature (T_g) of 473–573 K, which suggests that this material can be an efficient ETL in OSC structures. Among efficient ETLs, we identify PC₆₀BM ([6,6]-Phenyl-C61-butyric acid methyl ester) for aiding the extraction of electrons from the photoactive layer to the ETL and electrode. The high electron mobility of PC₆₀BM, approximately 2×10^{-7} m² v⁻¹ s⁻¹, facilitates rapid electron movement, lowering the likelihood of recombination [34]. Furthermore, PC₆₀BM has a high T_g of 430 K, which leads to it being considered among stable fullerene derivative structures and commonly used in OSCs. Additionally, it plays a critical function in determining the architecture of the photoactive layer, producing a bicontinuous network with the donor polymer for effective charge transport [34].

Employing C₆₀ (Buckminsterfullerene) as an ETL in inverted ternary OSCs has notable advantages, including its exceptional electron mobility, which enhances efficient charge mobility [35]. Utilizing C₆₀ in OSCs can decrease charge recombination rates, leading to more efficient charge extraction and higher *PCE*. Additionally, using C₆₀ as an ETL in OSCs improves device stability by reducing voltage hysteresis compared to traditional ETLs [35]. According to some previous studies, C₆₀ has a notably good performance in terms of *PCE* because it has a high electron mobility of 1.2×10^{-7} m² v⁻¹ s⁻¹ and a T_g of over 400 K, which explain why it is advantageous in OSC structures. This stability is advantageous for maintaining long-term performance under operational circumstances, particularly in high-temperature environments [35,36]. ETLs based on C₆₀ can enhance the resilience of ternary OSCs against light-induced degradation. This enables OSCs to maintain their performance for longer durations, even when subjected to continuous illumination [35].

Improving the thermal stability of OSCs is vital for advancing their commercial viability and operational reliability. Ongoing research focuses on developing new materials and structures that optimize both *PCE* and thermal stability. Recent research by Zhang et al. [29] highlights the efficacy of the ternary blend approach in enhancing the thermal stability of OSCs, and it was found that the *PCE* of the device at 76.85 °C remained, on average, 80% of its PCE at room temperature. Key contributors to the charge-transfer state energy in these cells were identified as PBDB-T and IDT-PDOT-C6, with ITC6-2F playing a crucial role in facilitating charge carrier transfers to IDT-PDOT-C6. This mechanism promotes the generation of additional excitons reaching the donor/acceptor interface, thereby achieving high-efficiency photoelectric conversion [29]. Anass et al. [37] studied the influence of temperature on the performance of solar cells with [(Cbz-Mth)-B-T]2–PCBM as an active blend, and they found that their structure kept 89% of its room-temperature *PCE* at 76.85 °C. Another study conducted by Muhammad et al. [38] performed a simulation by stepping up the temperature from 300 K to 400 K to study the impact of raised temperatures on the efficiency of solar cells with PBDB-T: ITIC-OE as the photoactive layer, and they obtained a loss ratio of efficiency in a temperature range of 300–350 K of approximately 9%, which indicates a highly stable device. Moreover, the use of charge transport layers is paramount in improving the *PCE* of OSCs. These layers are essential for the efficient extraction and mobility of charges, while also preventing electron leakage, and are utilized extensively across various materials in inverted OSC architectures [39–43]. Previous studies conducted by Anass et al. [37] and Muhammad et al. [38] found that the best thermal stability achieved by OSCs at 350 K is 9%, but the PCEs achieved in these studies were 7.4% and 6.2%, respectively, which is relatively low. It is challenging to achieve high thermal stability and high *PCE* at the same time.

This study aims to explore how temperature variations affect the efficiency of inverted bulk heterojunction (BHJ) OSCs. Temperature variation significantly affects the efficiency of inverted bulk heterojunction OSCs through several mechanisms: as increased temperatures can enhance charge carrier mobility in inverted BHJ OSCs, they also lead to increased recombination rates, reduced V_{oc} , and potential material degradation. Understanding and optimizing these factors is essential for improving the thermal stability and efficiency of OSCs, especially for applications in diverse environmental conditions. Through simulations using three different electron transport layers—SnO₂, C₆₀, and PC₆₀BM—we seek to derive insights into the performance and thermal stability of these OSCs, specifically focusing on analyses of their high thermal stability (quantified by the *PCE* loss ratio) at 350 K under a high *PCE* of ~20%. This could potentially lead to the development of more efficient and stable OSCs in laboratory settings.

For the reader's knowledge, these are purely original structure models with a device area of 4.84 mm² that were designed and performed inspired by a previous study conducted by Zhu et al. [44]. Furthermore, we selected the ETLs SnO₂, C₆₀, and PC₆₀BM because of the benefits shown in other studies, as we mentioned previously in the Introduction section as well as Table 1, and due to their high electron mobilities of 2×10^{-9} m² v⁻¹ s⁻¹, 1.2×10^{-7} m² v⁻¹ s⁻¹, and 2×10^{-7} m² v⁻¹ s⁻¹, respectively; additionally, as shown in Table 1, these selected ETLs have a high T_g of 473–573 K, 393–403 K, and 430 K, respectively. The simulated device structures are mentioned in Figure 1.



Figure 1. Schematic illustration of the structures under light exposure: (**a**) S1 under light exposure with a device area of 4.84 mm²; (**b**) S2 under light exposure with a device area of 4.84 mm²; (**c**) S3 under light exposure with a device area of 4.84 mm².

Table 1. The *T*g and electron motilities of the ETLs.

Materials	<i>Т</i> _g (К)	Electron Mobility (m ² $v^{-1} s^{-1}$)
SnO ₂	473–573	$2 imes 10^{-9}$
C ₆₀	393-403	$1.2 imes10^{-7}$
PC ₆₀ BM	430	$2 imes 10^{-7}$

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2. The Simulation Model and Performance Indicators

The simulation employs Oghma-Nano software, which utilizes a one-dimensional drift diffusion model under standard conditions (with a temperature variation within the range of 300–400 K) for its electrical modeling. The model includes the solution of bi-polar drift diffusion, charge carrier continuity, Poisson equation, and Maxwell–Boltzmann equation free charge carrier statistics. The mathematical model can be found in the software's documentation for more detailed information [45].

The position variable in the simulation of OSCs utilizing one-dimensional models, such as Oghma-Nano, is integral to understanding the performance attributes of these solar cells. This variable facilitates the modification of layer thicknesses within the OSC architecture. Alterations in the thickness of the active layer can markedly affect charge generation and transport, thereby influencing key parameters such as Jsc and Voc. Furthermore, the position variable is instrumental in modeling the electric field across the various layers of the solar cell. A uniformly distributed electric field is vital for efficient charge separation and collection, which in turn directly affects the overall efficiency of the device, as determined by the Poisson equation within the simulation framework. By simulating various positions for charge carriers, including electrons and holes, researchers can evaluate the efficacy of carrier movement throughout the device. This analysis examines recombination rates and the thermal effects on electrons and holes, essential for enhancing device performance. Additionally, simulations can account for temperature variations across different positions within the solar cell, enabling an evaluation of how thermal influences affect performance metrics such as FF and PCE under realistic operating conditions. The simulated structure of the device is FTO/ETL/PM6:D18:L8-BO (Poly[(2,5-bis(2-ethylhexyl)-1,3,4-oxadiazole-2,5-diyl)-alt-(3-hexylthiophene-2,5-diyl)]:4,4'-(2,5-bis(2-ethylhexyl)-1,3,4-oxadiazole-2,5-diyl)bis(3hexylthiophene):2,2'-((2,5-bis(2-ethylhexyl)-1,3,4-oxadiazole-2,5-diyl)bis(3-hexylthiophene)) bis(benzo[1,2-b:4,5-b']dithiophene))/PEDOT: PSS (Poly(3,4-ethylenedioxythiophene) polystyrene sulfonate)/Ag (Silver), and the input parameters and thicknesses are included in Tables 1 and 2. The input electrical parameters include the average mobilities, the effective densities of free electrons and holes per position x, recombination rates, and other important parameters in the drift diffusion model and the charge carrier continuity equations to determine the J-V characteristics and other measurement outputs. In addition, the energy bandgap is considered 1.29 eV for the active blend PM6:D18:L8-BO (0.8:0.2:1.2). All these values' details are included in Tables 2 and 3.

Parameters	Values	Ref.
Electron mobility (μ_e)	$1.49 \times 10^{-7} \text{ m}^{-2} \text{V}^{-1} \text{s}^{-1}$	[44]
Hole mobility (μ_h)	$1.42 \times 10^{-7} \text{ m}^{-2} \text{V}^{-1} \text{s}^{-1}$	[44]
Effective density of free electron (N_c at 300 K)	$1 imes 10^{26}~\mathrm{m}^{-3}$	[44]
Effective density of free hole (N_v at 300 K)	$1 imes 10^{26}~\mathrm{m}^{-3}$	[44]
n to P recombination rate constant	$1.15 \times 10^{-17} \text{ m}^{-3} \text{s}^{-1}$	[44]
Free electron (<i>n</i>) to trapped electron (<i>strap</i>)	$1 \times 10^{-15} \text{ m}^{-2}$	[44]
Trapped electron (n_{trap}) to free hole (<i>P</i>)	$1 \times 10^{-20} \text{ m}^{-2}$	[44]
Trapped hole (P_{trap}) to free electron (n)	$1 \times 10^{-20} \text{ m}^{-2}$	[44]
Free hole (P) to trapped hole (P_{trap})	$1 \times 10^{-15} \text{ m}^{-2}$	[44]
Number of traps (N _t)	5 Traps	[44]

Table 2. Simulation parameters of Oghma-nano software.

Table 2. Cont.

Parameters	Values	Ref.
Energy bandgap (E_g)	1.29 eV	[44]
Relative permittivity (ε_r)	3.0 a.u	[44]

Table 3. The thicknesses of the materials used in the three structures.

Layer	Thickness (nm)	Materials	Туре
ITO	130	Oxides	Contact
SnO ₂	5	Oxides	ETL
C ₆₀	5	Small molecules	ETL
PC ₆₀ BM	5	Polymers	ETL
PM6:D18:L8-BO (0.8:0.2:1.2)	80	Blends	Active
PEDOT: PSS	20	Polymers	HTL
Ag	100	Metal	Contact

Figure 2a–c represent the energy levels of OSC structures S1, S2, and S3, respectively. All the ETLs in the different structures show cascaded energy levels in terms of the lowest unoccupied molecular orbital (*LUMO*) between the active layer of -4.2 eV and SnO₂, C₆₀, and PC₆₀BM, which are -4.7 eV, -4.1 eV, and -3.95 eV, respectively. The cascaded energy levels create more straightforward pathways in terms of electron and hole transport. PEDOT:PSS forms a close energy level in terms of the highest occupied molecular orbital (*HOMO*), with an active layer of -5.5 eV. An efficient electron transport further affects the V_{oc} and *FF* values as analyzed later in Section 3.



Figure 2. The energy levels of the materials used in the structures: (**a**) energy levels of S1; (**b**) energy levels of S2; (**c**) energy levels of S3.

The transfer of charges is determined by solving the drift diffusion equations that account for the movement of both electrons and holes. Bi-polar drift diffusion equations at position x for both electrons and holes are represented by Equations (1) and (2),

$$J_n = q\mu_e n_f \frac{\varphi}{\partial x} + qD_n \frac{\partial n}{\partial x} \tag{1}$$

$$J_p = q\mu_h p_f \frac{\partial \varphi}{\partial x} + qD_p \frac{\partial p}{\partial x}$$
(2)

where $J_{n,p}$ is the electron and hole current density, q is the elementary charge, μ_e , and μ_h are the mobilities of electrons and holes, respectively; D_n and D_p are the electron and hole diffusion coefficients, respectively; n is the density of electrons, and p is the density of holes. The conservation of the charge carriers is ensured by resolving the continuity of charge carrier equations as specified in Equations (3) and (4). The charge carrier continuity equations are shown in Equations (3) and (4),

$$\frac{\partial J_n}{\partial x} = q(R_n - G + \frac{\partial n}{\partial t}) \tag{3}$$

$$\frac{\partial J_p}{\partial x} = -q(\mathbf{R}_p - G + \frac{\partial p}{\partial t}) \tag{4}$$

where R_n and R_p are the recombination rates of electrons and holes, respectively; *G* is the generation rate.

The solution to Poisson's equation is used to determine the distribution of potential within the device, and it is represented as follows in Equation (5):

$$\frac{\partial \mathcal{E}0 \,\mathcal{E}r}{\partial x} \,\frac{\partial \varphi}{\partial x} = q(n-p) \tag{5}$$

where ε_0 , ε_r , the constants of permittivity in free space and relative permittivity, respectively, are constant; φ is the voltage profile. The model applies Maxwell–Boltzmann statistics to solve free carrier statistics as shown in Equations (6) and (7):

$$n = N_c exp\left(\frac{F_n - E_c}{K_B T}\right) \tag{6}$$

$$v = N_v exp\left(\frac{E_v - F_p}{K_B T}\right) \tag{7}$$

where N_c , N_v are the constants of the effective density of states in the conduction and valence band of the semiconductor; $F_{n,p}$ are constants of the energy level of the Fermi level in the valence and conduction band of the semiconductor; E_c is the conduction band, E_v is the valence band, K_B is the Boltzmann constant, and T is the temperature.

The boundary conditions between the layer interfaces are represented as the tunneling of electrons and holes through layer interfaces provided by Equations (8) and (9):

$$J_n = -qT_e \Big[\Big(n_1 - n_1^{eq} \Big) - \Big(n_0 - n_0^{eq} \Big) \Big]$$
(8)

$$J_p = -qT_h \Big[\Big(p_1 - p_1^{eq} \Big) - \Big(p_0 - p_0^{eq} \Big) \Big]$$
(9)

where T_e and T_h are the rate constants of tunneling of electrons and holes, respectively; $n_{0,1}$ is the number of electrons in the layers before and after the interface; $p_{0,1}$ is the number of holes before and after the interface; $n_{0,1}^{eq}$ is the equilibrium number of electrons in before and after the interface; $p_{0,1}^{eq}$ is the equilibrium number of holes in the layers before and after the interface; $p_{0,1}^{eq}$ is the equilibrium number of holes in the layers before and after the interface; $p_{0,1}^{eq}$ is the equilibrium number of holes in the layers before and after the interface.

More details of the above electrical model of our simulation can be found in Refs. [46–48]. OSC performance is commonly measured by several key metrics that directly influence their efficiency and thermal stability. These indicators are detailed below.

PCE is the ratio of the electrical output of the solar cell to the incident light energy.

$$PCE = \frac{P_{max}}{I_{in}} = \frac{J_{sc}V_{oc}FF}{I_{in}}$$
(10)

where P_{max} is the maximum power output by the OSC and I_{in} is the illumination intensity. The V_{oc} value represents the highest voltage when the current through the device is null, and it is represented as follows in Equation (11):

$$V_{oc} = \frac{E_g - \Delta E}{q} \tag{11}$$

where ΔE is the energy offset.

The generation of charge and the overall performance of OSCs are significantly influenced by the energy offset. This indicates the discrepancy in energy levels between the acceptor material's lowest unoccupied molecular orbital (LUMO) and the donor material's highest occupied molecular orbital (HOMO), as represented in Equations (12) and (13):

$$qV_{oc} = E_g - \Delta E \tag{12}$$

$$\Delta E = 2\left(E_{F,h} - E_{HOMO}^D\right) - K_B T ln\left(\frac{e}{h}\right)$$
(13)

where ΔE is the energy offset; $E_{F,h}$ is the energy corresponding fermi-level; E_{HOMO}^D is the electron donor energy. *FF* represents the maximum power output of the OSC, which is defined below in Equation (14):

$$FF = \frac{P_{max}}{V_{oc}J_{sc}} = \frac{V_m}{V_{oc}} \left[1 - e^{\frac{q}{AkT}(V_m - V_{oc})} \right]$$
(14)

where P_{max} is the maximum power delivered by the OSC, V_m is the maximum voltage, and A is the ideality factor of the semiconductor. Equation 15 describes the current–voltage (J-V) characteristics of OSCs.

$$J(\mathbf{V}) = J_0 \left[\exp\left(\frac{\mathbf{eV}}{\mathbf{A}\mathbf{K}\mathbf{T}}\right) - 1 \right] + J_{\mathrm{ph}}$$
(15)

where J_0 is the reverse saturation current density, *e* is the charge of an electron, and J_{ph} is the photo-current density.

3. Results and Discussion

The density of *J*-*V* was simulated under AM 1.5 G illumination with an intensity of 100 mW cm⁻² and a temperature range between 300 K and 400 K. S1 showed the best *PCE* of 20.08% at 300 K with a J_{sc} of 27.4 mA cm⁻², a V_{oc} of 0.89 V, and an *FF* of 82.2%, while the performance relatively decreased between the range of 310 and 400 K to reach a *PCE* of 15.53%, a J_{sc} of 27.35 mAcm⁻², a V_{oc} of 0.73 V, and an *FF* of 77.7% at 400 K. As mentioned, the major parameters that cause a reduction in the efficiency of OSCs during the enhancement of temperature are V_{oc} and *FF* (see Figures 3a and 4b,c).

The increase in temperature causes an augmentation of energy loss (ΔE) as demonstrated by Equation (13), and then V_{oc} decreases as a result of that augmentation. *FF* showed a negative correlation with temperature, which decreased when the temperature increased from 300 K to 400 K (from 26.85 °C to 126.85 °C). The performance decrease of *FF* was due

to the increase in the exponential of the temperature, as demonstrated in Equation (14). J_{sc} showed a relative stability under the increase in temperature. The reduction in J_{sc} can be attributed to the bandgap energy (E_g) effect. As temperature rises, J_{sc} experiences a marginal increase due to the reduction in E_g . Consequently, a greater number of photons possess adequate energy to generate electron–hole pairs. Nevertheless, the impact of this phenomenon is quite minor. The exponential relationship between temperature and the reverse saturation current of photovoltaic cells is observed in Equation (15). Additionally, this factor can have an impact on J_{sc} .



Figure 3. (a) *J-V* curves of the S1 device in a temperature range of 300–400 K for a device area of 4.84 mm²; (b) *J-V* curves of the S2 device; (c) *J-V* curves of S3 device.

The second structure's results are presented in Figures 3b and 4. S2 showed the best *PCE* of 19.88% at 300 K with a J_{sc} of 27.15 mA cm⁻², a V_{oc} of 0.89 V, and an *FF* of 82.3%, while the efficiency relatively decreased in the range between 310 and 400 K to reach a *PCE* of 15.25%, a J_{sc} of 26.78 mA cm⁻², a V_{oc} of 0.73, and an *FF* of 78% at 400 K. These results for behave similarly to those for S1 and are slightly decreased in comparison. As depicted by the results for S2, the major parameters that cause a decrease in the performance of this device during the enhancement of temperature are fundamentally the same as before V_{oc} and *FF* (see Figure 4b,c).



Figure 4. Temperature effects on the performance of the S1, S2, and S3 devices: (a) J_{sc} as a function of temperature; (b) V_{oc} as a function of temperature; (c) *FF* as a function of temperature; and (d) *PCE* as a function of temperature.

The increase in temperature caused an enhancement of ΔE as demonstrated by Equation (13), and then V_{oc} decreased because of that enhancement. The diminution in J_{sc} can be attributed to the concept of E_g . As temperature rises, the short-circuit current, J_{sc} , experiences a slight increase due to the reduction in E_g . This decrease in bandgap energy enables a greater number of photons to possess sufficient energy for the generation of excitons. The reverse saturation current of photovoltaic cells exhibits an exponential growth pattern about temperature. Additionally, this factor can have an impact on the magnitude of J_{sc} , as demonstrated in Equation (15).

The third structure is presented in Figure 1, and its outcomes are depicted in Figures 3c and 4. At a temperature of 300 K, S3 exhibited the highest *PCE* of 18.9%. This was accompanied by a J_{sc} of 25.8 mA cm⁻², a V_{oc} of 0.89 V, and an *FF* of 82.36%. However, when the temperature increased to a range from 310 to 400 K, the performance of S3 declined.

At 400 K, *PCE* reduced to 14.6%, with a J_{sc} of 25.76 mA cm⁻², a V_{oc} of 0.73 V, and an *FF* of 78%. According to the findings from the results for S3, it can be observed that the primary factors contributing to the decline in the device's performance as temperature increases remain consistent with V_{oc} and *FF* (refer to Figures 3c and 4b,c).

The rise in temperature leads to an increase in ΔE , as indicated by Equations (12) and (13). Consequently, V_{oc} decreases as a result of this increase. The decrease in J_{sc} can be ascribed to E_g . J_{sc} undergoes a marginal augmentation as the temperature increases, primarily as a result of the decrease in E_g . The reduction in bandgap energy facilitates an increased likelihood of photons possessing the necessary energy to produce electron–hole pairs. The temperature-dependent behavior of photovoltaic cells' reverse saturation current is characterized by an exponential increase, as noted before in S1 and S2. Moreover, this particular element can influence the amount of J_{sc} , as exemplified in Equation (13).

The relationships between recombination, hole, and electron heating are essential to comprehensing the effectiveness and performance of OSCs. When semiconductor materials absorb photons, the energy of the electrons increases, a phenomenon known as electron heating. This process is essential to OSCs because it produces electron–hole pairs when light is absorbed. Elevating electrons from the valence band to the conduction band with

the energy of absorbed photons results in an increase in kinetic energy. Similarly, the energy dynamics of holes are involved in whole heating. The hole left by an electron moving to the conduction band may also acquire energy from thermal processes or interactions with other charge carriers. Charge carrier dynamics can be greatly impacted by the solar cell's overall temperature, which is influenced by both electron and hole heating. Recombination heating occurs when electrons and holes recombine, releasing energy in the form of heat instead of contributing to electrical current.

We investigated the electron, hole, and recombination heating for S1, S2, and S3 OSCs to discover the influence of temperature on carrier dynamics and the impact of carrier dynamics on the performance indicators J_{sc} , V_{oc} , and FF, which affect the PCE and the thermal stability of the devices. The investigation results of the carrier dynamics are depicted in Figures 5 and 6. Figure 5a,c,e represent the electron heating of S1, S2, and S3, respectively. Furthermore, Figure 5b,d,f represent the hole heating in S1, S2, and S3, respectively. We noticed peak values of charge carrier (electron and hole) heating in the position of 150 nm and saw increased electron heating values within the temperature range of 300–400 K. At 300 K, the devices showed peak values of 2.78×10^{11} J S⁻¹ m⁻¹, 2.75×10^{11} J S⁻¹ m⁻¹, and 2.3×10^{11} J S⁻¹ m⁻¹ for S1, S1, and S3, respectively. Furthermore, at 400 K, the devices showed 1.04×10^{12} J S⁻¹ m⁻¹, 1.05×10^{12} J S⁻¹ m⁻¹, and 9.28×10^{11} J S⁻¹ m⁻¹ for S1, S2, and S3, respectively. At the same position, the devices showed increased hole heating values within the temperature range of 300–400 K; at 300 K, the devices showed peak values of 2.57×10^{11} J S⁻¹ m⁻¹, 2.63×10^{11} J S⁻¹ m⁻¹, and 2.22×10^{11} J S⁻¹ m⁻¹ for S1, S2, and S3, respectively, while at 400 K, the results were 9.84×10^{11} J S⁻¹ m⁻¹, 9.86×10^{11} J S⁻¹ m⁻¹, and 8.74×10^{11} J S⁻¹ m⁻¹ for S1, S2, and S3, respectively.

The charge carrier dynamic was impacted by the increasing temperature in the three devices by increasing the interactions with other charge carriers. On the other hand, the recombination rates usually rise as temperatures rise as a result of electron and hole heating. The Arrhenius equation governs this, as shown in Equation (16), and it demonstrates how higher temperatures can speed up reaction rates, including recombination processes, as depicted in Figure 6.

$$k = Ae^{-\frac{L\alpha}{RT}} \tag{16}$$

where *k* is the rate constant of the reaction, *A* is the pre-exponential factor (indicating the frequency of collisions), E_{α} is the activation energy required for the reaction, and *R* is the universal gas constant (approximately 8.314 J mol K).

The recombination rate of S1, S2, and S3, as shown in Figure 6a, Figure 6b, and Figure 6c, respectively, is influenced by the increasing heating process of electrons and holes. While increasing the temperature, we noticed, as shown in Figure 6, that the recombination rate increased proportionally within the range of temperature of 300–400 K for all devices; at the same time, we noticed an inverse proportion of *FF* and V_{oc} specifically, as depicted in Figure 4b,c for S1, S2, and S3.

The materials used for the buffer and active layers have an impact on the production and movement of electrons. The heating profile is influenced by the energy levels and mobilities of various materials. The characteristics of the ETL can influence heating by either promoting or impeding electron mobility. By comparing the carrier dynamic results of electron, holes, and recombination heating rates between the devices using SnO₂, C₆₀, and PC₆₀BM, we found that PC₆₀BM had slightly less recombination and charge carrier heating values in the increasing temperature range between 300 and 400 K, which explains the high *FF* and *V*_{oc} achieved by the device that used PC₆₀BM as an ETL. However, the devices using SnO₂ and C₆₀ as ETLs achieved approximately close values in terms of carrier heating dynamics and performance indicator parameters such as *J*_{sc}, *V*_{oc}, and *FF*. At the same time, they had a higher value of J_{sc} than the PC₆₀BM device and higher PCE values in the entire range of 300–400 K as a result. Despite the stability of the PC₆₀BM device in terms of *FF* and V_{oc} , the lower J_{sc} values achieved by this device can be attributed to its material properties, such as light absorption and less interaction with other charge carriers, which slightly decreases charge mobility, which affects the J_{sc} value as a result.



Figure 5. (a) Electron heating of S1 for temperature range of 300–400 K; (b) hole heating of S1device; (c) electron heating of S2; (d) hole heating of S2; (e) electron heating of S3; (f) hole heating of S3.

We observed that all the structures—S1, S2, and S3—showed good performance in the temperature range between 300 and 340 K (from 26.85 °C to 66.85 °C) (see Table 4), which losses of only 8.7%, 9.6%, 8.8%, respectively, from their initial efficiency. The performance parameters J_{sc} , V_{oc} , and *FF* had somewhat the same progression, while S3 achieved a lower *PCE* of 18.9% in comparison to S1 and S2, which achieved 20.08% and 19.88%, respectively. The inverted structure employing SnO₂ and C₆₀ achieved the best *PCE* of 20.08% and 19.88%, respectively; on the other hand, the inverted structure using PC₆₀BM as an ETL showed a lower *PCE*. Nevertheless, all devices worked efficiently in terms of performance.



Figure 6. (a) Recombination heating of S1 for temperature range of 300–400 K; (b) recombination heating of S2; (c) recombination heating of S3.

Table 4. The photovoltaic parameters of three devices (S1, S2, and S3) under different temperatures.

	Т (К)	<i>T</i> (°C)	$J_{\rm sc}$ (mA cm ⁻²)	$V_{\rm oc}$ (V)	FF (%)	PCE (%)
S1	300	26.85	27.4	0.89	82.2	20.08
	310	36.85	27.4	0.875	81.97	19.66
	320	46.85	27.4	0.86	81.63	19.23
	330	56.85	27.4	0.84	81.26	18.78
	340	66.85	27.38	0.828	80.82	18.33
	350	76.85	27.38	0.812	80.4	17.88
	360	86.85	27.37	0.796	79.91	17.4
	370	96.85	27.37	0.78	79.38	16.95
	380	106.85	27.36	0.76	78.9	16.48
	390	116.85	27.36	0.747	78.35	16.01
	400	126.85	27.35	0.73	77.7	15.53

	Т (К)	<i>T</i> (°C)	$J_{\rm sc}$ (mA cm ⁻²)	$V_{\rm oc}$ (V)	FF (%)	PCE (%)
	300	26.85	27.15	0.89	82.3	19.88
	310	36.85	27.05	0.875	82	19.40
	320	46.85	27.05	0.86	81.6	18.94
	330	56.85	27.03	0.84	81.2	18.44
	340	66.85	26.80	0.83	80.8	17.97
S2	350	76.85	26.85	0.81	80.4	17.49
	360	86.85	26.65	0.8	79.9	17.03
	370	96.85	26.67	0.78	79.4	16.55
	380	106.85	26.80	0.76	78.9	16.07
	390	116.85	26.57	0.75	78.3	15.60
	400	126.85	26.78	0.73	78	15.25
	300	26.85	25.8	0.89	82.36	18.9
	310	36.85	25.8	0.87	82	18.5
	320	46.85	25.8	0.86	81.7	18.08
	330	56.85	25.79	0.84	81.32	17.67
	340	66.85	25.79	0.83	80.87	17.23
S3	350	76.85	25.78	0.81	80.43	16.81
	360	86.85	25.78	0.79	79.97	16.37
	370	96.85	25.77	0.78	79.43	15.93
	380	106.85	25.77	0.76	79	15.5
	390	116.85	25.77	0.75	78.4	15.05
	400	126.85	25.76	0.73	78	14.6

Table 4. Cont.

All the studies showed a stable loss ratio of around 11% between 300 K and 350 K according to the results shown in Table 4.

We investigated our results of the loss ratio of *PCE* as a function of increasing temperature with some other numerical studies, as shown in Table 5. The current study demonstrated the thermal stability of the devices, with a loss ratio of 11% at temperatures from 300 K to 350 K, in contrast to the results of Khan et al. and Kim et al., whose studies reported loss ratios of 15% and 17%, respectively.

Table 5. Results of the PCE loss ratio (%) due to temperature increment.

	<i>PCE</i> at 300 K	PCE at 350 K	Loss Ratio %
Khan et al. [49]	5.75%	4.88%	15%
Anass et al. [37]	8%	7.4%	9%
Muhammad et al. [38]	6.82%	6.2%	9%
Kim et al. [25]	3.25%	2.7%	17%
Our study	20.08%	17.88%	10.9%

Khan et al. performed a one-dimensional numerical simulation of a p-i-n OSC with a structure consisting of ITO/PEDOT: PSS/poly{4,4'-bis(2-ethylhexyl) dithieno [3,2-b:2',3'-d] silole-alt-5,6-difluoro-4,7-bis (4-hexylthiophen-2-yl)-2,1,3-benzothiadiazle): [6,6]-phenyl-C61 butyric acid methyl ester (PDTSDTFFBT: PCBM/PCBM)/Ca/Al with an

emphasis on enhancing the PCE. They measured the effect of the temperature of the device within a range of 300–400 K. Kim et al. investigated OSC performance at a high operating temperature near 300–420 K by using a P-i-n OSC within the structure of ITO/PEDOT: PSS/P3HT: PCBM (1:0.8)/LiF/Al, with an emphasis on studying operating temperature stability, and they measured the effect of the temperature of a device using PEDOT: PSS as the HTL within a range of 300–450 K.

Muhammad et al. performed a one-dimensional numerical simulation of a n-i-p OSC with a structure consisting of FTO/PDINO(Perylene diimide N-oxide)/PBDB-T: ITIC-OE/Spiro OMeTAD/Ni to examine the performance of the OSC. They studied the effect of the temperature of the device within a range of 300–400 K. Anass et al. performed a one-dimensional numerical simulation of a n-i-p OSC with a structure consisting of ITO/PEDOT: PSS/[(Cbz-Mth)-BT]2–PCBM/C61/Al to examine the performance of the OSC, and they studied the influence of variation in the thickness of the active layer, temperature, and the density of the effective states of the electrons and holes in the conduction and valence bands, respectively on the performance of solar cells based on [(Cbz-Mth)-BT]2–PCBM as the photoactive material. The studies conducted by Anass et al. and Muhammad et al. demonstrated an enhancement in thermal stability when compared to previous research. Furthermore, in addition to its thermal stability, our structure achieved a much higher *PCE* than those constructed by Anass et al. and Muhammad et al. Our structures can maintain high thermal stability at high *PCE* conditions, representing a notable advancement in OSCs. Our

4. Conclusions

Our research demonstrated that the efficiency of inverted ternary OSCs incorporating SnO_2 , C_{60} , and $PC_{60}BM$ is robust to temperature increases, sustaining less than 10% efficiency loss at 66.76 °C compared to their baseline efficiency at 26.76 °C. This finding underscores the commendable thermal stability of these structures. In addition, we achieved such thermal stability at a high PCE condition, indicating the superb environmental adaptability of investigated structures. However, the configuration using $PC_{60}BM$ as the electron transport layer exhibited a relatively lower PCE, highlighting potential areas for improvement. Future studies should expand on these preliminary findings with comprehensive evaluations focusing on the effects of operational temperature on newly synthesized organic semiconductor materials to discover more stable structures and further increase the thermal stability of the organic solar cells. This includes investigating charge mobilities, optimizing cooling systems, assessing material quality, and testing endurance under varying ambient temperatures. From the beginning of the work, we stated that this work involves a simulation of a one-dimensional optoelectronic model. The simulation is limited to a one-dimensional scale and provides findings based on specific parameters related to material properties, standard ambient conditions, defined structure, and a complete standard selection. In addition, practical models or models based on three dimensions have extra challenges and variables due to differences in dimensions compared to the real model, difficulties in controlling ambient conditions, and defects in morphology, deposition method, and material quality, which probably further affect the results.

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Nomenclature

Symbol	Description	Unit
9	Electrical charge	С
п	Concentration of free electrons	m^{-3}
n_0	Equilibrium density of electron	m^{-3}
N _{c,v}	Effective density of states in the conduction band	m^{-3}
p_0	Equilibrium density of holes	m^{-3}
Р	Concentration of free holes	m^{-3}
J _{n,p}	Current density of electrons and holes	Am^{-2}
vth	Thermal emission velocity of the carriers	ms^{-1}
$E_{c,v}$	Conduction and valence band	eV
E _{F,h,e}	Energy-corresponding Fermi level	eV
F _{n,p}	Fermi level in the conduction and valence band	eV
D _{n,p}	Diffusion coefficient of electrons and holes	$\mathrm{m}^2~\mathrm{s}^{-1}$
R _n	Recombination rate of electrons	s^{-1}
Rp	Recombination rate of holes	s^{-1}
G	Carrier generation rate	s^{-1}
KB	Boltzmann constant	JK^{-1}
k	Rate constant of the reaction	
Т	Temperature	Κ
Greek S	Symbols	
\mathcal{E}_0	Permittivity of free space	Fm^{-1}
\mathcal{E}_{r}	Relative permittivity	Fm^{-1}
φ	Voltage profile	V
$\mu_{\rm e,h}$	Electron and hole mobility	${ m m}^2 { m V}^{-1} { m s}^{-1}$
ΔE	Energy offset	%
ω	Angular frequency of the wave	$ m rad~s^{-1}$
λ	Wavelength	nm
Superso	cripts	
D	Electron donor	
Α	Electron acceptor	

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