

Article

# Radiation Response of Negative Gate Biased SiC MOSFETs

Akinori Takeyama <sup>1,\*</sup>, Takahiro Makino <sup>1</sup>, Shuichi Okubo <sup>2</sup>, Yuki Tanaka <sup>2</sup>, Toru Yoshie <sup>2</sup>, Yasuto Hijikata <sup>3</sup> and Takeshi Ohshima <sup>1</sup>

<sup>1</sup> National Institutes for Quantum and Radiological Science and Technology, 1233 Watanuki-machi, Takasaki 370-1292, Japan

<sup>2</sup> Sanken Electric Co., Ltd., Niiza, Saitama 352-8666, Japan

<sup>3</sup> Graduate School of Science and Engineering, Saitama University, Saitama 338-8570, Japan

\* Correspondence: takeyama.akinori@qst.go.jp; Tel.: +81-27-346-9171

Received: 12 July 2019; Accepted: 22 August 2019; Published: 27 August 2019



**Abstract:** Silicon carbide (SiC) metal-oxide-semiconductor field effect transistors (MOSFETs) are expected as power electronic devices for high radiative conditions, including nuclear plants and space. Radiation response of commercial-grade prototype SiC MOSFETs with applying the gate bias is of interest, in terms of installation of the device in robots or sensors working under such radioactive circumstances. Due to gamma-rays irradiation, the threshold voltages ( $V_{th}$ ) of samples with un- and negative-biased up to  $-4.5$  V slightly shift toward the negative voltage side. In contrast, the positive bias of  $2.25$  V shifts  $V_{th}$  more negatively. Positive charge densities trapped in the gate oxide of un- and positive-biased samples increased with increasing dose. However, no significant increase was observed for negative-biased samples of  $-2.25$  and  $-4.5$  V. We calculated characteristic parameters for the accumulation of holes in the gate oxide,  $\sigma_p J_p$  which is defined as the product of current density due to holes generated by irradiation and capture cross section for a hole in a trap, and it is lower for these negative biased samples compared with the unbiased case. Application of appropriate negative gate biases to SiC MOSFETs during irradiation suppresses accumulation of positive charges in the gate oxide and negative shift of  $V_{th}$ , due to irradiation.

**Keywords:** SiC; MOSFET; gamma-rays; radiation response; gate bias

## 1. Introduction

Owing to its wide band gap and strong covalent bond of silicon carbide (SiC), SiC metal-oxide-semiconductor field effect transistors (MOSFETs) are known as power electronic devices with a higher radiation tolerance. The radiation response of SiC MOSFETs is of interest in terms of the application of this device for use in radiative conditions, including nuclear plants and space. In particular, for the decommissioning of Tokyo Electric Power Company Holdings (TEPCO) Fukushima Dai-ichi nuclear plants, although operations of robots or sensors under high radioactive circumstance are required [1–3], commercial Si devices seem to be susceptible to radiation damage in such harsh conditions [4,5]. Recently, the radiation response of commercially available or prototype SiC MOSFETs against gamma ray irradiation was studied by several authors [6–12]. These MOSFETs have a channel length shorter than  $1 \mu\text{m}$  and width on the order of a meter to conduct large currents. Also, specific processes are employed to improve electric properties, including nitridation of the interface between the oxide and SiC. These characteristics of commercial or prototype SiC MOSFETs might make their electrical properties be more sensitive to irradiation compared with ones for the basic research. So far, the radiation response was studied in terms of device structure and fabrication process [6], irradiation conditions, such as high temperature and humidity [7,8,10], and application of gate bias [11,12].

In particular, applying the positive gate bias during irradiation, the threshold voltage ( $V_{th}$ ) remarkably shifts in the negative direction, as reported for irradiation of positively biased Si MOSFETs [13–15]. It is explained that the radiation-induced holes move toward the Si/oxide ( $\text{SiO}_2$ ) interface due to the applied electric field and are trapped by defects in the oxide near the interface [16–18]. In addition, varying the gate bias during irradiation from positive to either negative or zero volts results in substantial recovery of  $V_{th}$  [11]. These findings indicate that annihilation of holes trapped in the gate oxide takes place by changing the polarity of the bias [19–22]. Such radiation induced charge neutralization for Si MOS devices was also explained in terms of direct annealing of trapped holes and charge compensation of positive charges of trapped holes by electrons [23]. Contribution of the charge compensation to the annealing of net positive charges is less significant for non-hardened oxide, since it involves a larger amount of oxygen vacancies as a hole trap, resulting from dry or high temperature oxidation process [16]. In contrast to positive bias, the effect of negative gate bias on the radiation response of commercial or prototype SiC MOSFETs is still unclear. Although it has been suggested that negative gate bias results in the annihilation of accumulated holes [11], more detailed research is required for practical use and to determine the condition in which annihilation takes place. In addition, a study about the radiation response under a negative biased condition is also of interest to predict aged electrical properties of SiC MOSFETs in a non-radiative condition. Since gamma-rays can generate a number of additional holes/electrons in the oxide/semiconductor, irradiation serves as an accelerated test and allows us to avoid time-consuming experiments [24]. Previous reports about Si MOSFETs showed that long term degradation behavior under bias and temperature stress is quite similar to that caused by irradiation. In this report, the effect of negative gate bias on the radiation response of prototype SiC MOSFETs is studied. Dependence of annihilation and accumulation behaviors of holes on the applied bias is revealed and the condition in which annihilation predominantly takes place is determined.

## 2. Materials and Methods

The samples used in this study were vertical 4H-SiC MOSFETs mounted in “TO3P” packages, provided by Sanken Electric Co., Ltd. (Niiza, Japan). The blocking voltage and the rated current are 1200 V and 20 A, respectively. These were prototype devices, although fabricated using commercial processes, such as nitridation of the gate oxide. Dry oxidation followed by 10%  $\text{N}_2\text{O}$  treatment was carried out to fabricate a gate oxide of 45 nm thickness. P-wells in the channel region were formed by ion implantation. Doping concentration of acceptor was approximately  $1 \times 10^{16}$ – $2 \times 10^{16}$   $\text{cm}^{-3}$ . The samples were irradiated with gamma-rays from  $^{60}\text{Co}$  at a dose rate of 10 kGy ( $\text{SiO}_2$ )/h up to 2 MGy at room temperature (RT) in dry  $\text{N}_2$  atmosphere. During irradiation, negative biases of  $-9$ ,  $-4.5$ , and  $-2.25$  V were applied to the gate electrode, and source and drain electrodes were grounded. For comparison, SiC MOSFETs were also irradiated with the gate node grounded and with a positive bias of  $+2.25$  V. At the intervals between each irradiation, drain current ( $I_D$ )–gate voltage ( $V_G$ ), and  $I_D$ –drain voltage ( $V_D$ ), characteristics were measured at RT using a semiconductor parameter analyzer (Agilent 4156 B).

Densities of the positive charge trapped in gate oxide and negative charge at the interface of SiC/oxide were calculated on the slope of  $I_D$ – $V_G$  curves [25]. The midgap current  $I_{mg}$  was calculated using the equation representing ideal drain current in the subthreshold region [26]:

$$I_D = 2^{\frac{1}{2}} \mu \left( \frac{W}{2L} \right) \left( \frac{qN_A L_B}{\beta} \right) \left( \frac{n_i}{N_A} \right)^2 e^{\beta \Psi_s} (\beta \Psi_s)^{-\frac{1}{2}} \quad (1)$$

where  $W$ ,  $L$ : channel width and length,  $q$ ,  $N_A$ ,  $n_i$ ,  $\Psi_s$ : the elementary charge, acceptor concentration in channel region, intrinsic carrier concentration at the absolute temperature ( $T$ ), and band bending at the surface of SiC, respectively. Channel size and acceptor concentration data was provided by Sanken Electric Co., Ltd. The reciprocal thermal energy ( $=q/k_B T$ , where  $k_B$  is Boltzmann constant)

is  $\beta$  and Debye length  $(=\epsilon_{\text{SiC}} \cdot \epsilon_0 / (\beta q N_A))^{1/2}$ , where  $\epsilon_{\text{SiC}}$ ,  $\epsilon_0$  are the relative permittivity of SiC and permittivity in vacuum, respectively) is  $L_B$ . Effective mobility ( $\mu$ ) is calculated from linear region of  $I_D$ - $V_D$  curves. When the surface is bending,  $\Psi_s$  is equal to  $\Psi_b = (k_B T / q) \ln(N_A / n_i)$  in the above equation, the midgap current  $I_{\text{mg}}$  was obtained. Physical parameters, including the dielectric constant  $\epsilon_{\text{SiC}}$  of 9.7, and the intrinsic carrier concentration  $n_i$  at  $RT$  of  $5.44 \times 10^{-9} \text{ cm}^{-3}$  were adopted from the reference [27]. The subthreshold region of  $I_D$ - $V_G$  curves was extrapolated to the calculated  $I_{\text{mg}}$  to obtain the midgap voltage,  $V_{\text{mg}}$ . The extrapolation of  $I_D$ - $V_G$  curves was carried out carefully to exclude the influence of leakage current. For a sample before irradiation, the curve could be extrapolated from  $I_D = 10^{-10} \text{ A}$ , however, for irradiated samples, the curve was extrapolated from  $I_D = 10^{-7} \text{ A}$ . Finally, threshold voltage shift ( $\Delta V_{\text{th}}$ ) of irradiated samples was separated into two terms of charge trapped in oxide ( $\Delta V_{\text{ot}}$ ) and at interface ( $\Delta V_{\text{int}}$ ), depicted as follows:

$$\Delta V_{\text{th}} = \Delta V_{\text{ot}} + \Delta V_{\text{int}}, \quad \Delta V_{\text{ot}} = \Delta V_{\text{mg}}, \quad \Delta V_{\text{int}} = \Delta V_{\text{th}} - \Delta V_{\text{mg}} \quad (2)$$

where  $\Delta V_{\text{mg}}$  is the shift of  $V_{\text{mg}}$  due to irradiation.

The areal densities of trapped holes  $\Delta N_{\text{ot}}$  and interface state  $\Delta N_{\text{int}}$  are obtained from equations:

$$\Delta N_{\text{ot}} = \frac{C_{\text{ox}}}{q} \Delta V_{\text{ot}}, \quad \Delta N_{\text{int}} = \frac{C_{\text{ox}}}{q} \Delta V_{\text{int}} \quad (3)$$

where  $C_{\text{ox}}$  is oxide capacitance. Before estimation, we measured the high frequency (1 MHz) capacitance (C)-voltage ( $V$ ) curves of MOS capacitors provided by Sanken Electric at room temperature (data are not shown). The thickness and fabrication process of the oxide is the same as that of the gate oxide of the MOSFETs used in this study. Calculated dielectric constant from oxide capacitance allows us to consider the gate oxide as  $\text{SiO}_2$ . Accumulation and annihilation behavior of holes in the oxide due to irradiation is described by the following equation [22,28]:

$$\frac{\partial p_t}{\partial t} = \sigma_p J_p (N_t - p_t) - \sigma_n J_n p_t \quad (4)$$

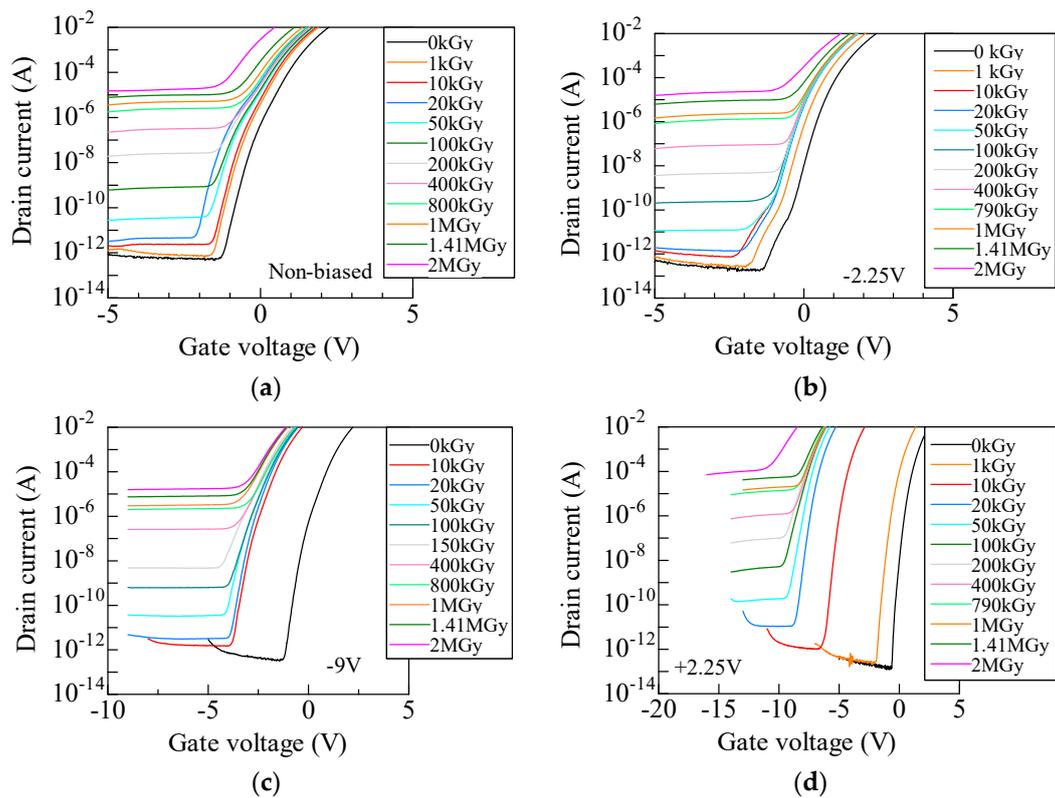
where  $p_t$  is hole density trapped at the defect,  $J_{p,n}$  is current density due to holes and electrons,  $\sigma_{p,n}$  is capture cross section for a hole in a trap and electron in trapped holes, respectively.  $N_t$  is the hole trap density. When the oxide electric field  $E_{\text{ox}}$  is positive, holes diffuse in the gate oxide from the gate electrode toward SiC/oxide interface and electrons diffuse in the opposite direction. The effect of detrapping of captured holes into  $\text{SiO}_2$  valence band [29] is also included in the second term of Equation (4). As previously reported, the gate oxide of practical SiC MOSFETs includes nitrogen-related defects [30] in addition to the oxygen vacancy, which is a popular positive charge trap in Si MOSFETs [31]. Thus, we evaluate accumulation and annihilation behavior of holes using Equation (4) in dose region below 100 kGy, in which the effect of nitrogen-related defects is not significant [6]. The oxide electric field during irradiation is approximated by dividing the sum of applied bias and the built-in voltage applied to the oxide at  $V_G = 0 \text{ V}$ , by the oxide thickness. That is calculated by the following procedure. Using experimentally obtained  $I_D$  at  $V_G = 0 \text{ V}$  from  $I_D$ - $V_G$  curve and Equation (1), surface potential  $\Psi_s$  at  $V_G = 0 \text{ V}$  is obtained. Assuming electrical properties of irradiated SiC MOSFETs are predominantly determined by oxide trapped ( $V_{\text{ot}}$ ) and interface trapped charges ( $V_{\text{int}}$ ),  $V_G$  is correlated with  $\Psi_s$  and flat band voltage ( $V_{\text{FB}}$ ) as follows:

$$V_G = V_{\text{FB}} + V_{\text{ot}} + V_{\text{int}} + \Psi_s + \sqrt{(2\epsilon_{\text{SiC}} \epsilon_0 q N_A \Psi_s) / C_{\text{ox}}} \quad (5)$$

After subtracting obtained  $\Psi_s$  from both side of the equation at  $V_G = 0 \text{ V}$ , the right-hand side of the equation corresponds to the voltage applied to the oxide before applying the bias.

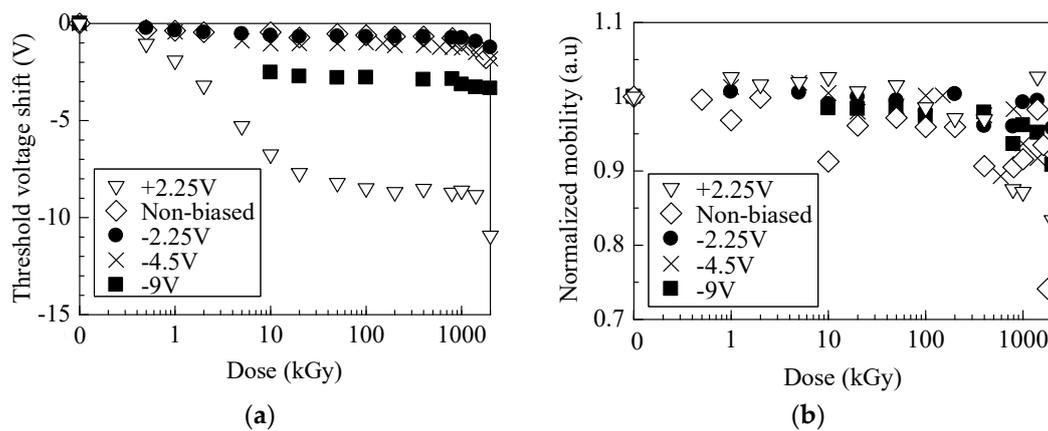
### 3. Results

Figure 1a–d show  $I_D$ – $V_G$  curves in the subthreshold region for unbiased,  $-2.25$ ,  $-9$ , and  $+2.25$  V biased samples. During the measurement,  $10$  V of  $V_D$  was applied.  $I_D$ – $V_G$  curves for negatively and unbiased samples (Figure 1a–c) negatively shift due to irradiation, however, no significant shift is observed above  $20$  kGy. It should be mentioned that an increase in leakage current with increasing dose is attributed to the leakage path generated by parasitic bias due to accumulated positive charges in the field oxide, rather than the degradation of the interface between SiC/oxide [10]. Since this study focuses on charge generated in the gate oxide, we do not discuss this leakage current in this study. In contrast,  $I_D$ – $V_G$  curves for  $+2.25$  V shift more negatively at even  $10$  kGy irradiation.



**Figure 1.**  $I_D$ – $V_G$  curves in the subthreshold region for irradiated SiC MOSFETs for (a) unbiased, (b)  $-2.25$  V, (c)  $-9$  V, and (d)  $+2.25$  V. These were measured applying a drain voltage of  $10$  V.

The threshold voltage shift  $\Delta V_{th}$  is plotted as a function of dose.  $V_{th}$  is calculated by linear extrapolation of the square root of  $I_D$ – $V_G$  curves in the saturation region.  $\Delta V_{th}$  is the voltage shift from the initial  $V_{th}$  of each sample before irradiation. In Figure 2a,  $\Delta V_{th}$  of unbiased and negative biased samples slightly decreased up to  $20$  kGy and the  $+2.25$  V biased sample decreased up to  $50$  kGy, and further negative shift is observed over  $1$  MGy. Observed shift above  $1$  MGy was caused by an increase of the density of hole traps in the oxide, probably as a result of cleavage of the Si–N bond [6].

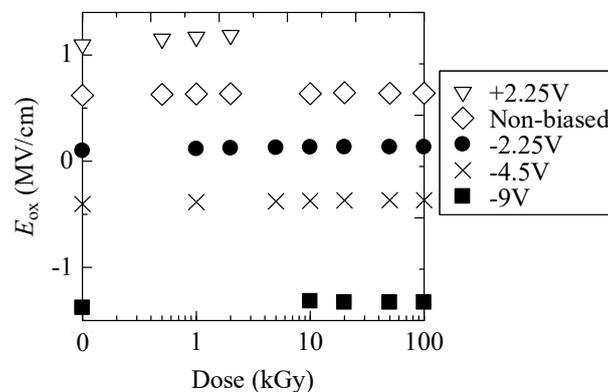


**Figure 2.** Plots of (a) the threshold voltage shift and (b) normalized mobility as a function of dose.

In order to investigate the effect of interface traps on electrical properties, normalized channel mobilities are plotted as a function of dose in Figure 2b. Normalized channel mobility is defined as a ratio of the effective mobility for irradiated samples ( $\mu_{\text{eff}}$ ), which was calculated from drain current ( $I_D$ )–drain voltage ( $V_D$ ) curves, to compare with the initial (before-irradiated) sample ( $\mu_{\text{eff0}}$ ). Each normalized mobility slightly decreased with the increasing dose. It resulted from electron scattering by increased shallow traps located at the interface between SiC/oxide [32]. However, at least below 200 kGy, significant degradation of mobility was not observed. It suggests that most of interface traps formed in this dose region were located far from the conduction band edge of SiC. Usually, charges trapped at such deep traps might also behave as scattering centers of carriers in the inversion layer. However, in the case of SiC MOSFETs, they are easily screened by the carriers, since the number of electrons in the inversion layer increases with increasing the gate voltage [33,34]. Although we cannot quantify contribution of the interface traps to the mobility degrade, deep traps act primarily as negative fixed charges in dose regions as low as several hundred kGy.

#### 4. Discussion

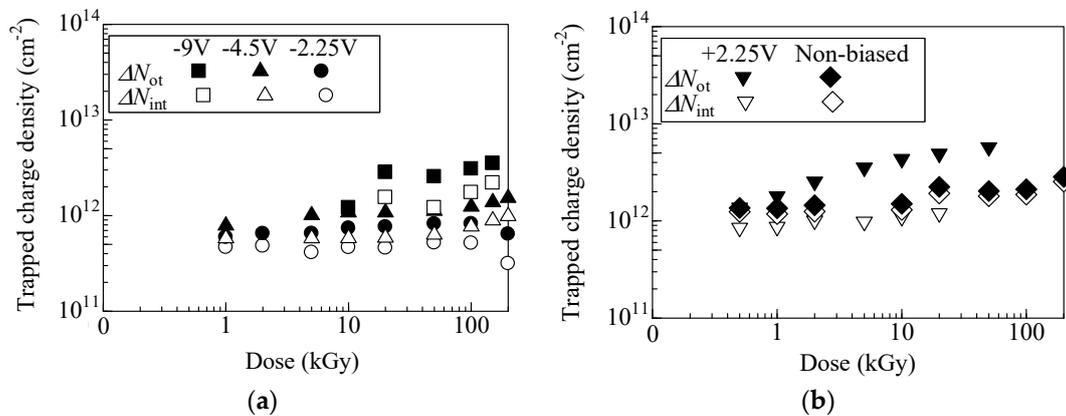
Figure 3 is the estimated electric field formed in the gate oxide ( $E_{\text{ox}}$ ) during irradiation. For unbiased and negative biased cases,  $E_{\text{ox}}$  slightly increased up to 10 kGy and no significant change was observed with further irradiation. As shown in Figure 3,  $E_{\text{ox}}$  for  $-2.25$  V is approximately 0 and  $E_{\text{ox}}$  for  $-4.5$  V is slightly negative. For  $+2.25$  V, we cannot obtain values above 5 kGy since large negative shift of  $I_D$ – $V_G$  curves inhibits to calculate  $\Psi_s$  at  $V_G = 0$  V.



**Figure 3.** Plots of electric field gradient in the oxide during irradiation ( $E_{\text{ox}}$ ) as a function of dose.

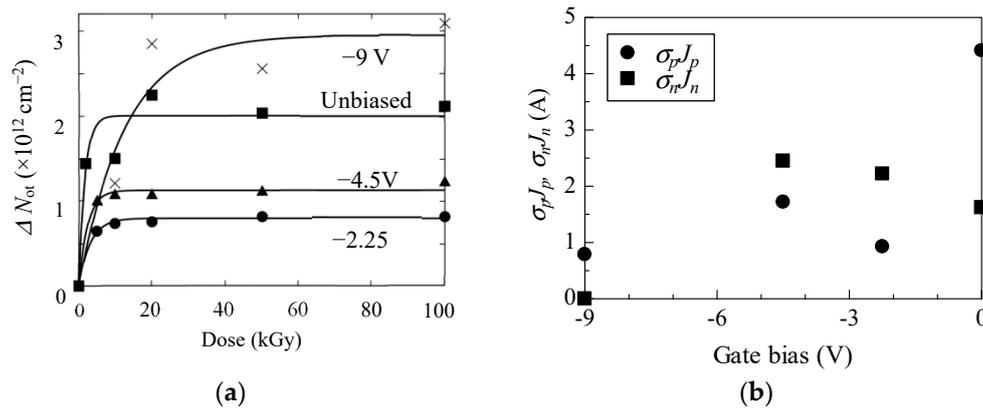
Figure 4a,b show charge densities trapped in oxide ( $\Delta N_{\text{ot}}$ ) and at interface ( $\Delta N_{\text{int}}$ ) plotted as a function of dose. Both  $\Delta N_{\text{ot}}$  and  $\Delta N_{\text{int}}$  are plotted up to 200 kGy, since large leakage current inhibits

extrapolation of the  $I_D-V_G$  curves. In the figures, an increase of  $\Delta N_{ot}$  with an increasing dose resulted from accumulation of holes in the oxide.  $\Delta N_{ot}$  for negative and unbiased samples gradually increased, while  $\Delta N_{ot}$  of +2.25 V monotonically increased. Relative to  $\Delta N_{ot}$ , a smaller increase of  $\Delta N_{int}$  with increasing dose was observed for every sample. The effect of application of the gate bias during irradiation on the interface property is smaller than that for the oxide.



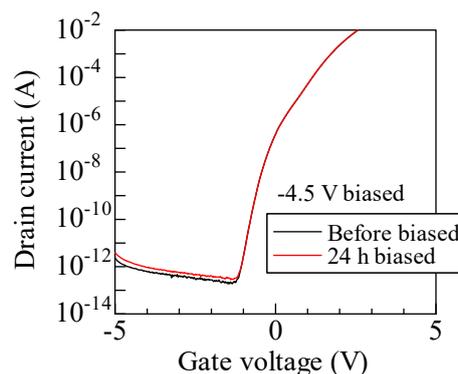
**Figure 4.** Positive charges trapped in the oxide ( $\Delta N_{ot}$ ) and at the interface ( $\Delta N_{int}$ ) for (a) negative biased, (b) un- and +2.25 V biased samples.

Accumulation and annihilation behavior of holes in the oxide were investigated by least square fitting of obtained  $\Delta N_{ot}$  curves. On the basis of  $\Delta N_{ot}$  of +2.25 V biased sample below 2 kGy, the density of hole trap in the oxide,  $N_t$ , was calculated using Equation (4). In such a low dose region, the annihilation effect (second term of Equation (4)) could be negligible. Least square fitting of plots of  $\Delta N_{ot}$  gives  $N_t$  of  $2.7 \times 10^{12} \text{ cm}^{-2}$ , which is somewhat smaller than the reported  $N_t$  of 4.50 and  $8.50 \times 10^{12} \text{ cm}^{-2}$  [22]. Figure 5a shows experimental plots and estimated curves of  $\Delta N_{ot}$  for unbiased and negative biased samples. We maintain the view that  $\Delta N_{ot}$  estimated from experimental results essentially follow Equation (4), although the experimental uncertainties should be improved. Estimated  $\sigma_p J_p$  and  $\sigma_n J_n$  are also plotted as a function of the gate bias in Figure 5b, respectively.  $\sigma_n J_n$  of negative biased samples slightly increased for -2.25 and -4.5 V biased cases relative to unbiased case. Since  $\sigma_n J_n$  is annihilation probability of a trapped hole, this indicates that annihilation behavior was not significantly affected by application of these negative biases, while  $\sigma_p J_p$  is less than half of the unbiased one. As the absolute value of the oxide electric field ( $E_{ox}$ ) for these negative biased samples was smaller than the unbiased case, as shown in Figure 3, the yield of hole due to irradiation is expected to be smaller [20]. Additionally, smaller  $E_{ox}$  might suppress movement of generated holes toward SiC/oxide interface, at which many hole traps exist. Thus, for -2.25 and -4.5 V biased cases, relatively lower  $\sigma_p J_p$  was obtained. Further increasing the negative bias up to -9 V,  $E_{ox}$  becomes larger, as seen in Figure 3. Nevertheless, remarkable increase of  $\sigma_p J_p$  was not observed and the annealing effect was not extracted from the fitting results. These suggest that most of the hole traps are located close to the SiC/SiO<sub>2</sub> interface [35]. Due to the suppression of movement of holes toward the interface by negatively large  $E_{ox}$ , a portion of holes generated by irradiation could be trapped. Low annealing effect is also interpreted as formation of traps too far from the gate electrode. They were beyond the reach of electrons coming from the gate electrode.



**Figure 5.** (a) Experimental plots and simulated curves for  $-2.5$ ,  $-4.5$  V, and non-biased samples plotted as a function of dose. (b) Estimated  $\sigma_p J_p$  and  $\sigma_n J_n$  plotted against applied gate bias during irradiation.

In this study, electrical properties of irradiated MOSFETs were characterized after each irradiation. It takes inevitably several minutes before the measurement is carried out. Such a relatively long interval is not recommended in terms of precise evaluation of the instability of  $V_{th}$  [36]. Due to the long interval, the amount of charges with a fast detrapping time constant cannot be evaluated by our measurement procedure [35]. In other words, obtained results in this study show the effect of charges with a slow time constant on the radiation response of MOSFETs. Furthermore, we applied the bias of  $-4.5$  V on the gate electrode with other electrodes grounded in order to investigate the contribution of only bias on the electrical properties. The bias is applied to MOSFETs without irradiation and measurement procedure is the same as the case of irradiation experiment. Figure 6 shows the  $I_D$ - $V_G$  curves of the MOSFETs before applying bias and after 24 h aging. This aging time corresponds to the time for a cumulative dose of 240 kGy in our irradiation condition. The  $I_D$ - $V_G$  curve of the aged sample is almost consistent with that of the pristine one, except for a slightly increased leakage current. Hence, only the application of the gate bias does not significantly affect the electrical properties. Accumulation and annealing of trapped charges observed in this study are attributed to a synergy effect between the gate bias and irradiation.



**Figure 6.**  $I_D$ - $V_G$  curves in the subthreshold region for biased SiC MOSFETs without irradiation. For  $I_D$ - $V_G$  measurement, drain voltage of 10 V were applied.

## 5. Conclusions

Electrical properties of prototype SiC MOSFETs irradiated at various gate biases were characterized. Threshold voltages ( $V_{th}$ ) for un- and negative-biased up to  $-4.5$  V samples slightly shift toward the negative voltage side, due to irradiation. A further increasing bias was observed negatively up to  $-9$  V,  $V_{th}$  shift more negatively. Moreover, a further negative shift of  $V_{th}$  was observed for the positive bias of 2.25 V. Positive charge densities trapped in the gate oxide of un- and positive-biased samples increased

with increasing dose, while no significant increase was observed for negative-biased samples of  $-2.25$  and  $-4.5$  V. The characteristic parameter for accumulation of holes in the gate oxide,  $\sigma_p J_p$ , was lower for these negative-biased samples compared with the unbiased case. Further increasing the negative bias up to  $-9$  V,  $\sigma_p J_p$  is not significantly varied. This indicates that negative bias and  $E_{ox}$  suppress the movement of holes toward traps located close to the SiC/SiO<sub>2</sub> interface and only a portion of holes generated by irradiation were trapped. In contrast, there is not as large a difference in the parameter for annihilation of holes trapped in the oxide,  $\sigma_n J_n$ , among  $-2.25$  and  $-4.5$  V biased and unbiased samples. For  $-9$  V biased case, no annihilation effect was recognized. These results indicate that trapped holes were predominantly annealed by electrons coming from the SiC side. Application of negative gate biases to SiC MOSFETs during irradiation by which  $|E_{ox}|$  within approximately  $0.5$  MV/cm was generated, suppresses accumulation of holes in the gate oxide.

**Author Contributions:** A.T., Y.H., T.O., and T.Y.; proposed the idea and the strategy for the experimental design, S.O., Y.T., and T.Y.; design and fabrication of SiC MOSFETs, A.T. and T.M.; methodology and investigation, A.T.; writing—original draft preparation, T.O. and Y.H.; reviewing and editing the original draft of manuscript.

**Funding:** This study was partially funded by the Nuclear Science Research Initiative of Japan.

**Acknowledgments:** We are also grateful to Koichi Murata of graduate student of Saitama University for the fabrication of irradiation equipment and fruitful discussions.

**Conflicts of Interest:** The authors declare no conflict of interest.

## References

1. Nagatani, K.; Kiribayashi, S.; Okada, Y.; Otake, K.; Yoshida, K.; Tadokoro, S.; Nishimura, T.; Yoshida, T.; Koyanagi, E.; Fukushima, M.; et al. Emergency response to the nuclear accident at the Fukushima Daiichi Nuclear Power Plants using mobile rescue robots. *J. Field. Robot.* **2013**, *30*, 44–63. [[CrossRef](#)]
2. Cho, J.; Choi, Y.; Jeong, K. Monitoring performance of the cameras under the high dose-rate gamma ray environments. *Health Phys.* **2014**, *106*, S47–S58. [[CrossRef](#)] [[PubMed](#)]
3. Cho, H.; Woo, T. Project strategy for clean-up of sedimentary radioactive material in Fukushima bay areas using snake-like robotics. *Nucl. Tec. Rad. Prot.* **2015**, *30*, 318–323. [[CrossRef](#)]
4. Dodd, P.E.; Shaneyfelt, M.R.; Schwank, J.R.; Felix, J.A. Current and future challenges in radiation effects on CMOS electronics. *IEEE Trans. Nucl. Sci.* **2010**, *57*, 1747–1763. [[CrossRef](#)]
5. Fleetwood, D.M. Total ionizing dose effects in MOS and low-dose-rate-sensitive linear-bipolar devices. *IEEE Trans. Nucl. Sci.* **2013**, *60*, 1706–1730. [[CrossRef](#)]
6. Mitomo, S.; Matsuda, T.; Murata, K.; Yokoseki, T.; Makino, T.; Takeyama, A.; Onoda, S.; Ohshima, T.; Okubo, S.; Tanaka, Y.; et al. Optimum structures for gamma-ray radiation resistant SiC-MOSFETs. *Phys. Status Solidi A* **2017**, *214*, 1600425-1-7. [[CrossRef](#)]
7. Matsuda, T.; Yokoseki, T.; Mitomo, S.; Murata, K.; Makino, T.; Abe, H.; Takeyama, A.; Onoda, S.; Tanaka, Y.; Kandori, M.; et al. Change in characteristics of SiC MOSFETs by gamma-ray irradiation at high temperature. *Mater. Sci. Forum.* **2016**, *858*, 860–863. [[CrossRef](#)]
8. Ohshima, T.; Yokoseki, T.; Murata, K.; Matsuda, T.; Mitomo, S.; Abe, H.; Makino, T.; Onoda, S.; Hijikata, Y.; Tanaka, Y.; et al. Radiation response of silicon carbide metal–oxide–semiconductor transistors in high dose region. *Jpn. J. Appl. Phys.* **2016**, *55*, 01AD01-1-4. [[CrossRef](#)]
9. Akturk, A.; McGarrity, J.M.; Potbhare, S.; Goldsman, N. Radiation effects in commercial 1200 V 24 A silicon carbide power MOSFETs. *IEEE Trans. Nucl. Sci.* **2012**, *59*, 3258–3264. [[CrossRef](#)]
10. Takeyama, A.; Matsuda, T.; Yokoseki, T.; Mitomo, S.; Murata, K.; Makino, T.; Onoda, S.; Okubo, S.; Tanaka, Y.; Kandori, M.; et al. Improvement of radiation response of SiC MOSFETs under high temperature and humidity conditions. *Jpn. J. Appl. Phys.* **2016**, *55*, 104101-1-4. [[CrossRef](#)]
11. Murata, K.; Mitomo, S.; Matsuda, T.; Yokoseki, T.; Makino, T.; Onoda, S.; Takeyama, A.; Ohshima, T.; Okubo, S.; Tanaka, Y.; et al. Impacts of gate bias and its variation on gamma-ray irradiation resistance of SiC MOSFETs. *Phys. Status Solidi A* **2017**, *214*, 1600446-1-7. [[CrossRef](#)]
12. Zhang, T.; Allard, B.; Bi, J. The synergetic effects of high temperature gate bias and total ionization dose on 1.2 kV SiC devices. *Microelectron. Reliab.* **2018**, *88–90*, 631–635. [[CrossRef](#)]

13. Messenger, G.C.; Steele, E.J.; Neustadt, M. Displacement damage in MOS transistors. *IEEE Trans. Nucl. Sci.* **1965**, *12*, 78–82. [[CrossRef](#)]
14. Anant, G.S. Characterization of annealing of Co60 gamma-ray damage at the Si/SiO<sub>2</sub> interface. *IEEE Trans. Nucl. Sci.* **1983**, *30*, 4094–4099.
15. Tallon, R.W.; Ackermann, M.R.; Kemp, W.T.; Owen, M.H.; Saunders, D.P. A Comparison of ionizing radiation damage in MOSFETs from cobalt-60 gamma rays, 0.5 to 22 MeV protons and 1 to 7 MeV electrons. *IEEE Trans. Nucl. Sci.* **1985**, *32*, 4393–4398. [[CrossRef](#)]
16. Schwank, J.R.; Shaneyfelt, M.R.; Fleetwood, D.M.; Felix, J.A.; Dodd, P.E.; Paillet, P.; Ferlet-Cavrois, V. Radiation effects in MOS oxides. *IEEE Trans. Nucl. Sci.* **2008**, *55*, 1833–1853. [[CrossRef](#)]
17. Oldham, T.R.; McLean, F.B. Total ionizing dose effects in MOS oxides and devices. *IEEE Trans. Nucl. Sci.* **2003**, *50*, 483–499. [[CrossRef](#)]
18. Zhang, C.; Zhang, E.; Fleetwood, D.M.; Schrimpf, R.D.; Dhar, S.; Ryu, S.; Shen, X.; Pantelides, S.T. Effects of bias on the irradiation and annealing responses of 4H-SiC MOS devices. *IEEE Trans. Nucl. Sci.* **2011**, *58*, 2925–2929. [[CrossRef](#)]
19. Faigon, A.; Lipovetzky, J.; Redin, E.; Krusczenski, G. Extension of the measurement range of MOS dosimeters using radiation induced charge neutralization. *IEEE Trans. Nucl. Sci.* **2008**, *55*, 2141–2147. [[CrossRef](#)]
20. Sambuco Salomone, L.; Faigón, A.; Redin, E. Numerical Modeling of MOS dosimeters under switched bias irradiations. *IEEE Trans. Nucl. Sci.* **2015**, *62*, 1665–1673. [[CrossRef](#)]
21. Fleetwood, D.M.; Winokur, P.; Riewe, L. Predicting switched-bias response from steady-state irradiations MOS transistors. *IEEE Trans. Nucl. Sci.* **1990**, *37*, 1806–1817. [[CrossRef](#)]
22. Faigon, A.; Garcia Inza, M.; Lipovetzky, J.; Redin, E.; Carbonetto, S.; Sambuco Salomone, L.; Berbeglia, F. Experimental evidence and modeling of non-monotonic responses in MOS dosimeters. *Radiat. Phys. Chem.* **2014**, *95*, 44–46. [[CrossRef](#)]
23. Fleetwood, D.M.; Reber, R.A., Jr.; Winokur, P.S. Trapped-hole annealing and electron trapping in metal-oxide-semiconductor devices. *Appl. Phys. Lett.* **1992**, *60*, 2008–2010. [[CrossRef](#)]
24. Haller, G.; Knoll, M.; Bräunig, D.; Wulf, F.; Fahrner, W.R. Bias-temperature stress on metal-oxide-semiconductor structures as compared to ionizing irradiation and tunnel injection. *J. Appl. Phys.* **1984**, *56*, 1844–1850. [[CrossRef](#)]
25. McWhorter, P.J.; Winokur, P.S. Simple technique for separating the effects of interface traps and trapped-oxide charge in metal-oxide-semiconductor transistors. *Appl. Phys. Lett.* **1986**, *48*, 133–135. [[CrossRef](#)]
26. Sze, S.M.; Ng, K.K. *Physics of Semiconductor Devices*, 3rd ed.; John Wiley & Sons, Inc.: Hoboken, NJ, USA, 2007; p. 314.
27. Yoshioka, H.; Senzaki, J.; Shimozato, A.; Tanaka, Y.; Okumura, H. Effects of interface state density on 4H-SiC n-channel field-effect mobility. *Appl. Phys. Lett.* **2014**, *104*, 083516-1-4.
28. Boesch, H.E., Jr.; McLean, F.B.; Benedetto, J.M.; McGarrity, J.M. Saturation of threshold voltage shift in MOSFET's at high total dose. *IEEE Trans. Nucl. Sci.* **1986**, *33*, 1191–1197. [[CrossRef](#)]
29. Oldham, T.R.; Lelis, A.J.; McLean, F.B. Spatial Dependence of trapped holes determined from tunneling analysis and measured annealing. *IEEE Trans. Nucl. Sci.* **1988**, *33*, 1203–1209. [[CrossRef](#)]
30. Rozen, J.; Dhar, S.; Dixit, S.K.; Afanas'ev, V.V.; Roberts, F.O.; Dang, H.L.; Wang, S.; Pantelides, S.T.; Williams, J.R.; Feldman, L.C. Increase in oxide hole trap density associated with nitrogen incorporation at the SiO<sub>2</sub>/SiC interface. *J. Appl. Phys.* **2008**, *103*, 124513. [[CrossRef](#)]
31. Lelis, A.J.; Boesch, H.E., Jr.; Oldham, T.R.; McLean, F.B. Reversibility of trapped hole annealing. *IEEE Trans. Nucl. Sci.* **1988**, *35*, 1186–1191. [[CrossRef](#)]
32. Suzuki, S.; Harada, S.; Kosugi, R.; Senzaki, J.; Cho, W.; Fukuda, K. Correlation between channel mobility and shallow interface traps in SiC metal-oxide-semiconductor field-effect transistors. *J. Appl. Phys.* **2002**, *92*, 6230–6234. [[CrossRef](#)]
33. Potbhare, S.; Goldsman, N.; Pennington, G.; Lelis, A.; McGarrity, J.M. Numerical and experimental characterization of 4H-silicon carbide lateral metal-oxide-semiconductor field-effect transistor. *J. Appl. Phys.* **2006**, *100*, 044515. [[CrossRef](#)]
34. Potbhare, S.; Goldsman, N.; Lelis, A.; McGarrity, J.M.; McLean, F.B. A physical model of high temperature 4H-SiC MOSFETs. *IEEE Trans. Electron. Devices* **2008**, *55*, 2029–2040. [[CrossRef](#)]

35. Sometani, M.; Okamoto, D.; Harada, S.; Ishimori, H.; Takasu, S.; Hatakeyama, T.; Takei, M.; Yonezawa, Y.; Fukuda, K.; Okumura, H. Threshold-voltage instability in 4H-SiC MOSFETs with nitrated gate oxide revealed by non-relaxation method. *Jpn. J. Appl. Phys.* **2016**, *55*, 04ER11. [[CrossRef](#)]
36. JEDEC. *Procedure for Wafer-Level DC Characterization of Bias Temperature Instabilities*; JESD241:2015; JEDEC: Arlington, VA, USA, 2015.



© 2019 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (<http://creativecommons.org/licenses/by/4.0/>).