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# **Dual-Level Voltage Bipolar Thermal Energy Harvesting System from Solar Radiation in Malaysia**

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**Abstract:** Harvesting energy from solar radiation in Malaysia attracts the attention of researchers to utilize the potential by ongoing improvement. Roofing material with low albedo absorbs the heat, that can then be harvested using a thermoelectric generator. Previous research only measured the open-circuit voltage with different thermoelectric generator configurations. Low power output limits the potential to be utilized. The low output power can be increased using a DC converter. However, the converter must be tuned concerning low- and high-voltage levels, bipolar, and the maximum power point tracking. Therefore, this paper presents a dual-level voltage bipolar (DLVB) thermal energy harvesting system. The circuit is tested at constant and various time intervals to evaluate the system's functionality and performance. Experiment results show that the proposed harvesting system can boost from 0.6 and 1.6 V to achieve the optimum level. The mean efficiency of the harvesting circuit obtains 91.92% at various time intervals. Further, the field test result obtains output power from 1.45 to 66.1 mW, with the mean efficiency range of 89.62% to 92.98%. Furthermore, recommendations are listed for future research.

**Keywords:** thermoelectric generator; bipolar; dual-level; real-time; solar radiation; low temperature

## **1. Introduction**

Recently, energy harvesting has become the main focus of research to provide power for devices, with various design approaches and methods for energy conversion [\[1](#page-22-0)[–4\]](#page-22-1). Solar energy is one renewable energy (RE) that is gaining attention for implementation in Malaysia [\[2\]](#page-22-2). In addition, the increase in solar power plants proves the vast amount of solar radiation available to be utilized at the optimum level. Several policies and incentives were introduced in 2016 by the Energy Commission in Renewable Energy Act 2011, which paved the way for the Feed-in Tariff (FiT) system and net energy metering (NEM) [\[3\]](#page-22-3). Commonly, solar energy is utilized from heat or light to convert it into electrical energy [\[4\]](#page-22-1). Perhaps the most popular is electrical energy, which employs solar cells. Solar energy research focuses on designing and implementing photovoltaic (PV) technologies in contrast to solar thermal technologies. Practically, solar energy is recognized as an alternative energy source for power generation and thermal applications. However, utilizing solar thermal energy requires detailed design evaluation to increase the conversion efficiency [\[3](#page-22-3)[,5](#page-22-4)[,6\]](#page-22-5).

The solar thermoelectric generator (STEG), which converts solar radiation heat into electrical energy using a thermoelectric generator (TEG), has gained researchers' attention. The STEG needs additional components, such as the optical collector, thermal storage, TEG, vacuum encapsulation, and heat sink [\[7\]](#page-22-6). In addition, the Fresnel lens and concentrator are also used to increase the solar concentration of the TEG. However, it makes the system bulky, concerning the roof-mount for domestic implementation. Furthermore, research



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has been conducted on improving the performance in different ways [\[8,](#page-22-7)[9\]](#page-22-8). Some of these methods, for instance, include using PCMs [\[10\]](#page-22-9), nano-enhanced PCMs [\[11](#page-22-10)[–13\]](#page-22-11), thermoelectric cooler [\[14\]](#page-22-12), TEGs [\[15\]](#page-22-13), nanofluids [\[16](#page-22-14)[–18\]](#page-22-15), nano-enhanced coatings [\[19\]](#page-22-16), electrical heaters [\[20\]](#page-22-17), and a solar collector [\[21\]](#page-22-18). Table [1](#page-2-0) summarizes TEG's integration in the hybrid system to harvest thermal energy. The methods used mainly depend on the specific design of the solar system and the possibilities observed for performance enhancement or taking advantage of the further availability in the system to generate other types of energy. TEGs tend to improve the efficiency of the hybrid system at low-temperature values, but most of the power generated is from the PV. The emergence of TEG material technology to improve the figure of merit, ZT, will help to increase the TEG's performance to harvest thermal energy. The TEG must be separate from the hybrid system to evaluate the TEG's performance of harvesting thermal energy from solar radiation [\[22\]](#page-22-19). Therefore, the capability of the TEG in a simple experiment and field setup is conducted through this research, focusing on roofing materials.

**Table 1.** The comparison of different solar thermal implementation.





<span id="page-2-0"></span>**Table 1.** *Cont.*

Fewer studies on the application of solar thermal technologies have been carried out in Malaysia, as solar thermal is still an emerging technology compared to PV technology. Therefore, Malaysia has a promising development for solar energy due to its tropical climatic conditions, which give abundant sunlight and high irradiance levels, with a solar radiation level from 4.21 to 5.56 kWh/m $^2$  [\[39\]](#page-23-14). From Malaysia's perspective, the research

Table [2.](#page-3-0)



<span id="page-3-0"></span>**Table 2.** The summary of thermal energy harvesting from solar thermal energy.

related to thermal energy harvesting (TEH) from solar thermal energy is summarized in

Based on Table [2,](#page-3-0) the research on thermal harvesting in Malaysia only measures the open-circuit voltage, Voc, of TEG due to solar radiation. Recently, two studies have implemented the thermal energy harvesting system (TEHS) to evaluate the potential of the TEG. However, the TEHS used is only suitable for unipolar conditions.

On the other hand, the houses are exposed to sizeable radiant heat gains on roofs due to higher solar altitudes during the daytime [\[49\]](#page-23-24), with the hottest time being from 11.30 a.m. to 4.30 p.m. [\[42\]](#page-23-17). Heat gain from the roof contributed 70% of total heat gain in tropical houses [\[49,](#page-23-24)[50\]](#page-23-25). Moreover, the building is directly exposed to solar radiation-stored thermal energy, especially on the roof, due to the low albedo characteristic of the roof material [\[42,](#page-23-17)[51\]](#page-23-26).

In further research [\[52\]](#page-24-0), the temperature difference in a building was obtained as 1.6 to 1.9 ℃ from 1.00 p.m. until 5.00 p.m. A field study by Toe and Kubota [\[53\]](#page-24-1) on a two-story modern terrace house in Malaysia demonstrates that the surface temperature of the ceiling beneath the roof recorded the highest value during the daytime, which was 2.7 ◦C higher than the room air temperature. Besides daytime, the temperature difference is also available at night [\[54\]](#page-24-2). Therefore, it should be evaluated for potential thermal energy.

Furthermore, most residential buildings' roofs are under substantial solar radiation during the day, resulting in as high as  $60^{\circ}$ C in the attics compared to the conditioned living space underneath [\[55\]](#page-24-3). Therefore, harvested thermal energy from the roof and attic areas can be evaluated. The authors of [\[56\]](#page-24-4) emphasized that research must be conducted to assess the feasibility and viability of solar thermal energy at low temperatures. The author of [\[46\]](#page-23-21) evaluated the potential of TEGs on shingle, focusing on the effect of solar radiation on temperature differences. The result indicates that the output power is 65.22  $\mu$ W at 138  $\Omega$  from the experiment, with a temperature difference at a TEG of 3 °C. However, the research only measured the open-circuit voltage of the TEG. The author emphasized the development of a thermal energy harvesting system. Therefore, further research must evaluate the full potential of thermal energy harvesting integrated with the storage system.

The TEG is a maintenance-free semiconductor device suitable for converting thermal energy from solar radiation into electrical energy by establishing a temperature difference across thermoelectric devices. The TEG converts thermal energy to electrical energy via the principle of Seebeck [\[57\]](#page-24-5). The generated power depends heavily on the temperature difference, which will inevitably fluctuate, especially in real-time applications. A disadvantage of this energy conversion method is its efficiency. The efficiency of the thermoelectric module as an energy converter is low  $[4]$ . However, a TEG has a high power density among various energy sources [\[58\]](#page-24-6). Still, the polarity of the TEG voltage output is unpredictable since the temperature difference between the hot and cold sides of the TEG is reversible in real-time implementation [\[59](#page-24-7)[–61\]](#page-24-8). Therefore, it is compulsory to design and develop a bipolar circuit system to be practically used in real-time thermal energy harvesting. However, the bipolar-input harvester is more complicated than the conventional harvesting circuit [\[62\]](#page-24-9). In addition, the output voltage from the TEG is too low to power up these devices. Thus, a DC converter is essential to increase the low-voltage input to the high-voltage output. For specific power supply scenarios, additional components could be selectively configured.

A DC-to-DC boost converter can boost the output voltage of the TEG to an appropriate higher level, and an energy storage unit such as a battery or capacitor can be loaded for emergencies. A maximum power point tracking (MPPT) can draw the maximum available output power of the TEG by matching its input impedance with that of the source [\[57](#page-24-5)[,63\]](#page-24-10). The authors of [\[58\]](#page-24-6) proposed a DC boost converter with bipolar-input bipolar-output, with maximum power point tracking (MPPT), zero-crossing switching (ZCS), and a control logic circuit. However, the converter operated in discontinuous mode (DCM) at a constant voltage level. In [\[62\]](#page-24-9), the bipolar circuit integrated with a boost/flyback hybrid converter and maximum power point tracking (MPPT) at a low-voltage level is proposed at a constant voltage. On the other hand, the input TEG voltage level is an additional factor to consider [\[59\]](#page-24-7). The input TEG voltage is proportional to the TEG array configuration and the temperature gradient.

Therefore, designing the thermal energy harvesting circuit from solar radiation in real-time implementation should consider the electrical bipolarity, the low and high input voltage from TEG, the DC boost converter, the TEG array configuration, and the rapid temperature fluctuation. Thus, this research work describes the development of a thermal energy harvesting circuit that considers all the factors integrated with MPPT to obtain optimum power transfer for real-time implementation. In detail, the novelty and contributions of this research work are in designing the dual-level voltage bipolar (DLVB) harvesting system to harvest thermal energy due to solar radiation in Malaysia. In addition, the system is integrated with the MPPT algorithm and DC boost converter to improve its performance.

The thermoelectric generator is described in Section [2,](#page-4-0) followed by the method and instruments of the preliminary work for real-time data, explained in Section [3.](#page-6-0) Then, Section [4](#page-9-0) briefly describes the details of the design and development of the thermal energy harvesting system structure and the system's topology in this research work. The result of the harvesting circuit is presented in Section [5.](#page-12-0) Next, the results are discussed in Section [6.](#page-18-0) Finally, the conclusion and recommendations for future work are presented in Sections [7](#page-20-0) and [8,](#page-20-1) respectively.

#### <span id="page-4-0"></span>**2. Thermoelectric Generator**

A thermoelectric generator consists of p-type and n-type semiconductors connected electrically in series and thermally in parallel. Figure [1](#page-5-0) shows the 1D schematic of a TEG along with the heat source ( $Q_H$ ), heat dispersed ( $Q_C$ ), internal resistance ( $R_{TEG}$ ), and hot side (T<sub>H</sub>) and cold side (T<sub>C</sub>) temperature.

The heat balance at the hot side is described as Equation (1), and at the cold side as Equation (2):

$$
Q_H = \alpha T_H I + \left(\frac{\lambda A}{L}\right) \Delta T - 0.5 I^2 R_{TEG} \tag{1}
$$

$$
Q_C = \alpha T_C I + \left(\frac{\lambda A}{L}\right) \Delta T - 0.5 I^2 R_{TEG}
$$
\n(2)

where  $\alpha$  is the Seebeck coefficient,  $T_H$  is the hot side temperature,  $T_C$  is the cold side temperature,  $\lambda$  is the thermal conductivity, *L* is the length, *A* is the area, and  $\Delta T$  is the temperature difference. The heat was dispersed to the ambient from the cold side, which can be described by Equation (3):

$$
Q_C = K(T_C - T_{amb})
$$
\n(3)

where K is thermal conductance and  $T_{amb}$  is ambient temperature. The temperature difference ( $\Delta T$ ) between the hot and cold sides of the TEG is determined using Equation (4): along with the heat source ( $\mathcal{L}_{\text{H}}$ ), internal resistance ( $\mathcal{L}_{\text{H}}$ ), and hotels and hote

$$
\Delta T = T_H - T_C \tag{4}
$$

<span id="page-5-0"></span>

**Figure 1.** Schematic diagram of heat flux distribution of the TEG. **Figure 1.** Schematic diagram of heat flux distribution of the TEG.

The current (I) in the module can be expressed by Equation (5):

$$
I = \frac{V_{oc}}{R_{TEG} + R_{load}}
$$
 (5)

The open-circuit voltage is calculated using Equation (6):<br>

$$
V_{oc} = \alpha \Delta T \tag{6}
$$

Optimum power output, *P*<sub>*max*</sub>, is determined by Equation (7):

$$
P_{max} = \frac{V_{oc}^2}{4R_{TEG}}\tag{7}
$$

The efficiency of power transfer is obtained using:

$$
\eta_P = \frac{P_{out}}{P_{TEG}} \times 100 \tag{8}
$$

where *Pout* is the output power of the DC converter and *PTEG* is the TEG input power

## *2.1. TEG in the Series Array Configuration*

The internal resistance and Seebeck coefficient are assumed constant parameters in the series TEG configuration. The module may experience different temperatures under non-uniform heat flux conditions, Δ*T*<sub>1</sub>, Δ*T*<sub>2</sub>, . . . , Δ*T*<sub>N</sub>, respectively. Thereby, Equation (1) becomes Equation (9) due to the non-uniform heat flux condition. The heat balance at the cold side for each module is also different, as shown in Equation (10).

$$
Q_{HN} = \alpha T_{HN} I + \left(\frac{\lambda A}{L}\right) \Delta T_N - 0.5 I^2 R_{TEG}
$$
\n(9)

$$
Q_{CN} = \alpha T_{CN} I + \left(\frac{\lambda A}{L}\right) \Delta T_N - 0.5I^2 R_{TEG}
$$
\n(10)

The heat dissipated to the ambient is expressed by Equation (11):

$$
Q_{CN} = K(T_{CN} - T_{amb})
$$
\n(11)

The open-circuit voltage and current were calculated using Equations (12) and (13), respectively:

$$
V_{oc} = \alpha(\Delta T_1 + \Delta T_2 + \dots \Delta T_N)
$$
\n(12)

$$
I = \frac{V_{oc}}{NR_{TEG} + R_{load}}
$$
 (13)

#### *2.2. TEG in the Parallel Array Configuration*

The heat balance equation in a parallel array configuration is similar to the series configuration. According to the Kirchhoff law, the relation of the current is  $I = I_1 + I_2 + ... + I_N$ . Thereby, the current is calculated using Equations (14) and (15):

$$
I_N = \frac{V_{ocN} - V_P}{R_{TEG}}\tag{14}
$$

$$
I = \frac{V_p}{R_{load}} \tag{15}
$$

where  $V_p$  is the load resistance voltage. For maximum output power, the internal resistance is equal to the load resistance. Therefore,

$$
R_{load} = \frac{1}{N} R_{TEG} \tag{16}
$$

$$
V_p = \frac{V_{oc1} + V_{oc2} + \ldots + V_{ocN}}{2_N} \tag{17}
$$

#### <span id="page-6-0"></span>**3. Method and Instrument of Preliminary Work for Real-Time Data**

The preliminary work of this research is illustrated in Figure [2.](#page-7-0) The characteristic of the temperature profile in real-time implementation should be considered to design the thermal energy harvesting circuit. Therefore, the process conducted to obtain all the necessary data to develop the harvesting circuit is briefly explained in this section.

An experiment was conducted at stage 1 to measure the temperature and the opencircuit voltage. The TEC1-12706 module ( $40 \times 40$  mm) parallel array configuration covers a roof area of 1 m<sup>2</sup>. In addition, the module was sandwiched between the roofing material (hot side) and the aluminum heatsink (cold side). The heatsink is used for heat dissipation by nature wind blowing. A thermal paste (Cooler master HTK-002) increases the thermal conductivity at TEG hot and cold sides. Specific modules from National Instrument, NI-9211 (temperature) and NI-9201 (voltage), were used to measure the temperature and voltage. A K-type thermocouple was used to measure the hot and cold side temperatures. The measuring system was integrated with the NI cRIO-9014 controller module, which records all experiment data and synchronizes with the computer. LABVIEW software was used to design the layout for the data recording. The data were recorded for 24 h at every second interval for 20 days. The experiment shows the amount of a temperature difference at the TEG that can be harvested. Based on the experiment, the maximum temperature difference range is from 0.3323 ◦C to 0.4871 ◦C (morning, 12.00 a.m. to 6.00 a.m.), 0.3407 ◦C to 0.7842  $\degree$ C (evening, 6.00 p.m. to 12.00 a.m.), and 2.0  $\degree$ C to 6.072  $\degree$ C (day, 6.00 a.m. to 6.00 p.m.). Refer to [\[2\]](#page-22-2) for details. From the results, understanding the real-time temperature variation at TEG's hot and cold sides due to solar radiation in Malaysia was achieved.

<span id="page-7-0"></span>

**Figure 2.** The stages of the preliminary experiment for real-time thermal energy harvesting. **Figure 2.** The stages of the preliminary experiment for real-time thermal energy harvesting.

From the experiment, the real-time temperature data show several characteristics: fluctuation, reverse conditions, low- and high-temperature differences, and sudden temperature changes. Concerning the experiment data, stage 2 is the evaluation of the temperature data at the TEG using MATLAB Simulink. First, real-time data were used in the simulation process. Then, the simulation protocol was designed to determine the TEG's open-circuit voltage at variable loads (90, 125, 200, 250, 503, and 600 Ω). From the analysis, the maximum voltage was proportional to the load value. Therefore, impedance matching was observed. In addition, the effect of the variable load to obtain the maximum power transfer was determined. Refer to [\[64\]](#page-24-11) for details. At this stage, absolute data analysis (ADA), as shown in Figure [3,](#page-8-0) and synchronous data management methods were proposed to process the real-time temperature data due to the mixed data (positive and negative values), rapid temperature fluctuation, intersection point, temperature variation range, and reverse condition. Using the conventional method, the real-time data must be processed part-by-part, leading to technical errors when determining the exact point of reverse transition. Therefore, the ADA method was applied to the TEG equivalent circuit to obtain the statistical parameter of real-time data (~86,000 per day, 1,720,000 for 20 days) for further analysis. Compared to the conventional method [\[65\]](#page-24-12), the ADA method indicates an accurate statistical parameter value. Using this method, rigorous analysis can be performed in terms of real-time data. Refer to [\[65\]](#page-24-12) for the result.

<span id="page-8-0"></span>

**Figure 3.** The block diagram of the ADA method of an equivalent TEG circuit. **Figure 3.** The block diagram of the ADA method of an equivalent TEG circuit.

The maximum power point (MPP) can be achieved by matching the impedance between the TEG internal resistance and the load. However, the real-time characteristic data makes it hard to achieve compared to constant temperature differences. Therefore, another option is to use the maximum power point tracking (MPPT) algorithm to track the MPP. The simple, fast response and ease of troubleshooting is the key to selecting the MPPT algorithm. However, the MPPT algorithm needs proper tuning, referring to the type of application, response time, parameter range, and input signal co[ndi](#page-24-13)tion [66]. Thereby,<br>the make time of the coefficience of fisicom and account concert the MPFT changitan the evaluation of the oscillation range, efficiency, and power losses of the MPPT algorithm inc evaluation of the osemation range, emerging, and power losses of the rm 11 digorithm.<br>is performed at stage 3. The frequency range of 10 to 60 kHz with 5 kHz increments was tested using the real-time temperature data. From the simulation, 55 kHz obtained the lowest oscillation range (0.0012), higher efficiency (84.12%), and the lowest power losses (4.55%) compared to other frequencies. Therefore, 55 kHz indicates the suitable frequency value for the controller.

> Based on the simulation results from stage 1 to stage 3 of real-time data, the design and development of the TEHS, considering all the values, was fabricated for field tests setup of the TEHS to harvest setup of the TEHS to harvest setup of the TEHS to harvest setup of the field test setup of the TEHS to ha (real-time implementation). Figure [4](#page-8-1) presents the field test setup of the TEHS to harvest thermal energy from solar radiation. It consists of a TEG module, roofing materials, and thermal energy from solar radiation. It consists of a TEG module, roofing materials, and the DLVB. The equipment setup was left exposed to solar radiation and weather changes the DLVB. The equipment setup was left exposed to solar radiation and weather changes to evaluate the effectiveness of TEHS in real-time conditions. The following section will to evaluate the effectiveness of TEHS in real-time conditions. The following section will briefly explain the design and development of the TEHS. briefly explain the design and development of the TEHS.

<span id="page-8-1"></span>

**Figure 4.** The real-time field test setup. **Figure 4.** The real-time field test setup.

## <span id="page-9-0"></span>**4. Energy Harvesting Circuit Design and Development**

This section explains the design and development of the thermal energy harvesting circuit, including the working principle. This type of harvesting circuit is proposed based on the TEG input voltage characteristic discussed in Section [3.](#page-6-0)

#### *4.1. Topology of the Harvesting Circuit*

Figure [5](#page-9-1) represents the stage of the proposed thermal energy harvesting circuit. Again, the DLVB is considered in designing the harvesting circuit, which is low voltage (<1 volt) and high voltage (>1 volt).

<span id="page-9-1"></span>

**Figure 5.** The stage of the thermal energy harvesting circuit. **Figure 5.** The stage of the thermal energy harvesting circuit.

At stage 1, the TEG input voltage is a random value depending on solar radiation and environmental conditions. It consists of low and high voltage, including the bipolar requirement for both voltage levels. Two operational amplifiers, ADC 1 and ADC 2, were used to detect the input value's level and condition, capable of categorizing the input (low and high voltage). It received the signal from VA and VB connected to the TEG input voltage with a voltage divider circuit. The value of R3 and R4 depends on the TEG input voltage. ADC 1 and ADC 2 transmit the data to the controller when the input value is confirmed. Then, the controller triggers the selector relay accordingly based on the programming setup. The initial position of the relay is connected to the low-voltage level circuit. When the voltage reaches a high-voltage level, the relay automatically switches to the high-voltage level circuit (stage 2).

In addition, the high-voltage level goes to the bipolar circuit before the DC boost converter. At this point, the bipolar circuit is compulsory due to the polarity of input voltage to the DC boost converter. Two voltage and current sensors at the input and output MPPT algorithm programmed in the controller tracks the MPP. On the other hand, if the **Parameter Rechargeable Battery Supercapacitor**  input voltage from the TEG is below 1 volt, the low-voltage level circuit will work (stage 3). This circuit consists of an inductor transformer integrated with a specified capacitor value. LTC 3109 (Texas Instruments) played a role in receiving and processing the input voltage value and amplifying the output depending on the VS1, VS2, and Vaux connection. The output voltage from both high and low circuits is stored in the storage component. Here, a<br>2.5 and 1.1 in the store of the store in the store of the 3.7-volt lithium-ion battery (4200 mAH) with a charger module (TP4056) was used (stage 4).<br>T The screenor of energy storage constants several nectors: sen alsemarge rate, enarging and discharging characteristics, cycling stability, and price. The comparison of the storage *4.2. Thermal Harvesting Circuit*  element is briefly presented in Table [3.](#page-10-0)of the DC boost converter sense the voltage value and transmit it to the controller. Then, the The selection of energy storage considers several factors: self-discharge rate, charging and



<span id="page-10-0"></span>Table 3. The comparison of selected parameters of a rechargeable battery and a supercapacitor. duality can compute was compared at  $\frac{1}{2}$  and  $\frac{1}{2}$  and  $\frac{1}{2}$  and  $\frac{1}{2}$  and  $\frac{1}{2}$  was compared at  $\frac{1}{2}$ 

# **4.2. Thermal Harvesting Circuit**

time of each piece of data.

The thermal energy harvesting circuit design comprises several modules combined on one board, as presented in Figure 6. PROTEUS software was used to design the harvesting circuit and the printed circuit board (PCB). The details of the components used in the harvesting circuit are listed in Table 4. The selection of each component used on the circuit board goes through comprehensive experimental testing to evaluate each module's robustness, endurance, and capability range. Furthermore, all the voltage and current values from the harvesting circuit are stored in the microSD card module, with 64 Gb capacity. Thus, expanding the amount of data stored for further analysis and the duration of the field test experiment. The real-time clock module (RTC 3231) records the present time of each piece of data.

<span id="page-10-1"></span>

**Figure 6.** Schematic diagram of bipolar dual-level voltage circuits. **Figure 6.** Schematic diagram of bipolar dual-level voltage circuits.

The input voltage from the TEG is connected to J5 without considering the electrical polarity. The voltage divider circuits VA and VB' detect the input voltage value with the dual-polarity capability. Thus, the value was compared at ADC 1 and ADC 2. ADC 1 was connected to ADC 0 (pin23) and ADC 2 to ADC 1 (pin24) at the AT328 microcontroller. If the input voltage is positive at the VA and VB' is negative, ADC 1 will send the signal to the controller. The process is reversed when the VA is negative and VB' is positive—ADC

2 will send the signal to the controller. The signal from ADC 1 and ADC 2 is sent to the controller to evaluate and sent back to the DPDT relay circuit (signal from the controller). If the input voltage is below 1 volt, the relay switches to a low-voltage level circuit and vice versa. This process continuously occurs along the circuit work.

Label	Module/Port	Description			
U1, U2	Voltage signal	Gain at 5			
ADC 1, ADC 2	Operational amplifier/Comparator	AD627			
R3, R4	Resistor	$1.5\,\mathrm{K}\Omega$			
R <sub>5</sub> , R <sub>6</sub>	Resistor	$100 \Omega$			
R7	Resistor	$1 K\Omega$			
R <sub>13</sub>	Resistor	$100 \Omega$			
R14	Resistor	$10 K\Omega$			
<b>DPDT</b>	Double pole double throw relay	5V			
Q1, Q2	<b>MOSFET</b>	<b>IRF7319</b>			
Q <sub>3</sub>	<b>MOSFET</b>	<b>IRLZ44N</b>			
Q4	Transistor	<b>BC548</b>			
D <sub>3</sub>	Diode	1N4148			
D2	Diode	1N5817			
L1	Inductor	$100 \mu H$			
C1	Capacitor	$47 \mu F$			
C1U	Capacitor	$47~\mu F$			
C <sub>2</sub> U, C <sub>4</sub> U	Capacitor	470 pF			
C <sub>3</sub> U, C <sub>5</sub> U	Capacitor	10 <sub>nP</sub>			
C6U	Capacitor	$1 \mu F$			
C7U	Capacitor	470 μF			
<b>TR1, TR2</b>	Step-up transformer	$25 \mu H$			
3109	Controller	LTC 3109			
<b>PWM Pulse</b>	Gate controller	AT328 microcontroller with MPPT			
<b>INA 219</b>	Voltage and current sensor	Serial connection to Vin+ and Vout+			

<span id="page-11-0"></span>**Table 4.** Detailed descriptions of each component in the harvesting circuit.

The bipolar circuit (Q1 and Q2) at a high-voltage level circuit was used to change the electrical polarity accordingly before connecting to the DC boost converter circuit. Thereby, the polarity has followed the polarity at the DC boost converter. Therefore, a DC boost converter was selected to increase voltage levels from low to high. The drawback is that the current is decreased [\[67\]](#page-24-14). At this point, the voltage and current sensor module (INA 219) was used to trace the input voltage and current value, and thus sent to the controller. A similar process occurs at the output of the DC boost converter. Concerning the sensor's input and output voltage values, the controller's MPPT algorithm will evaluate both values and adjust the duty cycle to track the maximum power point (MPP). Then, pulse width modulation (PWM) works according to a new value to control the MOSFET. Resistors (R13 and R14) ensure that the MOSFET gate is fully closed to prevent switching losses.

For low-input voltage (Vinlow), two step-up transformers with a coil ratio of 1:20 with coupling capacitors (C2U, C3U, C4U, and C5U) allow it to boost the input voltage. Typically, the oscillation frequency is determined by the inductance transformer secondary winding from 10 to 100 kHz. LTC 3109 can accurately regulate the output voltage when the Vaux is 2 V. The VS1 and VS2 at the LTC 3109 pin were tied to Vaux to achieve 5 V of the output voltage (Voutlow).

Preliminary testing of the harvesting circuit has been conducted to evaluate the functionality and performance of the proposed system for real-time implementation. DC power supply (GPC-3020, GW INSTEK) was used as the input voltage at [5. The voltage range was from 0.2 to 4 V, with a 0.2 V interval. The current from the DC power supply was set at

100 mA. Regarding voltage fluctuation from the preliminary experiment, the testing time duration for each voltage range was 10 s (constant time), with various time intervals.

#### <span id="page-12-0"></span>**5. Result**

This section presents and discusses the experimental testing result of the thermal energy harvesting circuit component. The experimental method was chosen to evaluate the performance and endurance of each component according to the input voltage from the TEG to prove the component's reliability in dealing with the fluctuation and reverse polarity of the TEG input voltage. Furthermore, a comparison among the component types was accomplished through the experimental method.

## *5.1. Individual Stage Experimental Testing 5.1. Individual Stage Experimental Testing*

Figure 7 shows the voltage selector switching result based on the normal (N) and Figure [7](#page-12-1) shows the voltage selector switching result based on the normal (N) and reverse (R) polarity tested according to the TEG input voltage from 0.1 to 5 V. As a result, reverse (R) polarity tested according to the TEG input voltage from 0.1 to 5 V. As a result, the  $V_A$  obtained a positive projection proportional to the increasing input voltage, while  $V_B$  remained at zero value. In contrast,  $V_B$  behavior was parallel to the increase of the input voltage. Therefore, a response between  $V_A$  and  $V_B$  related to the TEG input voltage condition was observed. tion was observed.

<span id="page-12-1"></span>

**Figure 7.** Voltage selector switch. **Figure 7.** Voltage selector switch.

The experimental result of IRF7319 consists of a combination of P- and N-channels The experimental result of IRF7319 consists of a combination of P- and N-channels for the bipolar circuit, as shown in Fig[ure](#page-13-0) 8. The MOSFET was tested at normal and reverse polarity to evaluate the output voltage from the MOSFET. The result indicates that the the minimum voltage was 1 volt, achieved without losses compared to a voltage range minimum voltage was 1 volt, achieved without losses compared to a voltage range below 1 volt. At the low-voltage level, the MOSFET indicated the losses with a maximum value of 30% obtained at 0.4 volts. However, the losses did not affect the implementation of the circuit because this bipolar circuit was used at a high-voltage level (>1 V). Thereby, the performance of the MOSFET achieved 100%.

Figure [9](#page-13-1) presents the results of three different types of MOSFETs used in the DC converter circuit. IRLZ44N indicated a higher performance compared to IRF540N and IRFZ44N. IRLZ44N is a logic-level MOSFET, requiring a minimum gate-source voltage  $(V_{\text{GS}})$  of 1 volt to operate. In contrast, IRF540N and IRFZ44N required a minimum  $V_{\text{GS}}$ of 2 volts. Therefore, it afforded the MOSFET and the DC converter circuit advantages to boost the voltage at a specific level. For example, at 0.5-volt, equivalent output voltage was



<span id="page-13-0"></span>obtained from all the MOSFETs and increased for each voltage increment. From 1 to 4 volts,  $\rm IRLZ44N$  performed well in the task as the switching component in the DC boost converter. value of 30% obtained at 0.4 volts. However, the losses did not affect the implementation  $\alpha$  $\delta$  of the circuit because the circuit because the circuit was used at  $\delta$  and  $\delta$  voltage increment. From  $1$  to  $4$  Voltage  $\delta$ 

 $v_{\rm{eff}}$  to evaluate the MOSFET. The  $\epsilon$ 

Figure 8. Bipolar circuit result.

<span id="page-13-1"></span>

**Figure 9.** Comparison of MOSFETs used in the DC converter. **Figure 9.** Comparison of MOSFETs used in the DC converter.

Furthermore, the DC boost converter circuit was tested at different duty cycle values Furthermore, the DC boost converter circuit was tested at different duty cycle values from 0.2 to 0.8 to evaluate the circuit's efficiency and endurance, as shown in Figure [10.](#page-14-0) In addition, the input voltage range was between 0.2 V and 10 V, with a MOSFET switching addition, the input voltage range was between 0.2 V and 10 V, with a MOSFET switching frequency value of 55 kHz. From the experimental testing, the 0.2 duty cycle obtained constant efficiency of 80% from 1.8 volts, with a maximum efficiency of 95.7% at 10 V. a constant efficiency of 80% from 1.8 volts, with a maximum efficiency of 95.7% at 10 V. Compared to 0.4 duty cycle value, the maximum efficiency is 89.5%. In contrast, 0.7 and Compared to 0.4 duty cycle value, the maximum efficiency is 89.5%. In contrast, 0.7 and 0.8 duty cycle values caused failure in the load resistance at 5.8 and 4.2 V, respectively. 0.8 duty cycle values caused failure in the load resistance at 5.8 and 4.2 V, respectively.

## *5.2. Circuit Experimental Test*

The TEG high- and low-voltage levels were extracted from the experimental results and plotted for comparison. For a clear view of the voltage generated from the harvesting circuit, the low and high voltages are plotted in Figure [11.](#page-14-1) It is seen that the low- and high-voltage level circuits can boost the input voltage at different capabilities. Although the low-voltage circuit directly implemented the on-shelf component, it fulfilled the circuit design's objective to harvest thermal energy at the low-voltage level. The low-voltage level circuit indicates that the voltage can be above 3.6 V at 0.6 to 0.8 V.

<span id="page-14-0"></span>

Figure 10. Comparison of the DC converter duty cycles.

<span id="page-14-1"></span>

**Figure 11.** Input and output voltage from the low- and high-voltage levels. **Figure 11.** Input and output voltage from the low- and high-voltage levels.

A high-voltage level DC converter indicates a good response to boost the input voltage above 1 V. It was observed that starting from 1.6 V, the voltage increased above 3.5 V and was constant along the increase of the input voltage. At this point, it was observed that the battery was in changing state. Furthermore, when using the battery as the load, the output voltage of both low- and high-voltage level circuits was capped at approximately 4 V. It is aligned with the nominal battery voltage at 3.6 V [68].

Moreover, the current indicates the increasing value proportional to the voltage, as presented in Figure [12](#page-15-0) The range at low-voltage levels was from 0.1 to 0.8 mA. While, for high-voltage levels, the current achieved 12 mA. The importance of the current value is to determine the potential to charge the battery. Although the voltage is high, it is not guaranteed to charge the battery without enough current, thus affecting the charging time. The current trend is aligned with the theoretical aspect, which is proportional to the voltage increase.

On the other hand, the harvesting circuit testing not only focused on the voltage and current values but also considered the duty cycle response to the present input value. The controller used the MPPT algorithm to track the MPP by adjusting the duty cycle based on the present input and output values, as shown in Figure [13.](#page-15-1) The duty cycle was at a steady state (point 1 and 1') until a sudden increment of the input voltage value from 0.79 (point 1) to 1.26 V (point 2) occurred. In addition, the response of the duty cycle from 18 (point  $1'$ ) to 16 (point 2') is in good agreement with the present value to track the MPP. Moreover, the present input value decreased from 1.26 (point 2) to 1 V (point 3), and the duty cycle tended to increase the value from 16 (point  $2^7$ ) to 18 (point 3<sup>7</sup>). Then, the duty cycle was constant at 16 (point 4') due to the constant input value. Significantly, there was no lagging between the input voltage and the duty cycle response. Therefore, tuning the duty cycle for further implementation in real-time behavior is achievable.

<span id="page-15-0"></span>

**Figure 12.** The output current of low- and high-voltage levels.

<span id="page-15-1"></span>

**Figure 13.** The duty cycle responds to the fluctuation of the input voltage.

Moreover, the overall data of the harvesting circuit is plotted in Figur[e 14](#page-16-0), focusing Moreover, the overall data of the harvesting circuit is plotted in Figure 14, focusing on the harvesting circuit's efficiency. The time for each voltage level was not set to constant time, and it varied to show an overview of the system response. As discussed previously with the voltage result, the output voltage achieved above  $3.5$  V at a  $1.6$  V input voltage. The output voltage was constant above 3.5 V until the input voltage was 4 V. For this setu Figure 13. The duty cycle responds to the fluctuation of the input voltage.<br>Moreover, the overall data of the harvesting circuit is plotted in<br>the harvesting circuit's efficiency. The time for each voltage level v<br>time, an The output voltage was constant above 3.5 V until the input voltage was 4 V. For this setup, the mean efficiency of the harvesting system was 91.92%.

## *5.3. Circuit Field Test*

Further, the DLVB was tested at the field test of real-time implementation to evaluate the output from the TEG. In addition, the performance of the DLVB can be determined by referring to the fluctuation of the temperature difference. At this point, the DLVB was fully exposed to the present solar radiation and weather conditions for 51 days. Therefore, the mean of input and output voltage results is presented in Figure [15.](#page-16-1) Based on the graph, the input voltage range was from 1.245 (day 18) to 3.437 V (day 6). For the output voltage, the range was 2.099 (day 22) to 3.847 V (day 42).

<span id="page-16-0"></span>

Figure 14. The input and output voltages, duty cycle, and efficiency of the harvesting circuit at various voltages and timeframes.

<span id="page-16-1"></span>

**Figure 15.** Mean voltage of real-time field test. **Figure 15.** Mean voltage of real-time field test.

Moreover, the mean input and output currents are shown in Figure [16.](#page-17-0) The maximum mean input current obtained was 18.78 mA (day 9), with a minimum of 1.066 mA (day 11). In addition, the maximum mean output current was 16.64 mA (day 9), and the minimum minimum was 0.464 mA (day 18). was 0.464 mA (day 18).

Further, the maximum mean input power was 74.1 mA (day 9), and the minimum mean Figure [17](#page-17-1) presents the mean input and output power. The maximum mean output power was 66.1 mW (day 9), and the minimum mean output power was 1.45 mW (day 18). input power was 1.56 mA (day 18).

The efficiency of the TEHS in the field test is portrayed in Figure [18.](#page-17-2) The maximum mean efficiency was 92.98% (day 43), and the minimum efficiency was 89.62% (day 9).

<span id="page-17-0"></span>

<span id="page-17-1"></span>Figure 16. Mean currents of the real-time field test.



**Figure 17.** Mean power of the real-time field test. **Figure 17.** Mean power of the real-time field test.

<span id="page-17-2"></span>

**Figure 18.** Mean efficiency of the real-time field test. **Figure 18.** Mean efficiency of the real-time field test.

## <span id="page-18-0"></span>**6. Discussion**

Typically, research related to TEG is categorized into three interdependent domains: (a) improvement of material science, (b) the adaptation of the TEG at the lowest possible degradation compared to its nominal performance, and (c) implementation of the TEG and ways to improve TEG systems considering the demands of those applications [\[22\]](#page-22-19). Harvesting the thermal energy in a real-time situation is challenging. Therefore, many parameters should be considered when designing and developing the thermal energy harvesting system (TEHS).

The comprehensive preliminary work has been conducted and divided into three stages. The brief explanation regarding the method and instrument of the preliminary work was in Section [3,](#page-6-0) where the setup implementation focused on thermal energy from the roofing materials due to solar radiation and environmental conditions in Malaysia. From the simulation result, a temperature difference was obtained in the range of 2 to 6  $\degree$ C (day), 0.3323 °C to 0.4871 °C (morning), and 0.3407 °C to 0.7842 °C (evening). According to the previous research, research at a low-temperature difference has been performed to evaluate the potential energy. However, certain research only measures the TEG's open-circuit voltage, especially for the roofing material. In addition, the researchers reported an output power value of 65.22 μW at 138  $\Omega$  ( $\Delta$ T = 3 °C).

Theoretically, a low temperature produces a low output power, thus limiting the generated power usage. Typically, a DC boost converter is a solution to the problem by increasing the input power. Therefore, a TEHS is necessary to design and develop based on the local environment to boost the input value with a load. In addition, the switching in the DC boost converter must be tuned according to the input parameter [\[69](#page-24-16)[,70\]](#page-24-17). Thereby, the tuning of the frequency and duty cycle of the MPPT algorithm is conducted. The input signal to the controller is from the voltage and current sensor. Here, 55 kHz indicates the suitable frequency to implement with a 0.012 oscillation range, 4.55% power losses, and 84.12% efficiency. The result was compared with the frequency range from 10 to 60 kHz, with a 5 kHz increment.

In a real-time application, the temperature varies depending on the amount of solar radiation and weather conditions. Thus, it will affect the output voltage from the TEG. In addition, the output voltage from the TEG has several characteristics: the fluctuation of the voltage level and reverse polarity. Thus, the TEG can be used in various fields if there is a temperature difference between the hot and cold sides. Concerning the implementation of the TEG, a DLVB harvesting circuit was proposed in this work to maximize energy harvesting.

Further, the voltage divider will sense the input voltage from the TEG at a positive and a negative value. At this point, ADC 1 and ADC 2 transmit the input signal to the controller. Then, the selector relay will trigger according to the present input voltage value from the low- to the high-voltage circuit. The low-voltage circuit indicates that the voltage achieved a 3.5 V output voltage starting a from 0.6 V input voltage. Below 0.6 V, the voltage increased, but not enough to charge the battery. It can be seen in Figure [11](#page-14-1) that the low-voltage circuit changed to a high-voltage circuit at 1 V. Thereby, the objective of the dual-level circuit was achieved. For the high-voltage circuit, the converter can boost the input voltage starting from 1 V. At this point, there is not enough voltage to charge the battery. When the input voltage increased at 1.6 V and above, the output voltage was constant above 3.5 V. Therefore, starting from 1.6 V, the output voltage can charge the battery.

The output current from the converter showed an increasing trend parallel to the increase in voltage. However, in a real-time application, the TEG's internal resistance was affected by the present temperature [\[71\]](#page-24-18). Therefore, the impedance matching between the TEG and the load varied. The MPPT algorithm was used in the controller to track the MPP value in the circuit. The MPPT will adjust the duty cycle according to the input and output values. The duty cycle response to the input and output value variation showed a good agreement, as depicted in Figure [13.](#page-15-1) The MPPT algorithm continuously tracks the MPP along the fluctuation of input and output values. Therefore, the duty cycle will respond with no lag if there is a sudden increase or decrease in the value. On the other hand, the circuit is tested by increasing the voltage without the time duration for each voltage range to evaluate the duty cycle response and efficiency. The result indicated that the mean efficiency was 91.92%.

On the other hand, the TEHS was applied to the field test to evaluate the setup's performance. The setup was tested for 51 days, facing a variation of solar radiation and environmental changing. Field test results indicated that the mean output voltage range was 2.099 V to 3.847 V, mean output current was 0.464 mA to 16.64 mA, and mean output power was 1.45 to 66.1 mW. In addition, the mean efficiency of the TEHS was from 89.62% to 92.98%. Although the field test duration was 24 h, the temperature difference at night was below 1  $\degree$ C, not enough to generate the output from the TEG. The comparison of the proposed TEHS with other studies is listed in Table [5.](#page-19-0)

Ref	$[72]$	$[73]$	741	$[75]$	[76]	This work
$\Delta T$ , °C	NA	9.5	4 and 12	5	6	6
	$+1.0$		$-1.73$	$+1.5$		$1 \text{ V} < \text{V}$ in > $1 \text{ V}$
Input	and	0.7V	and	and	0.4V	(dual-level
	$-1.0V$		$-0.238$ V	$-1.5V$		voltage)
					6.3	4 V
Output	2.4 V	2.4 V	1.2V	2.4 V	and	1.45 to 66.1
					$27.2 \text{ mW}$	mW
DC	Boost	<b>Boost</b>	Buck-	Boost	Boost	Boost
converter			boost			
<b>MPPT</b>	Yes	Yes	Yes	Yes	Yes	Yes
Unipolar	Bipolar	Bipolar	Bipolar	Bipolar	Unipolar	Bipolar
/Bipolar						
Additional					Radiator,	
component	NA	<b>NA</b>	NA	<b>NA</b>	thermal	NA
					harvester	
Application $N1A$ not applicable. Even computer ontal	Exp.	Exp.	Exp.	Exp.	Real-time	Real-time

<span id="page-19-0"></span>**Table 5.** Comparison of low-temperature difference thermal energy harvesting.

NA—not applicable, Exp.—experimental.

To estimate the potential of power generated from the result, considering the lowest mean power was 1.45 mW with an area of 1 m<sup>2</sup>, the power output per square meter,  $P_{\text{TEHS}}$ , was determined by:

$$
P_{TEHS} = \frac{P_{mean}}{A_R} = \frac{1.45 \text{ mW}}{1 \text{ x } 1} = 1.45 \text{ mW/m}^2 \tag{18}
$$

where  $A_R$  is the area of TEG.

If the TEHS was used at 5  $m^2$  using a similar setup, the power production,  $P_p$ , would be calculated as follows:

$$
P_P = P_{TEH}(A_R) = 1.45 \times 10^{-3} \ (5) = 7.25 \text{ mW}
$$
 (19)

Thereby, the generated power over 12 h (43,200 s) would be:

$$
P_P(43200) = 313.2 \text{ J}
$$
\n(20)

On the other hand, the potential of output power can be utilized for wireless sensor nodes (WSN) and wireless data transmission. The average power demand of a WSN at the operating stage is 100  $\mu$ W [\[77\]](#page-24-24). Therefore, the lowest mean output power can be used for 145 units of WSN. Furthermore, the wireless data transmission consisting of SHT31A-DSI-B (ambient temperature and humidity sensor), DS18B20 (soil temperature sensor), and DASH7 (transceiver module) required 3.3 V. In addition, the system consumes 900  $\mu$ J

(sensing) and 11.5 mJ (transmit 6 bytes) [\[78\]](#page-24-25). Therefore, referring to the energy generated, the system can be used continuously without any distractions.

#### <span id="page-20-0"></span>**7. Conclusions**

This research evaluated the energy harvesting circuit system to harvest thermal energy from solar radiation on the roofing material. The preliminary work was briefly explained in Section [3.](#page-6-0) The temperature differences recorded from the experiment were 0.3323 ◦C to 0.4871 °C (morning, 12.00 a.m. to 6.00 a.m.), 0.3407 °C to 0.7842 °C (evening, 6.00 p.m. to 12.00 a.m.), and 2.0 °C to 6.072 °C (day, 6.00 a.m. to 6.00 p.m.). The design and development of TEHS were according to the local solar radiation and environmental conditions to implement it in real-time application. Therefore, it was experimentally tested to evaluate its functionality and efficiency. Two different test approaches were used: (a) constant time and (b) varied time duration. The voltage range was from 0.2 to 4 V, with a 0.2 V interval. The results from the experiment show that the circuit design can detect, process, and function at a dual-level voltage. From the experimental testing, TEHS was seen to boost the voltage of a low-level circuit from 0.6 V. In comparison, a high-level circuit from 1.6 V obtained 3.6 V. Furthermore, the mean efficiency of the DC boost converter circuit was 91.92% for various time intervals. Therefore, the proposed TEHS can harvest thermal energy at different solar radiation levels and weather conditions. The field test result indicated that the mean output voltage range was 2.099 to 3.847 V, the mean output current was 0.464 to 16.64 mA, and the mean output power was 1.45 to 66.1 mW. In addition, the mean efficiency was from 89.62% to 92.98%.

## <span id="page-20-1"></span>**8. Directions for Future Research**

Based on the experimental and field test results, the recommendations for future research are as follows:

- Comparison of different roofing materials to harvest thermal energy.
- The development of integrated TEHS and roofing materials for flexibility, mobility, and ease of installation.
- Integrate the TEHS system with IoT technology.

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## **Abbreviations**



#### **Greek symbol**



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