

## Article

# Wafer-Scale Semipolar Micro-Pyramid Lighting-Emitting Diode Array

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**Abstract:** InGaN-based micro-structured light-emitting diodes ( $\mu$ LEDs) play a critical role in the field of full-color display. In this work, selected area growth (SAG) of a micro-pyramid LED array was performed on a 2-inch wafer-scale patterned SiO<sub>2</sub> template (periodicity: 4  $\mu$ m diameter), by which a uniform periodic  $\mu$ LED array was achieved. The single-element pyramid-shaped LED exhibited 6 equivalent semipolar {1-101} planes and a size of about 5  $\mu$ m, revealing a good crystalline quality with screw and edge dislocation densities of  $8.27 \times 10^7$  and  $4.49 \times 10^8$  cm<sup>-2</sup>. Due to the stress-relaxation out of the SAG, the as-built compressive strain was reduced to 0.59 GPa. The  $\mu$ LED array demonstrated a stable emission, confirmed by a small variation of electroluminescence (EL) peak wavelength over a wide range of current density up to 44.89 A/cm<sup>2</sup>, as well as tiny fluctuations (within 1.9 nm) in the EL full width at half maximum. The photoluminescence peak wavelength exhibits a good uniformity throughout the whole wafer with a discrete probability of only 0.25%.

**Keywords:** wafer-scale; selected area growth (SAG); pyramid  $\mu$ LED array; photoluminescence; electroluminescence



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## 1. Introduction

Over the past few years, nitride-based light-emitting diodes (LEDs) with chip size larger than 200  $\mu$ m have been proven to be a great success in general lighting, outdoor displaying, display backlighting, and many other applications [1–3]. Recently, there has been excitement in the display industry about micro-structured LEDs ( $\mu$ LEDs) with a size smaller than 50  $\mu$ m [4,5]. Compared with the traditional cathode ray tube (CRT) and liquid-crystal display (LCD), as well as emerging organic LED technologies, nitride-based  $\mu$ LEDs hold the promise of visually perfect displays with lower power consumption, enhanced speed, higher luminescence, and feasibility of 2D integration [5,6], emerging as great candidates for the next generation of display technology in the applications of high-end televisions, mobile phones, wearable display panels, augmented reality, and virtual reality [7,8]. Various types of nitride microstructures have been widely investigated, including waveguides, micro-disks, rings, pyramids, and nanowires [9–13].

However, there are still some challenges lying ahead to achieve nitride-based  $\mu$ LED display. First, the complicated transfer printing method is costly, which makes it difficult for

mass production at present, and equally so for monolithic full-color displays [14–16]. Second, bulk defect-related Shockley–Read–Hall (SRH) recombination, a strong polarization effect in c-plane InGaN multiple quantum wells (MQWs), and severe sidewall leakage related to dangling bonds, contaminations, and ion damages induced by inductively coupled plasma (ICP) etching all severely reduce the external quantum efficiency of  $\mu$ LEDs [17–19]. Furthermore, the quantum-confined Stark effect (QCSE) caused by the internal polarization field in MQWs would bring serious wavelength shifts and instability of  $\mu$ LEDs at different injected currents [20], which would subsequently lead to the Mura effect on the application of  $\mu$ LED display. In this regard, InGaN-based semipolar LEDs hold the potential to solve the above problems, with advantages in terms of wide bandgap tunability [21], reduced QCSE effect, and good stability in the emission wavelength. Thus, a wafer-scale  $\mu$ LED array grown on the semipolar plane is expected to achieve large-area full-color displays. Except for its ability to grow nitrides on homo semipolar substrates, selected area growth (SAG) on c-plane sapphire substrates would also provide great opportunities to achieve semi-polar growth over a wide area [22,23]. In addition, it is particularly important to study the uniformity of the optoelectronic characteristics of wafer-scale  $\mu$ LEDs.

In this work, we prepared a 2-inch wafer-scale micro-pyramid LED array with an InGaN MQWs structure on semipolar {1-101} planes through metal-organic vapor deposition (MOCVD). The epitaxial  $\mu$ LED array was grown via SAG on a patterned  $\text{SiO}_2$  template and the size of an individual  $\mu$ LED pixel was about 5  $\mu\text{m}$ . The wafer-scale morphology of the as-fabricated epilayer was characterized. The crystalline quality and biaxial stress of the epilayer was also investigated. Finally, the results of the electrical properties indicated that these  $\mu$ LEDs could still work well at a current density of about 44.9  $\text{A}/\text{cm}^2$ . The wafer-scale photoluminescence (PL) performance exhibited good uniformity throughout the whole 2-inch area with a discrete probability of only 0.25%. The full width at half maximum (FWHM) of electroluminescence (EL) fluctuated within a range of  $\pm 1.9$  nm on the whole wafer. This work opens a feasible pathway for the growth of mass-produced micro-pyramid LEDs arrays for the application of full-color displays.

## 2. Materials and Methods

### 2.1. Template Preparation

An n-GaN film (5  $\mu\text{m}$ ) was deposited on a 2-inch c-plane sapphire wafer by MOCVD (Veeco, K465i), utilizing trimethylgallium (TMGa),  $\text{NH}_3$ , and Silane ( $\text{SiH}_4$ ) as Ga, N precursor, and n-doped source, respectively. A 40 nm-thick  $\text{SiO}_2$  layer was first deposited on n-GaN film by plasma-enhanced chemical vapor deposition (PECVD) with temperature at 300  $^\circ\text{C}$ , pressure at 600 mTorr, RF-power at 50 W, and  $\text{SiH}_4$ , He,  $\text{N}_2\text{O}$ , and  $\text{N}_2$  flow of 500, 25, 1000, and 475 sccm respectively, followed by a patterning process to create an array of holes with a periodicity of 6  $\mu\text{m}$  and a diameter of 4  $\mu\text{m}$  by using contact lithography, and reactive ion etching (RIE) process with a mixture of  $\text{CF}_4/\text{O}_2$  gases. The wafer-scale patterned  $\text{SiO}_2$ /n-GaN structure acted as a template to achieve the SAG of the  $\mu$ LED array.

### 2.2. Formation of Pyramid $\mu$ LED Array

The pyramid  $\mu$ LED array was grown on the  $\text{SiO}_2$ /n-GaN structure by MOCVD. During the epitaxy process, trimethylindium (TMIn), trimethylaluminum (TMAI), and magnesocene ( $\text{Cp}_2\text{Mg}$ ) were adopted as In, Al precursor, and p-doped source, respectively. The growth condition of the epitaxial structure in the MOCVD process is summarized in Table 1. Considering the chemical etching effect of hydrogen ( $\text{H}_2$ ) on nitride,  $\text{H}_2$  and nitrogen ( $\text{N}_2$ ) mixed gases with a moderate flow of 20/125 standard liters per minute (slm) acted as the carrier gas to avoid the flat top of the nitride pyramid. For the following epitaxy, no GaN buffer layer was adopted. An n-GaN layer was directly grown on the prepared template at a high temperature of 1070  $^\circ\text{C}$ , with  $\text{SiH}_4$  flow of 23.7 standard cubic centimeters per minute (sccm), TMGa flow of 328 sccm, and  $\text{NH}_3$  flow of 50 slm, for about 30 min, followed by three-period InGaN/GaN shallow-well (SW) and shallow-barrier (SB) layers at a temperature of 860  $^\circ\text{C}$ . Afterward, four-period InGaN (10 nm)/GaN (3 nm)

MQWs were grown at 885 °C and 760 °C with NH<sub>3</sub> flow of 40 slm. Triethylgallium (TEGa) flows for the InGaN well and GaN barrier were 140 and 450 sccm. AlGaN/GaN electron barrier layer, p-GaN, and p-GaN contact layer were deposited subsequently at 950 °C, 950 °C, and 680 °C, respectively. In the final step, the p-GaN layer was in-situ annealed at 720 °C in an N<sub>2</sub> atmosphere in order to form good p-contacts.

**Table 1.** The growth condition of the epitaxial structure in the MOCVD process.

Step	Temperature (°C)	Time (min)	NH <sub>3</sub> (slm)	TMGa (sccm)	TEGa (sccm)	TMAI (sccm)	TMIn (sccm)	SiH <sub>4</sub> (sccm)	Cp <sub>2</sub> Mg (sccm)
n-GaN	1070	30	50	328	/	/	/	23.7	/
InGaN/GaN	860	4	40	/	218	/	570	/	/
SW/SB (LOOP 3)	860	2	40	/	172	/	/	/	/
InGaN/GaN	760	4	40	/	140	/	500	/	/
QW/QB (LOOP 4)	885	8	40	/	450	/	/	/	/
AlGaN/GaN EBL (LOOP 10)	950	0.5	10	67	/	62	/	/	160
	950	0.5	10	67	/	/	/	/	/
p-GaN	950	10	50	80	/	/	/	/	340
p-GaN CL	680	1.5	40	/	140	/	352	/	175

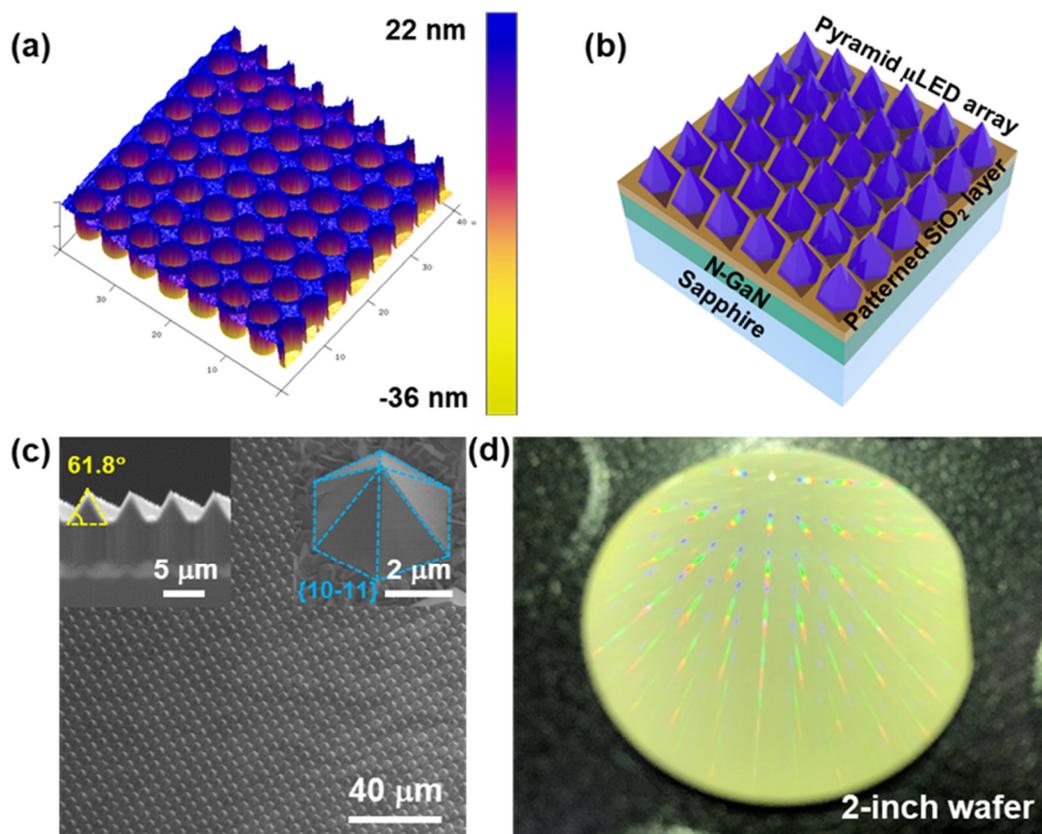
### 2.3. Characterization

The morphology of the as-fabricated pattern of the SiO<sub>2</sub> template was probed using an atomic force microscope (AFM) (D3100, Veeco, New York, NY, USA). The as-grown pyramid  $\mu$ LED array was characterized with scanning electron microscopy (SEM) (Hitachi, Tokyo, Japan) at a working voltage of 4.4 kV. The crystalline quality of the as-grown epilayers was analyzed through X-ray diffraction (XRD) (Bede D1, Bede, Durham, United Kingdom) with a Cu K $\alpha$  radiation source at 40 kV voltage and 35 mA current. PL microscopy (Horiba, Kyoto, Japan) was performed with a 325 nm He-Cd laser as the excitation source at power of 50 mW and spatial resolution of 2  $\mu$ m. Raman (Horiba, Kyoto, Japan) measurement was performed by using a 532 nm linearly polarized laser as the excitation source. The signals were collected by a spectrometer with a grating groove density of 1200 g/mm and a focal length of 800 mm under a backscattering configuration. The microstructure of the pyramid LED was characterized with a transmission electron microscope (TEM) (JEM-F200, JEOL, Tokyo, Japan), which was equipped with an energy-dispersive X-ray spectroscopy (EDS) detector (JEOL, Tokyo, Japan). The electrical characteristics of the LED were analyzed by a source meter (Keithley 2400, Keithley, New York, NY, USA).

### 3. Results and Discussion

A randomly selected 3D AFM image over an area of 40  $\times$  40  $\mu$ m<sup>2</sup> of the as-fabricated 2-inch wafer-scale patterned SiO<sub>2</sub> layer (thickness of 40 nm) on n-GaN template revealed a pattern consisting of uniformly created periodical holes, in which the hole diameter was 4  $\mu$ m and the period was 6  $\mu$ m (see, Figure 1a). The depth variation curve along a row of the SiO<sub>2</sub> layer is shown in Figure S1. The uniform pattern is beneficial to the gas flow distribution in the reactor during the growth and thus promotes the uniformity of the nitride microstructures, especially for those on wafer-scale substrates. The utilization of n-GaN instead of sapphire as the template aimed to provide a high-quality growth front and an electron injection layer. The pyramid  $\mu$ LED array on the patterned SiO<sub>2</sub> layer is schematically shown in Figure 1b. The  $\mu$ LED structure was grown directly on the patterned template through SAG, benefiting from the neglected nitride nucleation on the SiO<sub>2</sub> mask. The typical morphology of a pyramid  $\mu$ LED array is observed from a 25°-tilted SEM image (magnification: 500 $\times$ ) in Figure 1c, from which it can be seen that the growth exhibited good uniformity and selectivity, as epitaxy only took place in the pattern openings. Additionally, all  $\mu$ LED structures showed a smooth-faceted identical

pyramid structure with a hexagonal base and consistent crystalline orientations. From the cross-section and magnified SEM images in the inset of Figure 1c, 6 equivalent planes enclosed the pyramid, and the side facet was at a  $61.8^\circ$  angle to the (0002) plane of nitride, whereby it could be determined that the side planes belonged to the {1-101} facets. This is ascribed to the slower growth rate of {1-101} planes than that of (0002), and indicates that the semipolar planes {1-101} were energetically stable. Under this growth configuration, the (0002) facet gradually became extinct at the convex surface due to the faster growth rate with sufficient epitaxial time, and a self-limiting pyramid structure ultimately formed, in which the crystalline orientation can be identified in the SEM image as a single pyramid  $\mu$ LED. The typical base size of the pyramid  $\mu$ LED was about  $5 \mu\text{m}$ .



**Figure 1.** (a) AFM image of the as-fabricated patterned  $\text{SiO}_2$  layer on n-GaN template; (b) Schematic diagram of the as-grown pyramid  $\mu$ LED array on patterned  $\text{SiO}_2$  layer; (c) The  $25^\circ$  tilted SEM image (magnification:  $500\times$ ) of the as-grown pyramid  $\mu$ LED array. The insets represent the cross-section SEM image and a magnified pyramid structure; (d) Photograph of the 2-inch wafer with pyramid  $\mu$ LED array.

An optical photograph of the as-fabricated wafer was obtained (see Figure 1d). Rainbow colors could be seen when white light was scattered by the complex periodic structure. It is observed that the rainbow color stripes covered the entire wafer, indicating the good uniformity and strong periodicity of the pyramid  $\mu$ LED array on the wafer.

To evaluate the crystalline quality of as-grown nitride, the FWHM of (0002) and (10-12) planes was measured as 202.9 and 247.9 arcsec, respectively (see Figure 2a). The screw and edge dislocation densities of nitride could be estimated according to the following equations [24]:

$$N_{screw} = \beta_{tilt}^2 / 4.35b_s^2 \quad (1)$$

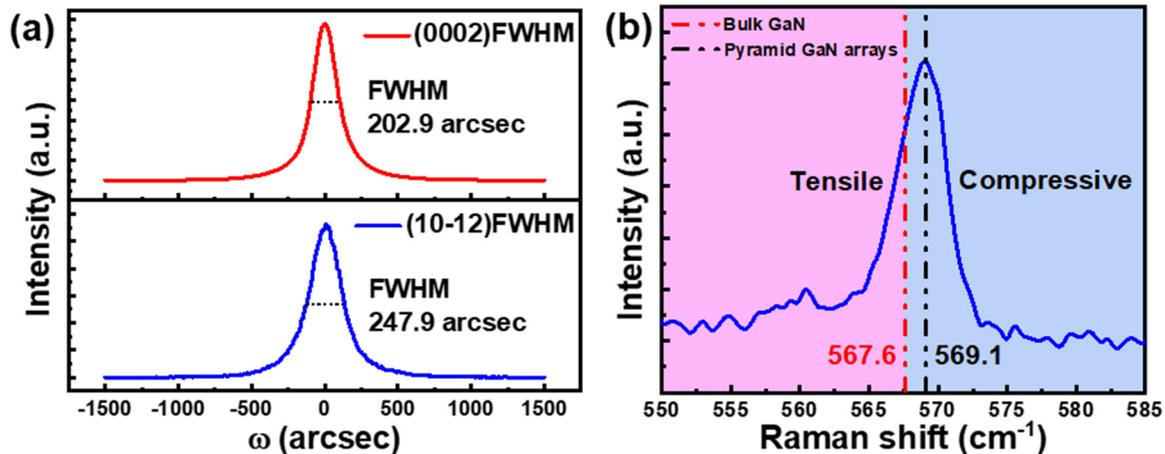
$$N_{edge} = \beta_{twist}^2 / 4.35b_e^2 \quad (2)$$

where  $b_s$  and  $b_e$  are the Burgers vectors of the screw dislocation ( $|b_s| = 0.5185 \text{ nm}$  for GaN) and edge dislocation ( $|b_e| = 0.3189 \text{ nm}$  for GaN).  $\beta_{tilt}$  and  $\beta_{twist}$  are the tilt and

twist spreads, respectively, which could be estimated by Equation (3) based on a previous report [25]:

$$\beta = \sqrt{(\beta_{\text{tilt}} \cos \varphi)^2 + (\beta_{\text{twist}} \sin \varphi)^2} \quad (3)$$

where  $\varphi$  is the angle between the reciprocal lattice vector ( $K_{\text{hkl}}$ ) and the (001) plane normal. As such, the corresponding screw and edge dislocation densities of the as-grown epilayer were estimated as  $8.27 \times 10^7$  and  $4.49 \times 10^8 \text{ cm}^{-2}$ , respectively, indicating the good crystalline quality of the as-grown pyramid  $\mu\text{LED}$  layer.

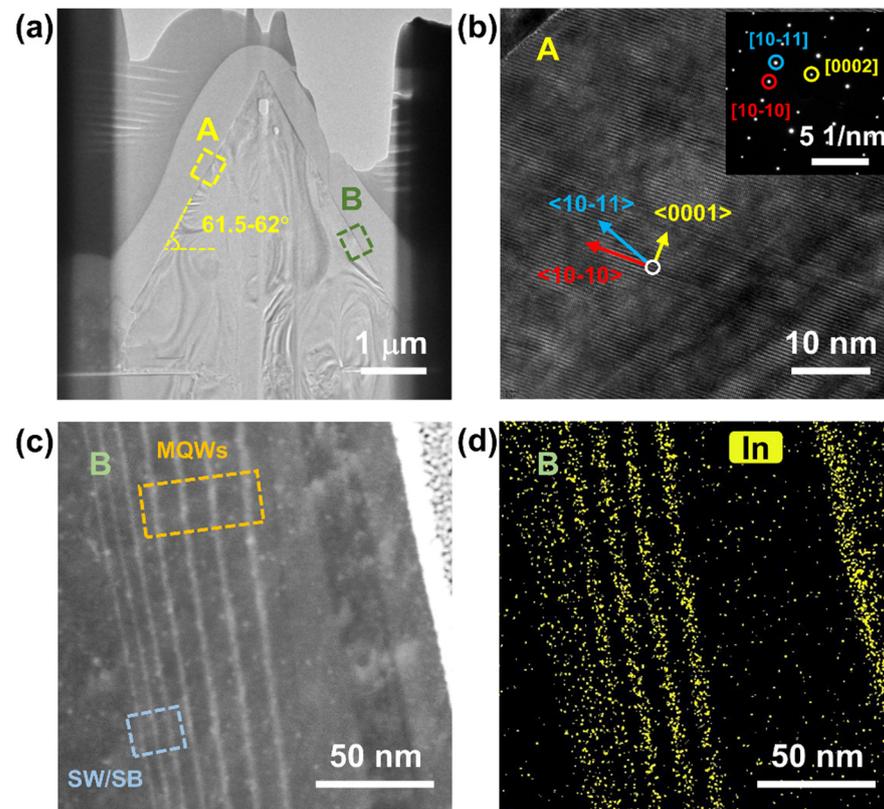


**Figure 2.** (a) XRD (0002) and (10-12) rocking curves, and (b) Raman spectrum of the as-grown pyramid  $\mu\text{LED}$  epitaxial structure on patterned  $\text{SiO}_2$  layer.

Raman spectroscopy was performed to quantitatively investigate the stress of the as-grown  $\mu\text{LED}$  structure. The  $E_2$  (high) peak mode in the Raman spectrum was closely related to the biaxial stress in the GaN film [26]. The peak position of the pyramid  $\mu\text{LED}$  structure was measured as  $569.1 \text{ cm}^{-1}$ , which deviates from the characteristic frequency of stress-free GaN ( $567.6 \text{ cm}^{-1}$ ) [27]. The biaxial stress  $\sigma_{xx}$  in the as-grown nitride layer could be estimated according to its frequency deviation  $\Delta\omega$  compared with the stress-free GaN and the equation  $\Delta\omega = K\sigma_{xx}$ , where  $K$  is the linear stress coefficient of GaN material ( $2.56 \text{ cm}^{-1}\text{GPa}^{-1}$ ) [28]. Through calculation, the biaxial stress of  $\mu\text{LED}$  structure was identified as  $0.59 \text{ GPa}$ , meaning that the epilayer was subjected to a weak compressive strain, which benefited from micro-scale footprints of epilayer on the n-GaN template and the suppression of stress accumulations.

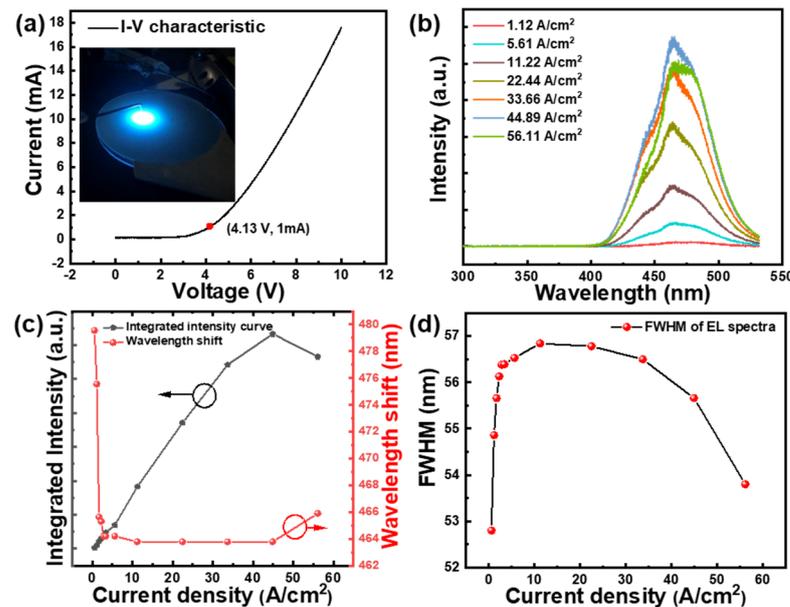
Detailed structural analysis of a single  $\mu\text{LED}$  pyramid in the array was characterized by TEM. The test lamella was fabricated using a focused ion beam. From the TEM image of a single pyramid  $\mu\text{LED}$  structure (see Figure 3a), a triangular outline is clearly resolved, corresponding to the cross-section of the pyramid. The  $\text{SiO}_2$  pattern and the opening hole could be clearly identified at the bottom of the pyramid. The top of the pyramid was sharp and the angle between the sidewall and the in-plane was around  $61.5^\circ$ – $62^\circ$ , which is consistent with the above SEM result. Figure 3b shows the high-resolution TEM (HRTEM) image from the place of interest marked by a yellow square in Figure 3a. From selected area electron diffraction (SAED) patterns in the set of Figure 3b, the d-spacing along growth direction was calculated as  $5.46 \text{ \AA}$ , corresponding to the (0002) plane of GaN, confirming the wurtzite structure of epilayer, [0001] growth direction of epilayer, and  $\langle 10\text{-}11 \rangle$  equivalent orientation perpendicular to the sidewall of the pyramid. Cross-section high angle annular dark-field (HAADF) scanning transmission electron microscopy (STEM) and corresponding EDS mapping of In from region B in Figure 3a were performed to evaluate the structure of MQWs, due to their good sensitivity and contrast to elements with different atomic numbers (see Figure 3c,d). The results indicate that three-period InGa $\text{N}$ /Ga $\text{N}$  shallow quantum well and four-period InGa $\text{N}$ /Ga $\text{N}$  MQWs exhibited

obvious light and dark stripes. The separate dark stripe near the sidewall represented the AlGaIn electron barrier layer, aiming to block the electron roll-over effect under the operation of high current density and further reduce the probability of electron-hole pair non-radiative recombination.



**Figure 3.** (a) TEM image of a single pyramid  $\mu$ LED structure; (b) HRTEM image of region A in (a), including part of the MQWs structure and the p-GaN layer; inset is the image of corresponding SAED patterns; (c) HAADF STEM image of region B in (a), including MQWs and p-GaN layer in the  $\mu$ LED structure; (d) EDS mapping of In element in MQWs, corresponding to (c).

The electrical characteristics of the as-fabricated wafer-scale pyramid  $\mu$ LED array were investigated by on-wafer electrical probing. Indium bumps were pinned on the wafer, serving as n-type and p-type contacts, respectively, while the n-GaN template acted as the n-type current spreading layer. The injected current density depends on the size of the indium bumps on the surface of the wafer (approximately  $1 \text{ mm}^2$  in this work). The I-V characteristic of the  $\mu$ LED array demonstrated a good rectification characteristic with a threshold voltage of 4.13 V (defined as the voltage value at an injection current of 1 mA) (see Figure 4a), which was slightly larger than that of conventional  $\mu$ LED chips. To explore the reason for this, the parasitic resistance characteristic of the  $\mu$ LED array was analyzed. The series resistance is generally influenced by various resistances including p-type contacts, n-type contacts, and especially the intrinsic resistance of the n-GaN layer in this work, as n-contacts were far away from the devices. Linear fitting of  $\text{IdV}/\text{dI}$  and  $I$  in the moderate current region (4–18 mA) showed that the series resistance of  $\mu$ LED array was calculated as  $223.1 \Omega$  (see Figure S2) [29]. The parallel resistance is normally related to the damage on MQWs and the surface defects of the epilayer, leading to a bypass current vertically through the LED structure. The parallel resistance was estimated to be  $610 \text{ k}\Omega$  through the linear fitting of the I-V curve within the low voltage region (0–2.5 V), indicating the good crystalline quality of the epilayer (see Figure S3).

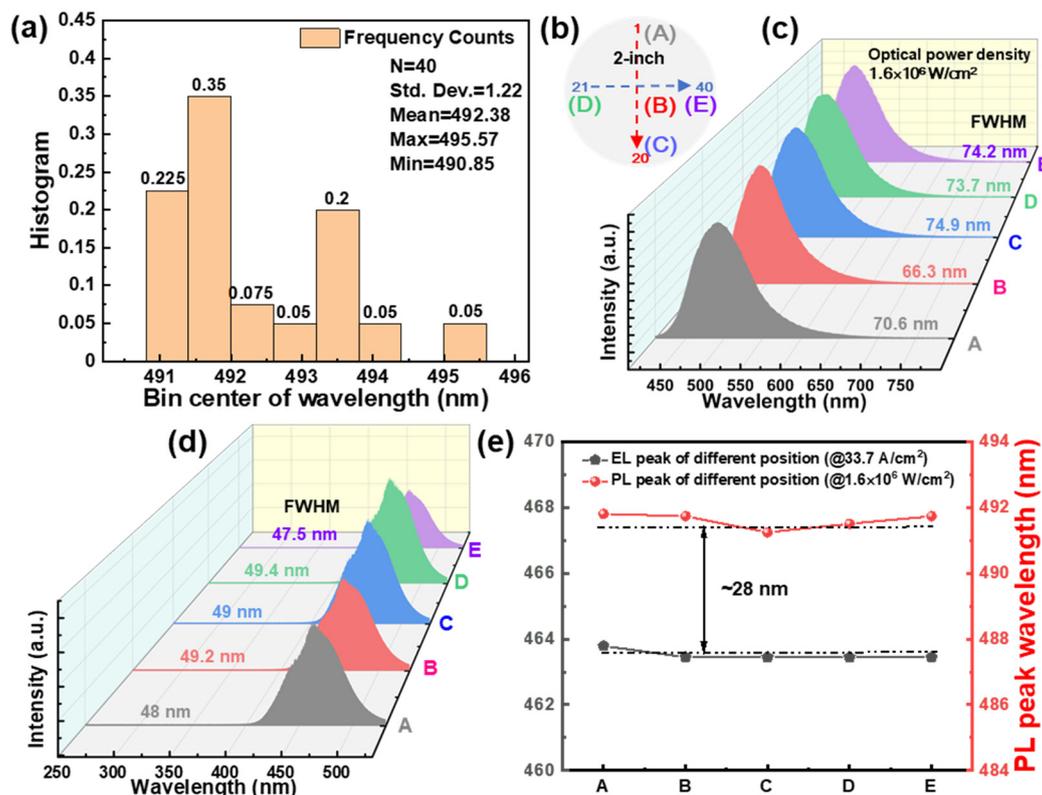


**Figure 4.** (a) The I-V characteristic of wafer-scale pyramid  $\mu$ LED array measured through on-wafer electrical probing; the inset is a photograph of the LED operated at  $22.44 \text{ A/cm}^2$ ; (b) EL spectra of the wafer-scale pyramid  $\mu$ LED array as a function of injected current density; (c) the integrated EL intensity and wavelength peak shift curve as a function of injected current density; (d) the FWHM of EL spectra as a function of injected current density.

The electrical luminescence picture from the  $\mu$ LED array in the inset of Figure 4a exhibited a uniform blue light emission on the wafer. The EL spectral spectra of the  $\mu$ LED array at different injected current densities (from  $1.12$  to  $56.11 \text{ A/cm}^2$ ) were also collected (Figure 4b). The EL peak position and EL integrated intensity as a function of injected current densities are summarized in Figure 4c. As the current density increased, the EL peaks first blue-shifted about  $16 \text{ nm}$  (current density from  $1.12$  to  $11.22 \text{ A/cm}^2$ ), then maintained stability at  $463.8 \text{ nm}$  in a wide range (current density from  $11.22$  to  $44.89 \text{ A/cm}^2$ ), and eventually exhibited a redshift of about  $2.1 \text{ nm}$  (current density from  $44.89$  to  $56.11 \text{ A/cm}^2$ ). The large blue shift could be largely attributed to different active regions, which have various strain states, to In atoms in the localized regions, or to the MQWs structure on (0002) plane [30,31]. In addition, the screening of charge to the built-in electrical field within the MQWs and the band filling effect as injected current density increased could also lead to a reduction in QCSE for the emission wavelength [32]. The bandgap of semiconductors decreases with increasing temperatures when self-heating effects start to take over at a high injected current density of LED, which causes redshift in the LED emission wavelength [33]. The EL integrated intensity increased first in a large current density range of  $1.12$ – $44.89 \text{ A/cm}^2$  and then decreased [34]. This is presumably attributed to the electrical breakdown of individual pyramid  $\mu$ LEDs at high current densities. As shown in Figure 4d, weak fluctuations centered at  $56 \text{ nm}$  FWHM of pyramid  $\mu$ LED array were observed. The wide EL spectrum is due to the native fluctuation of indium composition in the InGaN layer and the uneven thickness of the quantum well deposited on the semipolar plane of nitride [31,35]. The FWHM reached a maximum when the operating current density was  $11.22 \text{ A/cm}^2$ .

In this section, to investigate the uniformity of optical properties of wafer-scale pyramid  $\mu$ LED, we adopted the standard frequency distribution histogram for summarizing the PL peak wavelength data based on 40 samples (Figure 5a). The 40 samples are located on the 2-inch wafer, labeled as “1–40” (Figure 5b). The minimum and maximum were  $490.85$  and  $495.57 \text{ nm}$ , respectively. The fluctuation range of the PL wavelength peak within the 2-inch wafer-scale was less than  $5 \text{ nm}$ , indicating the preferable uniformity of the peak wavelength distribution. This helps to fabricate a large-scale  $\mu$ LED array with the same lu-

minous characteristics. Additionally, more than half of the samples had peak wavelengths within the range of 490.7 to 492 nm. The standard deviation (Std. Dev.) was derived as 1.22 nm, and the relative Std. Dev. (defined as Std. Dev./mean  $\times$  100%) was calculated as 0.25%. This means that the discrete probability of PL peak wavelength was only 0.25%, proving that the pyramid  $\mu$ LED array had uniform optical properties throughout the whole 2-inch wafer.



**Figure 5.** (a) The standard frequency distribution histogram of the PL peak wavelength. Number of samples (N) is 40; (b) Schematic diagram of locations of interests for PL spectroscopy, among which labels “1–40” represent the samples and labels “A–E” represent five samples with representative characteristics; (c) PL spectra (at an optical power density of  $1.6 \times 10^6$  W/cm<sup>2</sup>) and (d) EL spectra (at injected current density of 33.7 A/cm<sup>2</sup>) of the five samples; (e) EL and PL peak wavelengths of the five samples at different positions.

To illustrate detailed spectra characteristics, we selected five samples with representative characteristics and compared their PL and EL spectra (Figure 5c–e). The optical power density of the excited light source in the PL measurement remained steady at  $1.6 \times 10^6$  W/cm<sup>2</sup>, and EL spectra were collected at the injected current density of 33.7 A/cm<sup>2</sup>. The PL FWHM fluctuated in a range of  $\pm 8.6$  nm, which is attributed to the generation of localization centers [31]. As shown in Figure 5c,d, the value of the EL FWHM was on average 28 nm smaller than that of the PL and had smaller fluctuations within  $\pm 1.9$  nm, indicating that some localization centers are not electrically excited under conditions of certain current density. Additionally, as shown in Figure 5e, the peak wavelength of EL fluctuated within only  $\pm 0.34$  nm. A uniform EL characteristic is helpful to the stability of LED devices. The comparison of PL and EL peak wavelengths was also summarized. The mean PL peak wavelength demonstrated an obvious redshift of about 28 nm more than the EL peak wavelength, which may be related to Stokes’ shift induced by indium composition fluctuations in the PL measurement and the band filling effect in the EL measurement [36]. Moreover, the higher junction temperature in optical injection mode than in current injection mode may also be an important factor, which could be explained by the thermal emission of the illuminating laser [37].

#### 4. Conclusions

In summary, a micro-pyramid LED array was fabricated on an entire 2-inch sapphire wafer through the SAG method by MOCVD. The  $\mu$ LED pixels in the array were of the size of about 5  $\mu$ m, exhibiting excellent uniformity in size and morphology. Meanwhile, good crystalline quality with weak compressive strain was confirmed by XRD and Raman spectroscopy for the LED epitaxial layers. Furthermore, the  $\mu$ LED array showed a wafer-scale uniformity of optoelectronic characteristics through wafer-coverage PL and EL measurements. The peak wavelength and FWHM of EL fluctuations were within only 1.9 and 0.34 nm, respectively, demonstrating very stable EL emission properties. The discrete probability of PL peak wavelength throughout the whole 2-inch wafer was only 0.25%. Since the InGaN-based micro-pyramid LED array has such stable and uniform optoelectronic performance, the strategy proposed in this work can likely be used to achieve the fabrication of full-color display units. Future work can be conducted to study the multiple SAG of InGaN-based pyramid LED arrays with different emission wavelengths to achieve on-chip integration of the three primary colors.

**Supplementary Materials:** The following are available online at <https://www.mdpi.com/article/10.3390/cryst11060686/s1>, Figure S1: The linear fitting result of the IdV/dI curve as a function of I in the moderate current region (4–18 mA), Figure S2: The linear fitting result of the IdV/dI curve as a function of I in the moderate current region (4–18 mA), Figure S3: The linear fitting result of the I-V curve in the low voltage region (0–2.5 V) of the as-fabricated LED.

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