


## Article

# Identification and Evolution of Intermetallic Compounds Formed at the Interface between In-48Sn and Cu during Liquid Soldering Reactions

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**Abstract:** It is difficult to confirm the existence of intermetallic compounds (IMCs) between SnIn and Cu, as their atomic numbers are very close, making it challenging to differentiate them through experimental tests. In order to determine IMCs and understand their growth mechanism, this study employed phase identification, morphology observation, and growth kinetics analyses on IMCs formed between In-48Sn solder and polycrystalline Cu substrate during liquid soldering. The experiments were conducted within a temperature range of 160~250 °C for up to 90 min. The obtained results indicated that IMCs formed at the interface depended strongly on the soldering temperature. During long-time soldering below 200 °C, one main IMC species, Cu<sub>2</sub>(In,Sn), was found at the In-48Sn solder/Cu interface, which showed two different morphologies: a coarse-grained layer at the solder side and a fine-grained layer within the Cu component. When the soldering temperature was increased to 200 °C, Cu<sub>6</sub>(In,Sn)<sub>5</sub> was the only intermetallic compound (IMC) that formed at the point where the In-48Sn/Cu eutectic interface existed. At 250 °C, with an increase in the soldering time, there was a formation of Cu<sub>9</sub>(In,Sn)<sub>4</sub> between Cu and Cu<sub>6</sub>(In,Sn)<sub>5</sub>. The growth kinetics analyses indicated that the fast grain boundary/molten-channel diffusion of Cu into solder and their reaction with solder controlled the growth of the interfacial IMCs, with the activation energy of 24.56 kJ/mol when the sample was liquid-state-aged above 200 °C.



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**Keywords:** In-48Sn solder alloy; Cu substrate; interfacial reaction; intermetallic compound; TEM

## 1. Introduction

It is an inevitable trend in the microelectronics industry to use lead-free solder as a substitute for lead-containing solder due to environmental and human health issues [1,2]. During the last decade, several kinds of lead-free solders have been developed. However, most of the current commercial lead-free solders, such as tin/silver (Sn/Ag) and tin/silver/copper (Sn/Ag/Cu), have higher melting temperatures ( $T_m$  of Sn/Ag and Sn/Ag/Cu are 217 and 221 °C, respectively) than that of the conventional tin–lead eutectic solder (183 °C) [3]. Thus, the reflow temperature during electronic assembly should be raised, which would result in larger thermal stresses generated by different thermal expansions between the device and the substrate, thereby leading to mechanical damage in the device. In view of this, low-melting-point solders were remarkable during soldering in reducing the thermal stresses [4]. Amongst the Pb-free alloy families, the In-48Sn eutectic alloy has attracted a great deal of attention because of its low melting temperature, higher ductility, better wettability, and longer fatigue life [5]. Studies have reported that In-Sn solders had been used for SMT applications, step soldering sequences, heat-sensitive devices, etc. [6–8].

Although several studies focused on the reaction between In-48Sn and Cu substrate, most of these works paid attention to relatively low reactive temperatures in order to

simulate the service conditions of solder joints. However, the findings reported in various studies were not entirely consistent with each other, especially regarding the identification of phases [9–15], which is essential to clarify the service reliability and failure mechanism of the solder joint. Roming et al. [9] conducted a two-step process where a Cu-based metal was dipped into molten solder. Subsequently, the samples were transferred to alumina boats and subjected to air aging at temperatures ranging from 60 °C to 110 °C. At the interface, two types of intermetallic compounds (IMCs), namely  $\text{Cu}_2(\text{In},\text{Sn})$  and  $\text{Cu}_2\text{In}_3\text{Sn}$ , were identified. Furthermore, their investigation revealed that the growth of  $\text{Cu}_2\text{In}_3\text{Sn}$  dominated the growth of the IMC layer (although the  $\text{Cu}_2(\text{In},\text{Sn})$  layer formed during dipping, it did not undergo any further growth). Vianco et al. [10] conducted a follow-up study in which copper was hot-dipped into a molten solder of 50In-50Sn and subsequently subjected to solid-state aging at temperatures of 75 °C, 80 °C, and 100 °C for up to 200 days. Their findings revealed that the IMC layer was composed of a thin layer of  $\text{Cu}_{17}\text{Sn}_9\text{In}_{24}$  near the solder matrix and a thicker layer of  $\text{Cu}_{26}\text{Sn}_{13}\text{In}_8$  on the side of the Cu substrate. Another study by Chuang et al. [11] reported that the likely phases at the interface after soldering the In-49Sn/Cu system at temperatures ranging from 60 °C to 110 °C were  $\epsilon\text{-Cu}_3(\text{In},\text{Sn})$  and  $\eta\text{-Cu}_6(\text{In},\text{Sn})_5$ .

It is important to emphasize that all the aforementioned findings were acquired using either electron probe microanalysis (EPMA) or energy-dispersive X-ray spectroscopy (EDS) techniques. It is difficult to distinguish all of the IMCs formed between In-48Sn and Cu, such as  $\text{Cu}_6\text{Sn}_5$ ,  $\text{Cu}_3\text{Sn}$ ,  $\text{Cu}_7\text{In}_3$ ,  $\text{Cu}_2\text{In}$ ,  $\text{Cu}_9\text{In}_4$ , and  $\text{Cu}_{11}\text{In}_9$  [16], limited by the resolution of EPMA or EDS. This means that only compositional information was not enough for phase identification. To provide a detailed proof for the identification of phases, specifically with regards to the crystallographic structure of IMCs, is imperative. In our recent work, TEM and precise electron diffraction techniques were used to conduct interfacial phase identification for our sample, which was reflowed at 160 °C for 5 s and then solid-state aged at 100 °C up to 7 days. The resulting IMC was identified as  $\text{Cu}_2(\text{In},\text{Sn})$  [17].

In recent years, the transient liquid phase (TLP) soldering process as a potential alternative method to improve the reliability of solder joints in electronic assemblies and systems operated at elevated temperatures has attracted much attention [18–20]. The transient liquid phase (TLP) soldering process involves melting a minor amount of metal, which helps to fill in any defects and enhance the strength of the joint. Additionally, by controlling the temperature distribution and using appropriate materials, this process can effectively prevent the occurrence of cracks. The TLP soldering process necessitates a soldering temperature that is higher than the melting point of the solder alloy. However, to prevent excessive evaporation or loss of the solder alloy, the temperature is typically maintained below 300 °C. As for Cu/In-48Sn/Cu solder joints, earlier work was performed by Sommadossi et al. as a diffusion-soldering process [21,22] and was primarily conducted from 180 to 400 °C. They reported two kinds of IMCs,  $\text{Cu}_2(\text{In},\text{Sn})$  and  $\text{Cu}_2\text{In}_3\text{Sn}$ , at the interface based on their EPMA data. In this study, we utilized transmission electron microscope observations and precise diffraction analysis to investigate interfacial evolutions between In-48Sn and Cu, offering a high-resolution approach for phase identification. Experiments were conducted over a temperature span of 160 °C to 250 °C, with a time limit of 90 min.

## 2. Experimental Procedure

### 2.1. Soldering Sample Preparation

In this study, oxygen-free high-conductivity (OFHC) multi-crystalline Cu samples were prepared by cutting them with spark erosion machining to dimensions of  $10 \times 2.5 \times 2 \text{ mm}^3$ . The In-48Sn solder employed was fabricated by heating high-purity indium and tin until it became molten to produce a solid block of metal, which was subsequently subjected to cold-rolling to obtain a 1 mm thin sheet and cut into dimensions of  $10 \times 2.5 \text{ mm}^2$ . To ready the Cu surfaces, they were ground and polished with 0.5  $\mu\text{m}$  diamond paste, then cleansed in an ultrasonic bath of distilled water, containing acetone and methanol

alcohol. Afterwards, two copper sheets were bonded using eutectic SnIn solder to produce a sandwich comprising Cu-solder-Cu, with 50  $\mu\text{m}$  diameter brass wires inserted between them to control the thickness of the solder layer. The soldering process was performed for different durations ranging from 5 s to 90 min at varying temperatures, including 160 °C, 200 °C, 220 °C, and 250 °C.

## 2.2. Electron Microscopy Analysis

After the soldering process, the sample was cross-sectionally cut and subsequently polished using a BUEHLER polishing suspension (Bühler Group, Uzwil, Switzerland) that included 0.05  $\mu\text{m}$   $\text{Al}_2\text{O}_3$  powder. The polishing process was conducted meticulously to ensure high-quality results. An etchant of 90%  $\text{CH}_3\text{OH}$ -10%  $\text{HNO}_3$  was used to dissolve the remaining solder to observe the top view of the IMCs. Using an FEI Quanta 600 SEM (FEI company, Hillsboro, OR, USA) equipped with an Oxford Link ISIS EDS system (Oxford Instruments, Abingdon, UK), morphology observations were performed through scanning electron microscopy (SEM). To prepare the TEM foils, the slices underwent mechanical grinding until they reached a final thickness of 40  $\mu\text{m}$ . They were then subjected to ion milling using a Gatan model 691 PIPS system (Gatan, Pleasanton, CA, USA) under conditions of 5.0 keV and 5  $\mu\text{A}$ , with a low milling angle of less than 6°. Transmission electron microscopy (TEM) observations were conducted using FEI Tecnai F30 (FEI company, Hillsboro, OR, USA) and JEM-2100 electron microscopes (JEOL Ltd., Tokyo, Japan).

## 2.3. IMC Thickness Measurement

For the kinetic studies, the thickness of the IMC layer was digitally measured on SEM images using the following steps: (1) differentiate the individual IMC layers based on their image contrast using the commercial software “Adobe Photoshop”; (2) generate a binary image for the IMC layer and save it in BMP format; (3) utilize self-developed software to calculate the total area of the IMC layer, and subsequently divide the total area of the IMC layer by the length of the IMC layer to derive the mean thickness of each layer, even if the layer itself is non-uniform.

# 3. Experimental Results and Discussion

## 3.1. Interfacial IMC Evolution during Soldering at Different Temperatures

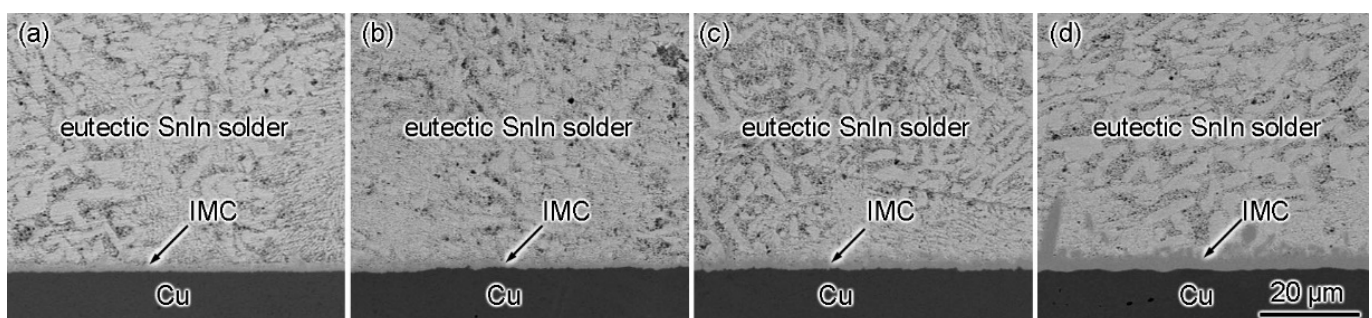
Figure 1 shows SEM images showing a cross-sectional view of the In-48Sn/Cu interface after soldering at 160 °C for different times. At the solder/Cu interface, only one kind of IMC layer could be distinguished by contrast analysis of the backscattering electron image in Figure 1. EDS analysis indicated that it consisted of 65.18 at.%Cu, 15.94 at.%In, and 18.88 at.%Sn, which corresponded to Cu-16In-19Sn. After soldering at 160 °C for 1 min, Figure 2 presents an interfacial TEM bright field (BF) image of the In-48Sn/Cu joint, along with the corresponding selected area electron diffraction patterns (SAEDPs). Detailed information of the interfacial microstructures indicated two different morphological IMC layers, as shown in Figure 2a. Upon examination, it was observed that an IMC layer exhibiting a fine-grained structure with a grain size of approximately 50 nm had formed at the Cu side of the joint. Conversely, at the solder side, the IMC grain size was much larger, at around 500 nm. In order to identify the crystallographic structure, the SAEDPs along the different zone axes were taken when the specimen was rotated in TEM. Figure 2b,c is from grain A in Figure 2a, which represents the grains in the coarse-grained IMC layer, and Figure 2d,e is from grain B in Figure 2a, which represents the grains in the fine-grained IMC layer. The lattice parameters for hexagonal  $\text{Cu}_2\text{In}$ , with dimensions of  $a = b = 0.4292$  nm,  $c = 0.5232$  nm,  $\alpha = \beta = 90^\circ$ , and  $\gamma = 120^\circ$  [23], were utilized for indexing and assigning crystallographic parameters to the SAEDPs along the  $[01\bar{1}1]$  zone axis and  $[14\bar{5}3]$  zone axis in Figure 2b,c, respectively. Similarly, the  $[11\bar{2}0]$  zone axis and  $[02\bar{2}1]$  zone axis were employed to index the SAEDPs in Figure 2d,e, respectively. This indicates a consistent match between the

observed diffraction patterns and the hexagonal  $\text{Cu}_2\text{In}$  crystal structure. The angle of rotation from the zone axis A to zone axis B can be calculated by the following equation:

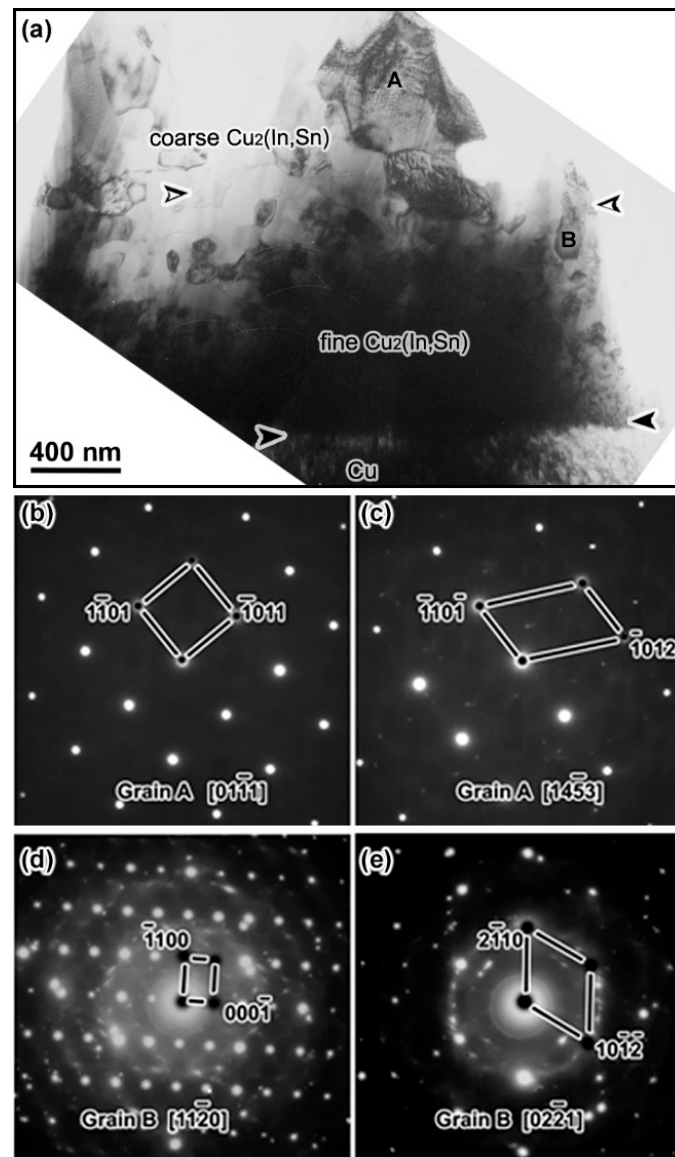
$$\cos \theta = \cos(Y_1 - Y_2) \cos(X_1 - X_2) \quad (1)$$

where  $\theta$  is the angle of rotation from zone axis A to zone axis B,  $(X_1, Y_1)$  is the rotation angle along the  $x, y$  axis when the electron beam is parallel to zone axis A, and  $(X_2, Y_2)$  is the rotation angle along the  $x, y$  axis when the electron beam is parallel to zone axis B. Based on Equation (1), the experimental measurement of the angle of rotation from the  $[14\bar{5}3]$  zone axis to the  $[01\bar{1}1]$  zone axis was found to be  $14.27^\circ$ , which is in close agreement with the theoretical value of  $14.02^\circ$ . Similarly, the experimental measurement of the rotational deviation from the  $[11\bar{2}0]$  zone axis to the  $[02\bar{2}1]$  zone axis was  $36.39^\circ$ , while the theoretical angle was  $35.22^\circ$ . Based on the morphologies of the IMC layers in Figure 2a, it can be observed that they are different. The black arrowheads indicate the interface between IMC and Cu substrate, while the white arrowheads show the interface between coarse IMC grains and fine IMC grains. However, both IMC layers belong to the  $\text{Cu}_2\text{In}$  type. It is important to note that during soldering, some Sn atoms merged with the  $\text{Cu}_2\text{In}$  lattice and occupied the substitutional sites of the In atoms due to the similar atomic diameters of In and Sn. Therefore, the intermetallic compound found at the interface is consistent with the hexagonally structured  $\text{Cu}_2(\text{In},\text{Sn})$  phase.

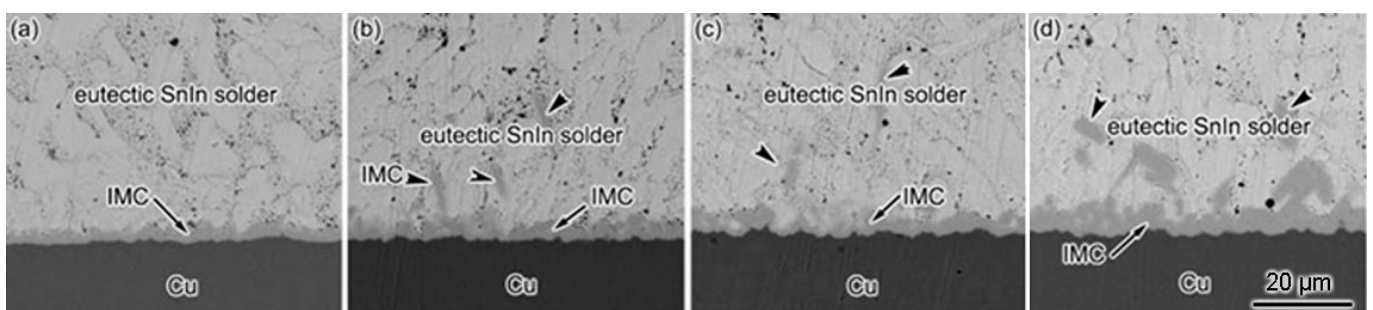
When the soldering temperature was raised up to  $200^\circ\text{C}$ , the interfacial IMC phase changed simultaneously. Moreover, with the increase in soldering time, IMC increasingly depleted from the solder/IMC interface into the bulk solder, as shown with short arrowheads in Figure 3. The EDS analysis for the interfacial IMC layer (marked with long arrows near interface) indicated that it consisted of 56.90 at.% Cu, 19.86 at.% In, and 23.24 at.% Sn, which corresponded to Cu-19In-23Sn. The interfacial microstructure of the In-48Sn/Cu contact region and the associated SAEDPs were observed after polishing and soldering at a temperature of  $200^\circ\text{C}$  for a period of 10 min in Figure 4. The SAEDPs illustrated in Figure 4b,c were identified according to the crystalline framework of  $\text{Cu}_6\text{Sn}_5$ , which is characterized by a monoclinic lattice characterized by dimensions of  $a = 1.103$  nm,  $b = 0.7294$  nm,  $c = 0.983$  nm, and  $\beta = 98.82^\circ$  [24]. During the experimental process, the rotation angle from the zone axis  $[\bar{2}51]$  to the zone axis  $[\bar{3}54]$  yielded a measured value of  $26.2^\circ$ , which was consistent with the theoretical value of  $26.63^\circ$ . Hence, it can be concluded that the IMC present at the interface can be identified as  $\text{Cu}_6(\text{In},\text{Sn})_5$ .



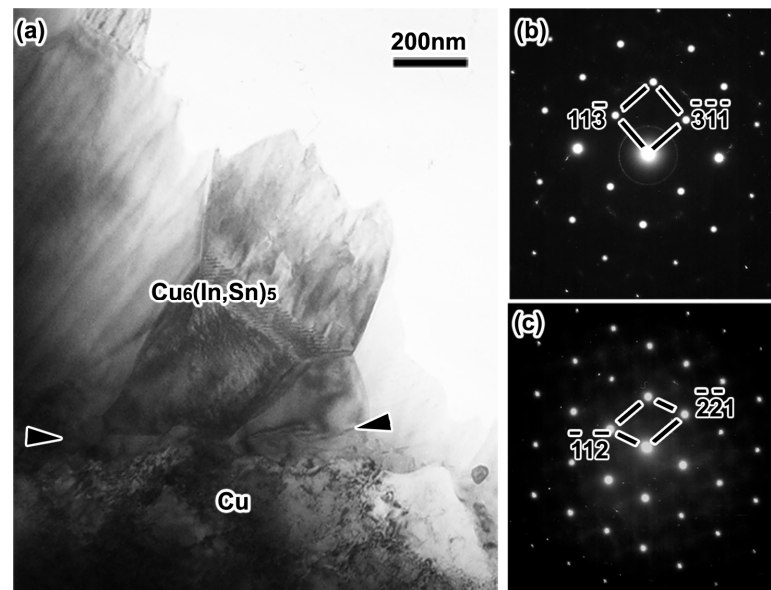
**Figure 1.** SEM images showing interfacial microstructures of In-48Sn/Cu interface after liquid-state aging for different times at  $160^\circ\text{C}$ : (a) 5 min; (b) 10 min; (c) 30 min; (d) 60 min.



**Figure 2.** TEM bright field image (a) showing interfacial microstructures of In-48Sn/Cu interface after reflowing at 160 °C for 1 min, and the corresponding SAEDPs of (b,c) from grain A in (a), as well as SAEDPs of (d,e) from grain B in (a).

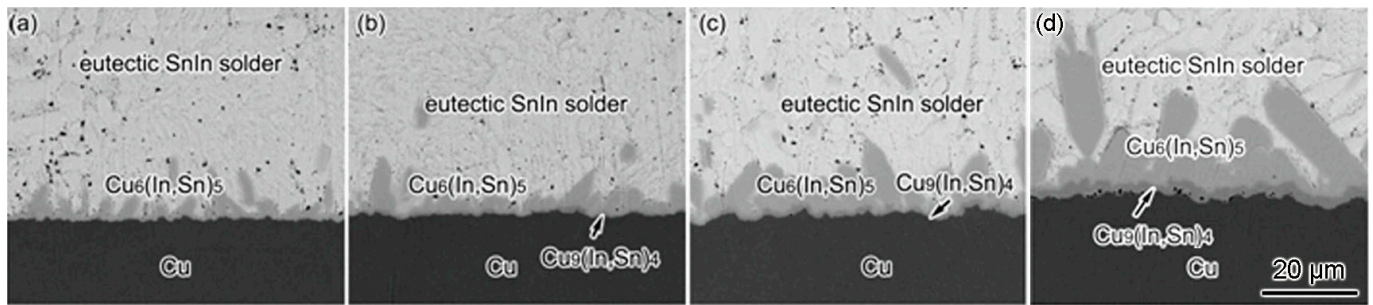


**Figure 3.** SEM images showing interfacial microstructures of In-48Sn/Cu interface after liquid-state aging for different times at 200 °C: (a) 5 min; (b) 10 min; (c) 30 min; (d) 60 min. Long arrows represent the IMCs at the interface after reaction, while short arrowheads indicate the depleted IMCs inside the bulk solder.

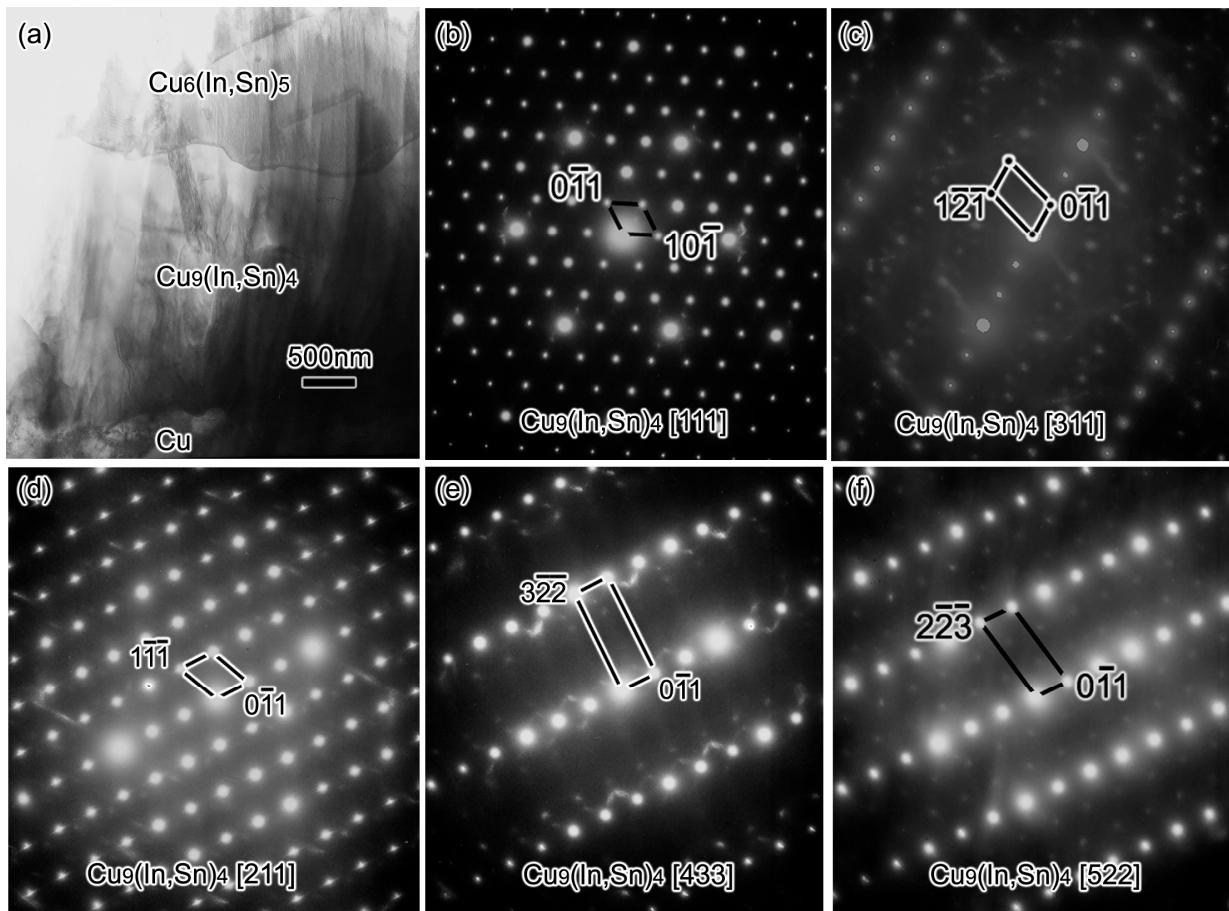


**Figure 4.** TEM bright field image (a) of In-42Sn/Cu interface after liquid-state aging for 10 min at 200 °C and corresponding SAEDPs (b,c).

Figure 5 shows the SEM cross-sectional morphologies of the In-48Sn/Cu interface after soldering at 250 °C for different times. Observably, the IMC thickness on the solder side exhibited an increase with the gradual soldering time. Additionally, at the solder/IMC interface, there was a noticeable depletion of IMC from the IMC layer into the solder, consistent with the phenomenon observed in Figure 3. It is widely believed that the quantity of IMC depleted into solder from the solder/IMC interface is related to the soldering temperature. In addition, the higher soldering temperature required a shorter aging time when the IMC at the solder/IMC interface started to deplete into the solder. The IMC layer formed at the solder side dominates the growth of the whole IMC layers. The EDS analysis revealed that it was composed of 57.27 at.%Cu, 21.06 at.%In, and 21.67 at.%Sn. This is close to the elemental composition of  $\text{Cu}_6(\text{In,Sn})_5$ , which is verified by the TEM BF image and precise electron diffraction analyses. At the Cu side, with a rise in the liquid-reaction temperature, a new IMC appeared between the  $\text{Cu}_6(\text{In,Sn})_5$  and Cu substrate. Different imaging contrasts indicated the different types of IMCs formed at the In-48Sn/Cu interface, which are shown by arrows in Figure 5. It can be seen that the IMC layer is thin and grows slowly during subsequent liquid-state aging. The EDS analysis indicated that it consisted of 73.89 at.%Cu, 12.17 at.%In, and 13.94 at.%Sn. However, it was not adequate to identify the phase crystal structure because of the similar elemental compositions of  $\text{Cu}_7\text{In}_3$ ,  $\text{Cu}_9\text{In}_4$ , and  $\text{Cu}_2\text{In}$ . Further investigation was needed. Figure 6 shows a bright field image of the In-48Sn/Cu interface and SAEDPs of the IMC at the Cu side. The SAEDPs were identified as having a  $\text{Cu}_9\text{In}_4$ -type crystal structure, which exhibits a cubic lattice with dimensions of  $a = b = c = 0.9097$  nm and angles of  $\alpha = \beta = \gamma = 90^\circ$  [23]. The rotation angle values measured in the experiment and the theoretical values are listed in Table 1. It is evident that the rotation angle values measured in the experiment are close to the theoretical values. Hence, the intermetallic compound generated at the Cu interface region between In-48Sn and Cu can be characterized as  $\text{Cu}_9(\text{In,Sn})_4$ , whose crystal structure belongs to the  $\text{Cu}_9\text{In}_4$  phase.



**Figure 5.** SEM images showing interfacial microstructures of In-48Sn/Cu interface after liquid-state aging for different times at 250 °C: (a) 5 min; (b) 10 min; (c) 30 min; (d) 60 min.



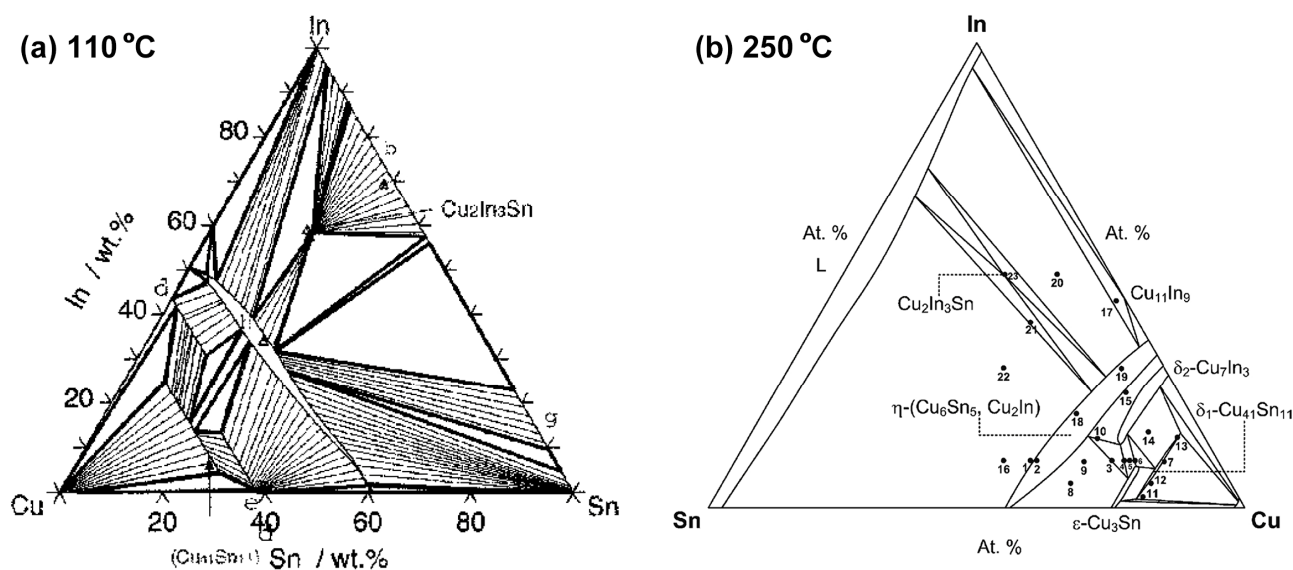
**Figure 6.** TEM bright field image showing the interfacial microstructure between eutectic SnIn and Cu (a) and SAEDPs (b–f) of interfacial IMC beside the Cu substrate after liquid-state aging at 250 °C for 30 min.

**Table 1.** A comparison of the rotation angle measured in the experiment with the theoretical values.

Zone Axis 1	Zone Axis 2	Experimental Values (°)	Theoretical Values (°)
[311]	[111]	29.50	28.78
[211]	[433]	11.42	11.72
[433]	[522]	17.19	16.29

According to the above mentioned results, the microstructures and crystallographic structure of interfacial IMCs were obviously affected by the soldering temperature. Numer-

ous theoretical and experimental studies have been conducted to establish the complete ternary phase diagram of Cu-In-Sn [25,26]. Figure 7a displays the isothermal section of the Cu-In-Sn ternary phase diagram at 110 °C, as presented by Liu et al., which is the lowest temperature reported in the literature for this ternary system [25]. They identified the  $\eta$  phase, which corresponds to  $\text{Cu}_6\text{Sn}_5$  or  $\text{Cu}_2\text{In}$ , as continuously existing from the Cu-In to the Cu-Sn systems [27]. However, when compared to the findings of the current study, it appears that the compositional range of the  $\eta$  phase should be broader. This observation was also supported by Lin et al. [26], who investigated the isothermal section of the Cu-In-Sn ternary phase diagram at 250 °C. They observed that the  $\eta$ -( $\text{Cu}_6\text{Sn}_5$ ,  $\text{Cu}_2\text{In}$ ) phase formed a continuous solid solution, as depicted in Figure 7b. These studies indicate that the understanding of the  $\eta$  phase in the Cu-In-Sn system has evolved with further investigations, suggesting a wider compositional range for this phase than previously reported. Indeed, the crystal structures between  $\text{Cu}_6\text{Sn}_5$  and  $\text{Cu}_2\text{In}$  are difficult to distinguish based solely on these studies. The  $\eta$ - $\text{Cu}_6\text{Sn}_5$  phase is known to possess a NiAs B8 crystal structure with lattice parameters  $a = b = 0.4200$  nm,  $c = 0.5090$  nm,  $\alpha = \beta = 90^\circ$ , and  $\gamma = 120^\circ$ , which is stable above 189 °C [28,29]. It is important to mention that  $\eta$ - $\text{Cu}_6\text{Sn}_5$  often displays a superstructure phenomenon [30,31]. This superstructure refers to a crystalline formation that transitions from disorder to orderliness when the alloy cools slowly. Additionally, the superstructure enhances the high-temperature stability and reduces resistance in the crystalline structure. Based on the detailed electron diffraction patterns (EDPs) depicted in Figure 4b,c, none of the three-modulated or five-modulated structures were observed. When the temperature is below 189 °C,  $\text{Cu}_6\text{Sn}_5$  transforms into  $\eta'$ - $\text{Cu}_6\text{Sn}_5$ , which possesses a monoclinic structure with lattice parameters  $a = 1.1033$  nm,  $b = 0.7294$  nm,  $c = 0.9830$  nm,  $\alpha = 90^\circ$ ,  $\beta = 98.82^\circ$ , and  $\gamma = 90^\circ$  [28]. It is evident that the lattice parameters of  $\eta'$ - $\text{Cu}_6\text{Sn}_5$  were not consistent with the observed EDPs when the In-48Sn/Cu joint was soldered below 160 °C (although this soldering temperature could be improved to 180 °C), as depicted in Figure 2b,d. Based on the factors mentioned above, including the similar diameters of Sn and In atoms, it can be inferred that the IMC that was formed at the interface between the eutectic SnIn solder and Cu during the process soldering below 180 °C may be identified as a hexagonal  $\text{Cu}_2\text{In}$  lattice that has been dissolved with some Sn atoms. However, the crystal structure of IMC formed above 200 °C should be identified as  $\text{Cu}_6(\text{In},\text{Sn})_5$ , as shown in Figures 3 and 4.



**Figure 7.** Ternary Cu-In-Sn isothermal section at 110 °C (adapted from Ref. [25]) and 250 °C (adapted from Ref. [26]).



Although the  $\text{Cu}_9(\text{In,Sn})_4$  phase was not discovered in the ternary phase diagram of Liu et al. [25] and Lin et al. [26], it was universally observed in the Cu/In thin films [12,13] when the soldering temperature increased above 200 °C. Thus, during liquid-state aging, the  $\text{Cu}_6(\text{In,Sn})_5$  phase first nucleated and grew at the interface of the liquid solder/Cu substrate [32]. It is known that the melting point of the  $\text{Cu}_6(\text{In,Sn})_5$  phase is higher than the soldering temperature (250 °C). Thus, the formation of the  $\text{Cu}_9(\text{In,Sn})_4$  phase is believed to be a solid–solid diffusion process, which means that Cu further reacts with the  $\text{Cu}_6(\text{In,Sn})_5$  phase to form the  $\text{Cu}_9(\text{In,Sn})_4$  phase [33]. In summary, the species of interfacial IMCs formed at the In-48Sn/Cu interface had a close relationship with the soldering temperature and are listed in Table 2.

**Table 2.** The IMC species formed at the In-48Sn/Cu interface during long-time liquid soldering ranging from 160 °C to 250 °C.

IMC	Soldering Temperature (°C)		
	160	200	250
	Duplex $\text{Cu}_2(\text{In,Sn})$	$\text{Cu}_6(\text{In,Sn})_5$	$\text{Cu}_6(\text{In,Sn})_5 + \text{Cu}_9(\text{In,Sn})_4$

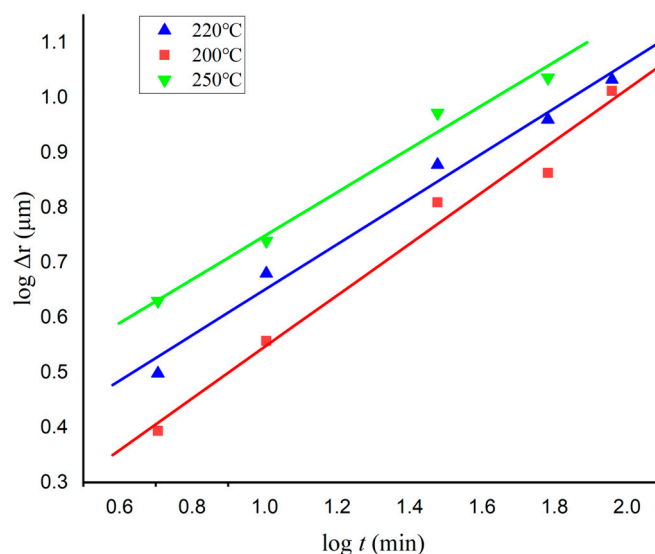
### 3.2. Growth Kinetics of the Interfacial IMC Layer

Generally, the growth of the IMC layer may be expressed as a function of aging time as:

$$\Delta x = kt^n \text{ or } \log \Delta x = A + n \log t \quad (2)$$

where  $\Delta x$  is the IMC thickness,  $k$ —the growth rate constant,  $t$ —the reaction time, and  $n$  is the time exponent. There is an empirical notion that the values of  $n$  could reflect the rate-controlling mechanisms of IMC growth. Generally,  $n$  takes the value of 0.5 when IMC growth is controlled by volume diffusion. In contrast, if  $n$  takes the value of 1, it means that the growth of IMC is controlled by the reaction at the interface.

Figure 8 shows the growth kinetics results after the samples were liquid-state soldered at 200, 220, and 250 °C. The value of the time exponent ( $n$ ) can be obtained from the slope of  $\log \Delta x$  versus  $\log t$ , which is listed in Table 3 with the growth rate constants at different aging temperatures. From Table 3, it was observed that IMC growth was preferably controlled by species diffusion during liquid-state soldering at the temperature range of the present study, although the time exponent ( $n$ ) was not exactly 0.5.



**Figure 8.** The logarithm of IMC thickness as a function of the logarithm of aging time during liquid-state aging at a temperature range of 200 °C to 250 °C.

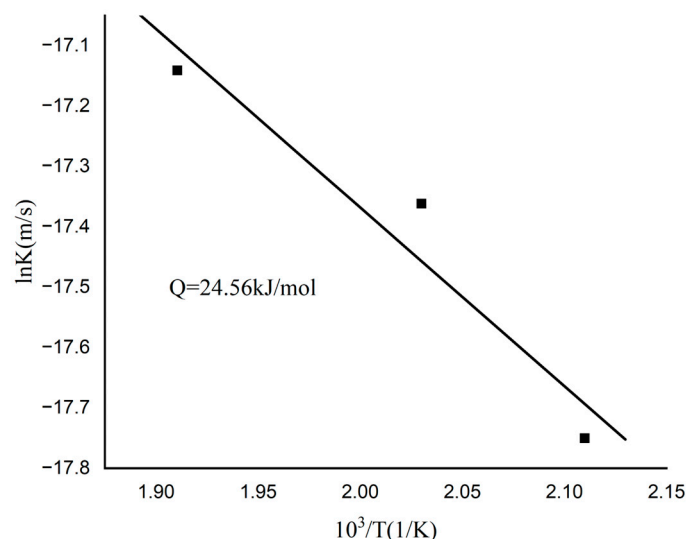
**Table 3.** Growth rate exponents  $n$  obtained from Equation (2) when the samples were soldered at a temperature range of 200 °C to 250 °C.

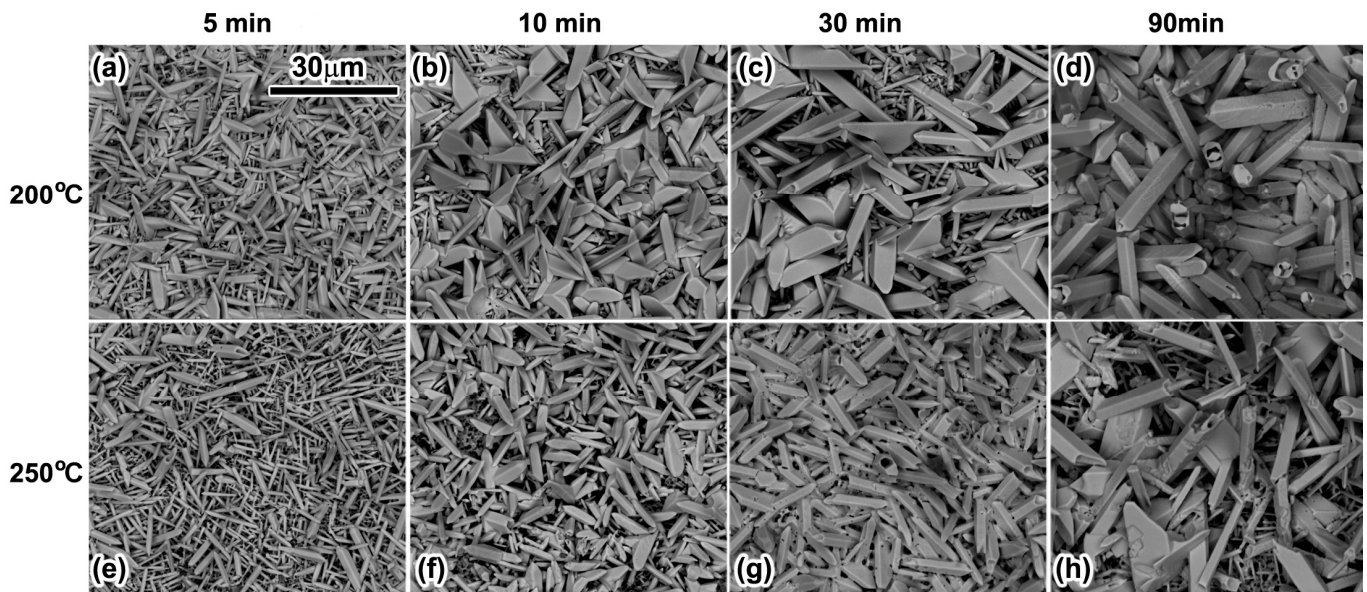
	Soldering Temperature (°C)		
	200	220	250
$n$	0.498	0.440	0.447

An Arrhenius relationship was used to calculate the activation energy for IMC growth, as follows:

$$k = k_0 \exp\left(-\frac{Q}{RT}\right) \quad (3)$$

where  $k$  is the growth rate constant,  $k_0$  is the pre-exponential coefficient,  $Q$  is activation energy,  $R$  is the gas constant (8.314 J/mol K), and  $T$  is the aging temperature (absolute units). The activation energy for the interfacial IMC growth during liquid-state soldering, obtained from the slope of the Arrhenius plots, as shown in Figure 9, is 24.56 kJ/mol, which is distinctly smaller than the activation energy of scallop-type  $\text{Cu}_6\text{Sn}_5$ , where the activation energy was around 52.48 kJmol<sup>-1</sup> [34]. However, it is consistent with the activation energy of Cu in liquid Sn (19.5 kJmol<sup>-1</sup>) [35]. Figure 10 shows top-viewed images of IMC morphology for In-48Sn/Cu soldered under various soldering conditions. It can be seen that three types of  $\text{Cu}_6(\text{In},\text{Sn})_5$  morphology were observed in the In-48Sn/Cu interface during soldering. In detail, only needle-like  $\text{Cu}_6(\text{In},\text{Sn})_5$  formed at the shorter soldering time, as shown in Figure 10a,e. With an extension in soldering time, the needle-like  $\text{Cu}_6(\text{In},\text{Sn})_5$  transformed into rod-like  $\text{Cu}_6(\text{In},\text{Sn})_5$  of polyhedron-type [36]. Finally, the IMC changed to a tube-like morphology with a core, as shown in Figure 10d. Whatever the morphology of the  $\text{Cu}_6(\text{In},\text{Sn})_5$  IMC, a ubiquitous phenomenon is that there are grain boundaries/molten channels in between  $\text{Cu}_6(\text{In},\text{Sn})_5$  crystals, which could be fast diffusion channels of Cu to solder [37,38]. This is different from Sn diffusion along the grain boundary/molten channel controlled by the growth of  $\text{Cu}_6\text{Sn}_5$  [37]; otherwise, the morphology of  $\text{Cu}_6(\text{In},\text{Sn})_5$  would be scalloped-type and the time exponent ( $n$ ) of the IMC growth kinetic would be 1. Therefore, during soldering above 200 °C, Cu diffusing along the grain boundary/molten channel in between  $\text{Cu}_6(\text{In},\text{Sn})_5$  crystals controlled IMC growth.

**Figure 9.** An Arrhenius plot for the IMC layer formed during eutectic SnIn/Cu soldering reactions at the temperature range of 200 °C to 250 °C.



**Figure 10.** Top-viewed images of IMC morphology for 48Sn52In/Cu soldered at 200 °C for (a) 5 min, (b) 10 min, (c) 30 min and (d) 90 min, together with those soldered at 250 °C for (e) 5 min, (f) 10 min, (g) 30 min and (h) 90 min. The scale bar in (a) is applicable for all images.

#### 4. Conclusions

Microstructural changes between In-48Sn and Cu were investigated using TEM bright-field (BF) imaging and precise diffraction analyses over a temperature range of 160 °C to 250 °C for up to 90 min. When the sample was subjected to reflow at 160 °C for an extended period, only one type of intermetallic compound (IMC) formed at the interface between In-48Sn and Cu, which was  $\text{Cu}_2(\text{In},\text{Sn})$ . However, two distinct morphologies were observed: a coarse-grained layer on the solder side and a fine-grained layer on the Cu side. Precise electron diffraction analyses revealed that both IMC layers exhibited a  $\text{Cu}_2\text{In}$ -type crystal structure. As the aging temperature increased beyond 200 °C, a phase called  $\text{Cu}_6(\text{In},\text{Sn})_5$  initially nucleated and grew at the interface between In-48Sn and Cu. Subsequently, a  $\text{Cu}_9(\text{In},\text{Sn})_4$  phase formed through a solid-state reaction between  $\text{Cu}_6(\text{In},\text{Sn})_5$  and Cu. Growth kinetic analyses indicated that the activation energy of IMC growth is 24.56 kJ/mol, which is consistent with the activation energy of Cu in liquid Sn (19.5 kJ/mol). This implied that the IMC growth above 200 °C was mainly controlled by the diffusion of Cu atoms dissolved into the liquid solder through grain boundaries/molten channels in between  $\text{Cu}_6(\text{In},\text{Sn})_5$  crystals.

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