



# Article Temperature-Dependent Electrical Properties of Al<sub>2</sub>O<sub>3</sub>-Passivated Multilayer MoS<sub>2</sub> Thin-Film Transistors

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Abstract: It is becoming more important for electronic devices to operate stably and reproducibly under harsh environments, such as extremely low and/or high temperatures, for robust and practical applications. Here, we report on the effects of atomic-layer-deposited (ALD) aluminum oxide (Al<sub>2</sub>O<sub>3</sub>) passivation on multilayer molybdenum disulfide (MoS<sub>2</sub>) thin-film transistors (TFTs) and their temperature-dependent electrical properties, especially at a high temperature range from 293 K to 380 K. With the aid of ultraviolet-ozone treatment, an Al<sub>2</sub>O<sub>3</sub> layer was uniformly applied to cover the entire surface of MoS<sub>2</sub> TFTs. Our Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFTs exhibited not only a dramatic reduction of hysteresis but also enhancement of current in output characteristics. In addition, we investigated the temperature-dependent behaviors of the TFT performance, including intrinsic carrier mobility based on the Y-function method.

**Keywords:** transition metal dichalcogenide; molybdenum disulfide; thin-film transistor; passivation; contact resistance; intrinsic mobility

# 1. Introduction

With the discovery of graphene, two-dimensional (2D) layered materials have been drawing intense research interest. Despite its excellent mechanical, optical, and electrical properties, however, graphene is considered unsuitable as an active component of field effect transistors due to its lack of a band gap [1–3]. In efforts to overcome this limitation, i.e., to produce a band gap in graphene, a great deal of research has been carried out, but the results thus far have only added to the complexity of the processes and reduced mobility [4,5].

In this context, layered 2D transition metal dichalcogenides (TMDs)-based thin-film transistors (TFTs) are drawing considerable attention as promising candidates to lead the next-generation transistor technology. Among the 2D TMDs, molybdenum disulfide ( $MoS_2$ ) is at the center of attention, and relevant research is underway [6–8].  $MoS_2$  TFTs have excellent carrier mobility, a high on- and off-current ratio ( $I_{on}/I_{off}$ ), mechanical flexibility, and a relatively large band gap [9–12]. Despite these properties, when the  $MoS_2$  channel is exposed to atmospheric environments,  $MoS_2$  TFTs exhibit a hysteresis phenomenon in the transfer characteristic. Various research groups have already shown that this hysteresis phenomenon occurs in  $MoS_2$  TFTs due to the effects of oxygen and water in the air [13–15]. To suppress this phenomenon, the surface of a semiconducting active channel must be isolated from the air by passivation layers, e.g., atomic-layer-deposited (ALD) aluminum oxide ( $Al_2O_3$ ) or hafnium dioxide.

It is known that the surface of pristine (i.e., without any surface modifications)  $MoS_2$  does not react well with trimethylaluminum (TMA), which is used as a precursor to form  $Al_2O_3$  layers, due to the absence of dangling bonds [16–19]. However, high-*k* dielectric layers without pinholes and/or cracks are indispensable for nano-electronic devices. To improve the wettability and reactivity between the MoS<sub>2</sub> surface and TMA and to ensure complete isolation of the surface from atmospheric environments, some strategies prior to deposition of the passivation layer were proposed, for example, oxygen plasma or ultraviolet-ozone (UV-ozone) treatment [18–23]. Conventional MoS<sub>2</sub> TFTs without passivation layers exhibited different electrical properties at high temperatures with respect to the height of the Schottky barrier between the metal electrodes and MoS<sub>2</sub> [8,24,25]. For a relatively high Schottky barrier, charge carriers (i.e., electrons) were blocked by the barrier and thus could not pass

at a low temperature. However, as the temperature increased, those carriers started to transport into the MoS<sub>2</sub> channels through thermionic emission [26]. This led to an increase in carrier mobility with respect to increased temperature. Unlike previous reports on MoS<sub>2</sub> TFTs, little research has been conducted on the transport mechanism at high temperatures in passivated MoS<sub>2</sub> TFTs.

The present study investigated how passivated  $MoS_2$  TFTs with a high Schottky barrier change in a high-temperature environment. First, cross-sectional transmission electron microscopy (TEM) was utilized to visualize the quality of  $Al_2O_3$  deposited on the  $MoS_2$  surface through UV-ozone treatment. The surface morphology of the  $Al_2O_3$  on the  $MoS_2$  channel was investigated by atomic force microscopy (AFM), depending on UV-ozone treatment. Also, the electrical properties of the devices were characterized to assess the effectiveness of  $Al_2O_3$  passivation. As a result, it was confirmed that carrier mobility improved and that the hysteresis effect decreased significantly; thus, the desired effect of passivation was achieved. After that, the same tests were performed while the temperature was increased from room temperature to 380 K, and the results showed that the mobility also increased with increasing TFT operating temperature. To analyze the increment of the mobility, the *Y*-function method was employed to examine the intrinsic mobility of the  $MoS_2$  TFT by excluding contact resistance between the metal electrode and  $MoS_2$  channel.

### 2. Materials and Methods

#### 2.1. Device Fabrication

Multilayer MoS<sub>2</sub> flakes were mechanically exfoliated from bulk MoS<sub>2</sub> (SPI Supplies, West Chester, PA, USA) through the conventional Scotch tape method and transferred onto thermally grown silicon dioxide (SiO<sub>2</sub>) with a thickness of 300 nm used as a gate insulator. A *p*-type doped silicon (Si) wafer (resistivity  $\leq 0.005 \ \Omega \cdot cm$ ) was used as a gate electrode and substrate. To remove the organic and inorganic residues resulting from the transfer procedure, the MoS<sub>2</sub>-covered SiO<sub>2</sub>/Si substrate was cleaned in acetone and isopropyl alcohol for 1 h 30 min and 30 min, respectively. Titanium/gold (20 nm/100 nm) layers were sequentially deposited by electron beam (e-beam) evaporation, and then source/drain (S/D) electrodes were patterned through conventional photolithography and wet etching processes. To improve the contact between the S/D electrodes and MoS<sub>2</sub> active channel, the devices were annealed at 200 °C for 2 h under mixed gas flow (100 sccm of Ar/10 sccm of H<sub>2</sub>).

#### 2.2. UV-Ozone Treatment

For uniform growth of the  $Al_2O_3$  layer, as-fabricated  $MoS_2$  TFTs were subjected to UV-ozone treatment (UVC-30, Jaesung Engineering Co., Anyang-Si, Korea) at power of 15 to 25 mW for 5 min with wavelengths of 185 nm and 254 nm.

## 2.3. ALD Al<sub>2</sub>O<sub>3</sub> Passivation

An Al<sub>2</sub>O<sub>3</sub> layer was deposited on the UV-ozone-treated MoS<sub>2</sub> TFTs by ALD (Lucida D100, NCD Co., Ltd., Daejeon, Korea). The deposition conditions of the unit ALD sequence consisted of TMA  $(0.2 \text{ s})/N_2 (10 \text{ s})/H_2O (0.2 \text{ s})/N_2 (10 \text{ s})$  with a chamber temperature of 200 °C. The thickness of the deposited Al<sub>2</sub>O<sub>3</sub> per unit sequence was controlled to be approximately 0.118 nm. The unit sequence was iterated 340 times for a target thickness of 40 nm.

Then a selected area in the  $Al_2O_3$  deposited on the S/D electrode was eliminated to create the electrical contact during measurements.

#### 2.4. Device Characterization

The cross section of the Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFT was analyzed by TEM (Titan Cubed 60-300, FEI, Hillsboro, OR, USA). The surface morphologies of the Al<sub>2</sub>O<sub>3</sub> on MoS<sub>2</sub> TFTs were investigated by AFM (XE7, Park Systems, Suwon-si, Korea) with noncontact mode operation. The electrical properties of the MoS<sub>2</sub> TFTs were measured by using a semiconductor characterization system (4200 SCS, Keithley, Cleveland, Ohio, USA) with a probe station. The temperature dependence of the TFT performance was characterized by using a home-made temperature-controlling vacuum chamber with temperatures ranging from 293 K to 380 K under a moderate vacuum environment (<10<sup>-3</sup> torr). Before each measurement, the temperature was maintained for 10 min to minimize variations in device performance.

# 3. Results and Discussion

Figure 1a shows a three-dimensional (3D) schematic illustration of the back-gated TFT encapsulated with  $Al_2O_3$  on top of the multilayer  $MoS_2$  active channel, which was also confirmed in a top-view optical microscope image, as shown in Figure 1b.  $MoS_2$  and contact holes in the S/D electrodes are indicated by red dashed and green dotted lines, respectively. It should be noted that we could not find any pinholes or cracks in the  $Al_2O_3$  layer resulting from UV-ozone pre-treatment, which will be further discussed in relation to Figure 2. Channel length (*L*) and width (*W*) of the  $MoS_2$  device were 13.06 and 20.32 µm, respectively, used for calculating the TFT performance in relation to various temperatures.

Figure 1c shows a cross-sectional TEM image of the Al<sub>2</sub>O<sub>3</sub>-encapsulated MoS<sub>2</sub> TFT with UV-ozone pretreatment. It can be seen that the Al<sub>2</sub>O<sub>3</sub> with an average thickness of approximately 42.0 nm uniformly covered the entire MoS<sub>2</sub> surface. In addition, the thickness of the MoS<sub>2</sub> multilayer was estimated to be approximately 64.6 nm, indicating its nearly bulk-like energy band characteristics. The fast Fourier transform patterns and high-angle annular dark-field imaging of the MoS<sub>2</sub> are shown in Figure S1 in the Supplementary Materials.



**Figure 1.** (a) 3D schematic structure of multilayer molybdenum disulfide (MoS<sub>2</sub>) thin-film transistor (TFT) passivated with aluminum oxide (Al<sub>2</sub>O<sub>3</sub>); (b) optical microscope and (c) cross-sectional transmission electron microscopy (TEM) images of the MoS<sub>2</sub> TFT with Al<sub>2</sub>O<sub>3</sub> passivation, respectively.



**Figure 2.** Atomic force microscopy (AFM) images of Al<sub>2</sub>O<sub>3</sub> surfaces on MoS<sub>2</sub> active channel: (**a**) without and (**b**) with ultraviolet-ozone (UV-ozone) treatment for 5 min before atomic-layer-deposited (ALD) procedure. RMS: root-mean-square.

Figure 2 compares the surfaces morphologies of the Al<sub>2</sub>O<sub>3</sub> deposited on MoS<sub>2</sub> with and without UV-ozone pretreatment, which were measured by AFM scanning in a 1  $\mu$ m × 1  $\mu$ m region. Many clusters and boundaries can be observed on the surface of the Al<sub>2</sub>O<sub>3</sub> (thickness of approximately 40 nm) deposited on the pristine MoS<sub>2</sub> (Figure 2a). However, in the case of Al<sub>2</sub>O<sub>3</sub> deposited on the MoS<sub>2</sub> with UV-ozone pretreatment, it shows complete coverage with high uniformity (Figure 2b). The root-mean-square surface roughness (*R*<sub>RMS</sub>) of the directly deposited Al<sub>2</sub>O<sub>3</sub> is 0.672 nm, but it decreases to 0.190 nm with a 5-min UV-ozone treatment on MoS<sub>2</sub>. These results indicate that UV-ozone exposure is an efficient way to achieve uniform growth of Al<sub>2</sub>O<sub>3</sub> on a MoS<sub>2</sub> surface.

Previous studies have found that the electrical properties and device performance of MoS<sub>2</sub> transistors can be enhanced by high-k dielectric encapsulation [27,28]. Therefore, we measured the electrical properties of the MoS<sub>2</sub> TFTs to elucidate the effect of Al<sub>2</sub>O<sub>3</sub> passivation on multilayer MoS<sub>2</sub> with UV-ozone pretreatment. Figure 3a compares the transfer characteristic curves  $I_{ds} - V_{gs}$  of the MoS<sub>2</sub> TFTs without and with Al<sub>2</sub>O<sub>3</sub> passivation layers with the application of a source-to-drain voltage  $(V_{\rm ds})$  of 1 V. Before Al<sub>2</sub>O<sub>3</sub> passivation, the pristine MoS<sub>2</sub> TFT exhibits a large hysteresis (black-solid lines in Figure 3a). However, after Al<sub>2</sub>O<sub>3</sub> passivation with UV-ozone pre-treatment, represented by the red solid lines in Figure 3a, the transfer curve obtained from a forward  $V_{\rm gs}$  scan closely approached that obtained from a reverse  $V_{gs}$  scan and vice versa, which indicates a distinct reduction of hysteresis behavior (see also Figure S3). It should be noted that reduction of hysteresis was not observed in the MoS<sub>2</sub> TFTs with only UV-ozone treatment, i.e., without Al<sub>2</sub>O<sub>3</sub> passivation (Figure S4). For quantitative comparison, we define the difference of threshold voltage ( $\Delta V_{\text{th}}$ ) by subtraction between the  $V_{\text{th}}$  values in the forward and reverse transfer characteristic curves, which are estimated to be 20.1 V and 0.5 V for the pristine and Al<sub>2</sub>O<sub>3</sub>-passivated devices, respectively. However, I<sub>off</sub> in the Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFT increased by about one order of magnitude, resulting in a decrease in the  $I_{on}/I_{off}$  from 10<sup>6</sup> to 10<sup>5</sup>. The results may be attributed to the fact that excess electrons could be induced in the MoS<sub>2</sub> active channel due to the positive fixed charges in the  $Al_2O_3$  layer [23,28].

According to transport physics in TFTs, the relation among  $I_{ds}$ ,  $V_{gs}$  and  $V_{ds}$  are expressed for a linear regime ( $|V_{ds}| < |V_{gs} - V_{th}|$ ) as in [29]:

$$I_{\rm ds} = \frac{\mu_{\rm eff} W C_{\rm GI}}{L} \left[ (V_{\rm gs} - V_{\rm th}) V_{\rm ds} - \frac{V_{\rm ds}^2}{2} \right], \tag{1}$$

where  $C_{\text{GI}}$  is capacitance per unit area of the gate insulator, and  $\mu_{\text{eff}}$  is the field-effect mobility, one of the figure-of-merit for evaluating TFT characteristics. Based on the standard model of

metal-oxide-semiconductor (MOS) FETs and a parallel plate model of gate capacitance [8,29,30],  $\mu_{\text{eff}}$  of the TFT can be expressed in terms of transconductance ( $g_{\text{m}} \equiv \partial I_{\text{ds}} / \partial V_{\text{gs}}$ ) as

$$u_{\rm eff} = \frac{Lg_{\rm m}}{WC_{\rm ox}V_{\rm ds}},\tag{2}$$

where  $C_{\rm ox}$  is capacitance per unit area of gate insulator  $(1.15 \times 10^{-8} \text{ Fcm}^{-2})$ . The maximum transconductance of the pristine MoS<sub>2</sub> TFT was  $2.31 \times 10^{-7}$  S, which was increased to  $3.25 \times 10^{-7}$  S after Al<sub>2</sub>O<sub>3</sub> passivation. As a result, the  $\mu_{\rm eff}$  values for the pristine and Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFTs were calculated to be 40.9 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup> and 57.6 cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, respectively. Figure 3b compares the output characteristic curves ( $I_{\rm ds} - V_{\rm ds}$ ) of the MoS<sub>2</sub> TFTs without and with Al<sub>2</sub>O<sub>3</sub> passivation layers under the application of  $V_{\rm gs}$  ranging from -30 V to 0 V with a step of 5 V. At a low  $V_{\rm gs}$  range from -30 V to -15 V, the  $I_{\rm ds}$  values of the Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFTs were slightly lower than those of the pristine MoS<sub>2</sub> TFT. However, the clear enhancement of the  $I_{\rm ds}$  of the passivated device has been observed at higher  $V_{\rm gs}$  than -10 V, which agrees well with Figure 3a.



**Figure 3.** Comparisons of (**a**) transfer and (**b**) output characteristics of  $MoS_2$  TFTs with respect to  $Al_2O_3$  passivation. Variations of (**c**) field-effect mobility ( $\mu_{eff}$ ) and (**d**) threshold voltage ( $\Delta V_{th}$ ) of 15 representative  $Al_2O_3$ -passivated  $MoS_2$  TFTs compared with those of pristine devices. All the  $Al_2O_3$  layers were deposited after UV-ozone treatment.

It is well known that  $MoS_2$  seems to be very sensitive to oxygen and water when exposed to an ambient environment [31]. Oxygen and water molecules can be adsorbed on the defect sites of  $MoS_2$  to induce charge traps, thus leading to a relatively larger and clockwise hysteresis as well as mobility degradation [13,32]. High-*k* dielectric passivation through ALD can be suggested as an efficient method to isolate  $MoS_2$  from atmospheric environments that include external contaminants [27,28,33]. The enhanced  $I_{ds}$  is also attributed to the annealing effect, which decreases the contact resistance between the S/D electrodes and  $MoS_2$  surface. These results are consistent with those of previous

reports on  $Al_2O_3$ -encapsulated  $MoS_2$  devices, which indicates that no significant defect was introduced in the  $MoS_2$  by the UV-ozone treatment [27,28]. The  $MoS_2$  surface before and after UV-ozone treatment were investigated using Raman and X-ray photoelectron spectroscopy (shown in Figure S2).

Figure 3c,d show the statistical distributions of  $\mu_{eff}$  and  $\Delta V_{th}$  for 15 representative MoS<sub>2</sub> TFTs in order to confirm the effect of UV-ozone pre-treatment followed by Al<sub>2</sub>O<sub>3</sub> passivation. The results of all the devices exhibit increments of the  $\mu_{eff}$  as well as reduction of the  $\Delta V_{th}$ . The average increment (reduction) rate of  $\mu_{eff}$  ( $\Delta V_{th}$ ) is 40.4% (4.2%). The individual values of  $\mu_{eff}$  and  $\Delta V_{th}$  are listed in Table 1. These results are evidence that UV-ozone treatment of the multilayer MoS<sub>2</sub> TFTs not only improves the quality of the ALD-grown Al<sub>2</sub>O<sub>3</sub> layer, but also enhances the mobility and stability of the MoS<sub>2</sub> devices.

	$\mu_{\rm eff}~({\rm cm}^2)$	$V^{-1} s^{-1}$ )	$\Delta V_{\rm th}$ (V)		
# of IFI	Before	After	Before	After	
1	31.58	36.57	15.16	1.79	
2	24.79	40.55	22.33	0.98	
3	15.49	23.58	19.66	0.87	
4	32.07	42.59	19.32	0.64	
5	36.99	48.52	16.32	1.04	
6	51.19	75.53	20.05	0.94	
7	32.79	42.18	12.25	0.5	
8	40.93	57.62	20.09	0.54	
9	31.87	37.79	16.18	0.51	
10	23.49	35.55	19.43	0.17	
11	36.83	57.22	24.37	0.72	
12	33.14	41.69	13.08	0.87	
13	10.82	18.19	20.61	0.53	
14	74.57	96.67	8.58	0.15	
15	32.11	46.63	14.6	0.47	

**Table 1.** Comparison of  $\mu_{\text{eff}}$  and  $\Delta V_{\text{th}}$  values of 15 representative Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFTs. <sup>1</sup>

<sup>1</sup> All the Al<sub>2</sub>O<sub>3</sub> layers were deposited after UV-ozone treatment.

To demonstrate the temperature-dependent behavior of the  $Al_2O_3$ -passivated  $MoS_2$  TFT, the electrical properties of the device were measured at 10 different temperatures ranging from 293 K to 380 K under a moderate vacuum condition. Figure 4a presents a semi-logarithmic-scale plot of the temperature-dependent transfer characteristic curves at five different temperatures (293, 310, 330, 350 and 370 K). At the semi-logarithmic-scale of  $I_{ds}$ , variation of  $I_{on}$  was not clearly distinguished. To reveal the enhancement of  $I_{on}$ , linear scale plots of the transfer curves at five other temperatures (300, 320, 340, 360 and 380 K) are compared in Figure 4b.

As shown in Figure 4,  $I_{ds}$  increased with increasing temperature over the whole  $V_{gs}$  region. The results also confirmed the linear increment of the  $I_{ds}$  values at a  $V_{gs}$  of 40 V with respect to 10 different temperatures, as shown in the inset of Figure 4a. From the transfer characteristic curves, we extracted the  $V_{th}$  at 10 different temperatures, which linearly shifted toward the negative  $V_{gs}$  region with increasing temperature (inset of Figure 4b).

Figure 5a shows the output characteristics of the  $Al_2O_3$ -passivated  $MoS_2$  TFT at 293 K with those at 380 K, which exhibits almost double increments of  $I_{ds}$ . The results also imply that high temperature would influence the other aspects of TFT performance. However, our TFT architecture has a two-terminal configuration resulting in contact resistance between the S/D electrodes and the  $MoS_2$ active channel, which should be minimized for further investigation of carrier transport mechanisms.



**Figure 4.** Transfer characteristics of  $Al_2O_3$ -passivated MoS<sub>2</sub> TFT under various temperatures ranging from 293 K to 380 K in (**a**) logarithmic and (**b**) linear scales. Insets: temperature-dependent variations of (**a**)  $I_{ds}$  at  $V_{gs}$  = 40 V and (**b**)  $V_{th}$ .

In this context, the *Y*-function method can be suggested as a proper solution for handling contact resistance in two-terminal systems [8,34–37], which underestimates the intrinsic capabilities of active materials. By adopting the mobility reduction coefficient  $\eta$  [34], Equation (1) can be rewritten as

$$I_{\rm ds} = \frac{WC_{\rm GI}}{L} \frac{\mu_0}{\left[1 + \eta \left(V_{\rm gs} - V_{\rm th}\right)\right]} \left(V_{\rm gs} - V_{\rm th}\right) V_{\rm ds} , \qquad (3)$$

where  $\mu_0$  is the intrinsic mobility excluding any contact resistance component. Considering the definition of transconductance,  $g_m$  is also modified using  $\eta$  as follows:

 $g_{\rm m} \equiv \frac{\partial I_{\rm ds}}{\partial V_{\rm gs}} = \frac{W C_{\rm GI}}{L} \frac{\mu_0}{\left[1 + \eta \left(V_{\rm gs} - V_{\rm th}\right)\right]^2} V_{\rm ds} \,.$ 



**Figure 5.** (a) Output characteristics of Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFT under different temperatures of 293 K and 380 K ( $V_{gs}$  from -40 to 0 V in 5 V steps); (b) comparison of  $\mu_{eff}$  and the intrinsic carrier mobility  $\mu_0$  at various temperatures ranging from 293 K to 380 K. Inset: temperature-dependent variations of the difference between  $\mu_{eff}$  and  $\mu_0$  ( $\Delta\mu$ ).

Then,  $\eta$  is eliminated by combining Equations (3) and (4), and the Y-function can be expressed as

$$Y \equiv \frac{I_{\rm ds}}{\sqrt{g_{\rm m}}} = \sqrt{\frac{W}{L}C_{\rm GI}\mu_0 V_{\rm ds}} \times (V_{\rm gs} - V_{\rm th}).$$
<sup>(5)</sup>

(4)

Finally, we can extract the intrinsic carrier mobility  $\mu_0$  from the slope of the  $Y - V_{gs}$  plot [8].

Figure 5b presents  $\mu_{\text{eff}}$  and  $\mu_0$  of the Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFT at various temperatures, which were calculated from Equations (2) and (5), respectively. At all temperatures, the results showed that (i) all  $\mu_0$  values were higher than  $\mu_{\text{eff}}$ , indicating the existence of relatively high Schottky barriers and subsequent contact resistances in our devices; and (ii) both  $\mu_{\text{eff}}$  and  $\mu_0$  gradually increased with increasing temperature, which can be attributed to reduction of the contact resistance and increased thermionic emission due to high temperature [25,38]. The inset of Figure 5b presents the temperature-dependent variation of the difference between  $\mu_{\text{eff}}$  and  $\mu_0$  ( $\Delta\mu$ ), which shows linear response behavior with respect to temperature increase. The details of  $\mu_{\text{eff}}$ ,  $\mu_0$  and  $\Delta\mu$  are provided in Table 2.

Temperature	293 K	300 K	310 K	320 K	330 K	340 K	350 K	360 K	370 K	380 K
$\mu_{\rm eff}$	6.4 7.73	6.57 8.07	6.81 8.45	6.95 8.68	7.31	7.39 9.47	7.61	7.81 10.39	7.99 10.81	8.14 11 31
$\Delta \mu$	1.33	1.5	1.64	1.73	2.01	2.08	2.28	2.58	2.82	3.17

**Table 2.** Values of  $\mu_{\text{eff}}$  and  $\mu_0$  at various temperatures.

# 4. Conclusions

In this paper, we reported on the effects of ALD-Al<sub>2</sub>O<sub>3</sub> passivation on multilayer MoS<sub>2</sub> TFTs and the temperature-dependent variation of the TFT performance. High-*k* Al<sub>2</sub>O<sub>3</sub> layers were uniformly passivated over the entire surface of MoS<sub>2</sub> TFTs with the aid of UV-ozone treatment leading to (i) huge reductions of the hysteretic transfer curves; (ii) saturation current enhancement of the output characteristics; and (iii) increases in  $\mu_{eff}$  of approximately 40.4%. Based on investigations of the temperature-dependent TFT characteristics including intrinsic carrier mobility ( $\mu_0$ ) extracted by the *Y*-function method, we could conclude that the dominant transport mechanism is thermionic emission in our Al<sub>2</sub>O<sub>3</sub>-passivated MoS<sub>2</sub> TFTs with a considerable Schottky barrier between the S/D electrodes and active channel. In addition, the proposed approach at relatively high (i.e., realistic device working) temperatures can be employed to realize stable and reproducible electronic devices for robust and practical applications.

**Supplementary Materials:** The following are available online at www.mdpi.com/2076-3417/8/3/424/s1, Figure S1: (a) Cross-sectional TEM image of a MoS<sub>2</sub> encapsulated by Al<sub>2</sub>O<sub>3</sub>. Inset: FFT patterns of the MoS<sub>2</sub> obtained in the area marked with the white dashed line. (b) HAADF image of a MoS<sub>2</sub> TFT with Al<sub>2</sub>O<sub>3</sub> passivation; (c) atomic percentage of the atoms contained in the area marked with red and green rectangles in (b). Figure S2: Raman and (b) XPS spectra of (i) pristine and (ii) UV-ozone treated MoS<sub>2</sub>. Figure S3: Transfer characteristics of 14 MoS<sub>2</sub> TFTs before (black line) and after (red line) Al<sub>2</sub>O<sub>3</sub> passivation., Figure S4: Transfer characteristics of six MoS<sub>2</sub> TFTs without Al<sub>2</sub>O<sub>3</sub> passivation before (i.e., pristine) and after UV-ozone treatment.

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**Author Contributions:** Y.K.H. and S.K. conceived and designed the experiments. S.H.J. and H.P. performed the experiments. Y.K.H., S.K. and N.L. analyzed the results. All the authors wrote and reviewed the manuscript. S.H.J. and N.L. contributed to this work equally.

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