

Article

Power and Signal-to-Noise Ratio Optimization in Mesh-Based Hybrid Optical Network-on-Chip Using Semiconductor Optical Amplifiers

Jun Yeong Jang ¹ , Min Su Kim ¹ , Chang-Lin Li ² and Tae Hee Han 3,[*](https://orcid.org/0000-0001-8508-7536)

- ¹ Department of Semiconductor and Display Engineering, Sungkyunkwan University, Suwon 16419, Gyeonggi-Do, Korea; sodehd11@skku.edu (J.Y.J.); runemory@skku.edu (M.S.K.)
- ² Eyenix Co., Suwon 16690, Gyeonggi-Do, Korea; clli@eyenix.com
- ³ Department of Electrical and Computer Engineering, Sungkyunkwan University, Suwon 16419, Gyeonggi-Do, Korea
- ***** Correspondence: than@skku.edu; Tel.: +82-31-299-4587

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Abstract: To address the performance bottleneck in metal-based interconnects, hybrid optical network-on-chip (HONoC) has emerged as a new alternative. However, as the size of the HONoC grows, insertion loss and crosstalk noise increase, leading to excessive laser source output power and performance degradation. Therefore, we propose a low-power scalable HONoC architecture by incorporating semiconductor optical amplifiers (SOAs). An SOA placement algorithm is developed considering insertion loss and crosstalk noise. Furthermore, we establish a worst-case crosstalk noise model of SOA-enabled HONoC and induce optimized SOA gains with respect to power consumption and performance, respectively. Extensive simulations for worst-case signal-to-noise ratio (SNR) and power consumption are conducted under various traffic patterns and different network sizes. Simulation results show that the proposed SOA-enabled HONoC architecture and the associated algorithm help sustain the performance as network size increases without additional laser source power.

Keywords: hybrid optical network-on-chip (HONoC); insertion loss; crosstalk noise; signal-to-noise ratio (SNR); semiconductor optical amplifier (SOA)

1. Introduction

As multicore systems-on-chips (SoCs) have become mainstream as chips suffer from diminishing returns of miniaturization and the power wall, a new high-performance on-chip network architecture is needed to overcome the physical limitations of metal interconnects [\[1](#page-20-0)[,2\]](#page-20-1). A hybrid optical network-on-chip (HONoC) in which the electrical and optical layers are combined, using silicon photonics technology, is emerging as a new alternative for replacing metallic interconnects [\[3\]](#page-20-2). The HONoC uses the optical layer to transmit massive data and uses the electrical layer to send control packets or small-size data. As the number of cores in the system increases, the network-size-scalable architecture is highly desirable. The widely-known mesh topology is chosen due to its simplicity and regularity in terms of layout, after considering the advantages of the topology revealed by existing studies on mesh-based HONoCs [\[4](#page-21-0)[–6\]](#page-21-1).

For reliable communication, the optical signal power arriving at the receiver should be higher than the sensitivity of the photodetector. Because of the significant insertion loss in large-scale HONoCs due to the accumulated microring resonator (MR) drops and waveguide crossings, the laser source output power should be increased. As the laser source is the most power-consuming device among the optical devices, the insertion loss acts as a dominant factor for the overall power consumption of the HONoC. *Appl. Sci.* **2018**, *8*, x FOR PEER REVIEW 2 of 23

Lan et al. [\[7\]](#page-21-2) suggested the dynamic laser power control to reduce the power consumption of the ONoC. However, adaptive laser power management demands complicated control and exhibits limited power efficiency. Moreover, even if the power efficiency of the chip is increased by reducing the laser source power, reliable communication is not guaranteed under high crosstalk noise environments with heavy data traffic cases [\[8\]](#page-21-3). $\frac{1}{\sqrt{10}}$

near_{cy} and name cases _{top}.
We propose a low-power scalable HONoC architecture by deploying semiconductor optical amplifiers (SOAs) and the closely related SOA placement algorithm considering the insertion loss and crosstalk noise change caused by SOA deployment. An analytic worst-case crosstalk noise model is developed to find the optimized SOA gain that minimizes crosstalk noise and to investigate the effect of SOAs on worst-case signal-to-noise ratio (SNR) in HONoC. Considering the relationship between SOA placement and worst-case insertion loss, we calculate the minimum SOA gain for laser source power savings. the propose a low power sequence include alemeeting by deproying semiconductor op $\mathbf r$ sayings signal-to-noise ratio (SNR) in HONOC. Considering the relationship to $\mathbf r$ in HONOC.

The remainder of this paper is organized as follows. Section [2](#page-1-0) briefly provides related research and background information regarding insertion loss and crosstalk noise. In Section [3,](#page-2-0) we introduce the optical router and the SOA model and present a constraint optimization problem for implementing an SOA-enabled HONoC. In Section [4,](#page-5-0) to solve the constraint optimization problem, we propose an SOA placement algorithm and analyse the worst-case crosstalk noise. Based on the crosstalk noise model, the correlation between the SOA and the SNR is analysed. We also calculate the minimum SOA gain to apply laser source power independent of the longest path. In Section 5, we evaluate and compare the worst-case SNR and power consumption of SOA-enabled HONoC. We draw the conclusion in Section [6.](#page-20-3) The remainder of the remainder of the remainder is organized as follows. Section 2, we have

2. Related Work

2.1. Insertion Loss

The worst-case insertion loss is the dominant factor in determining the laser source output power to ensure reliable communication in the HONoC. The two major causes of insertion loss due to MR drop (Figure [1a](#page-1-1)) and waveguide crossings (Figure [1b](#page-1-1)) were reported to be 0.5–1.5 dB and 0.04–0.12 dB, respectively [\[9](#page-21-4)[–13\]](#page-21-5).

Figure 1. Two major insertion losses in optical network-on-chip (ONoC): (a) Microring (MR) drop loss; and (**b**) waveguide crossing loss.

The received signal power should be higher than the sensitivity of the photodetector. Therefore, The received signal power should be higher than the sensitivity of the photodetector. Therefore, the relationship between the laser source power (P_{laser}) , worst-case insertion loss (P_{worst_IL}) and sensitivity of the photodetector (*P*_{sensitivity}) can be expressed as

$$
P_{laser} \ge P_{worst_{IL}} + P_{sensitivity'} \tag{1}
$$

where P_{sensitivity has been reported as approximately −20 dBm [\[14\]](#page-21-6).}

Other types of optical signal power losses considered at the system-level analysis are as follows:

- (1) The waveguide propagation loss is -0.274 dB/cm [\[15\]](#page-21-7).
- (2) The MR through loss is smaller than the MR drop loss by two orders of magnitude [\[16\]](#page-21-8).

Given that these factors are minor in comparison to major insertion losses, most on-chip network architecture studies focus on reducing the insertion losses caused by MR drops and waveguide crossings. G_{GOM} that the minor in comparison to major insertion losses m

2.2. Crosstalk Noise $a_n = \frac{a_n}{\sqrt{a_n}}$

Crosstalk noise is an unavoidable characteristic of photonic devices. Sanchis et al. proposed a method for selecting the optimal crossing angle of the waveguide to reduce the crosstalk [\[17\]](#page-21-9). Chen et al. proposed multimode-interference (MMI)-based waveguide crossing instead of plain waveguide crossing for reducing signal power loss [18]. They designed a compact structure to minimize the crosstalk at the device level.

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Despite these efforts, leakage signals that act as crosstalk cannot be ignored due to their accumulation. Especially, crosstalk noise should be considered as network size increases. In a network-level, Xie et al. analysed crosstalk noise and worst-case SNR through a formal approach in a mesh-based $\rm ONoC$ [\[19\]](#page-21-11). According to their research, crosstalk noise causes severe performance degradation and limits the scalability of the HONoC architectures. accumulation. Especially, considered as newspaper as newspaper as network size in a network size in a newspaper

Studies on crosstalk noise have been conducted with respect to router architectures, network topologies and traffic patterns [\[20–](#page-21-12)[22\]](#page-21-13). On the other hand, there are few studies regarding crosstalk noise when integrating the SOA. The SOA can significantly reduce insertion loss; however, it can also increase crosstalk noise. In this regard, we analyse the correlation between the SOA, insertion loss and crosstalk noise and then propose a methodology to efficiently compensate insertion loss and minimize crosstalk noise. \mathbf{h} minimize crosstalk noise.

3. SOA-Enabled HONoC Architecture 3. SOA-enabled HONoC Architecture

Our proposed architecture, as shown in Figure [2,](#page-2-1) is a mesh-based HONoC with integrated SOAs Our proposed architecture, as shown in Figure 2, is a mesh-based HONoC with integrated SOAs and consists of two layers. The electrical layer controls the optical layer while transmitting control and consists of two layers. The electrical layer controls the optical layer while transmitting control packets and small-size data through packet switching. Before the optical data transaction between the packets and small-size data through packet switching. Before the optical data transaction between cores starts, the routing path should be setup in advance through the electrical layer. During the path setup process, the ON/OFF state of SOAs and MRs located in the path are determined and the SOA gain is controlled by adjusting the bias current applied to the SOA. SOA gain is controlled by adjusting the bias current applied to the SOA.

Figure 2. Semiconductor optical amplifier (SOA)-enabled HONoC architecture.

In the optical layer, data transaction is performed using circuit switching due to the absence of In the optical layer, data transaction is performed using circuit switching due to the absence of optical storage. The SOAs can amplify the optical signal power attenuated by photonic devices in the optical storage. The SOAs can amplify the optical signal power attenuated by photonic devices in the routing path. The SOAs outside of the routing path are in the OFF state and absorb the optical signal routing path. The SOAs outside of the routing path are in the OFF state and absorb the optical signal power. In this study, the expected effects of integrating the SOA with the mesh-based HONoC are as power. In this study, the expected effects of integrating the SOA with the mesh-based HONoC are as follows: follows:

- The SOA compensates for the insertion loss by amplifying the optical signal and reduces the The SOA compensates for the insertion loss by amplifying the optical signal and reduces the burden of the laser source to reduce the total power consumption of the HONoC. burden of the laser source to reduce the total power consumption of the HONoC.
- An appropriate SOA gain can prevent the SNR degradation problem of the longest path due to the network size growth of the mesh-based HONoC. to the network size growth of the mesh-based HONoC.

3.1. Optical Router Model 3.1. Optical Router Model

The 5×5 general optical router for mesh-based HONoC, as shown in Figure [3,](#page-3-0) consists of the following five ports: Injection/Ejection, North, East, South and West. The ONoC parameters in this following five ports: Injection/Ejection, North, East, South and West. The ONoC parameters in this analysis are based on the study conducted by Xie et al. [\[19\]](#page-21-11), which presented the formal analytic analysis are based on the study conducted by Xie et al. [19], which presented the formal analytic models of insertion loss and crosstalk noise for mesh-based HONoCs. models of insertion loss and crosstalk noise for mesh-based HONoCs.

Figure 3. General optical router model.

0 to 4: 0 denotes the Injection/Ejection port and 1–4 denote the North, East, South and West ports, respectively. P_{in}^i denotes the optical power coming into the *i*th port and $L_{i,j}(x, y)$ is the insertion loss respectively. P_{in}^i denotes the optical power coming into the *i*th port and $L_{i,j}(x, y)$ is the insertion loss from the *i*th port to the *j*th port in the optical router located at (x, y) in the mesh-based HONoC. $P_{i,j}(x,y)$, defined in (2), is the optical power when going from the *i*th port to the *j*th port in the optical $\text{router } R(x, y).$ Optical signals can traverse from the *i*th port to the *j*th port and the value of *i* and *j* varies from

$$
P_{i,j}(x,y) = P_{in}L_{i,j}(x,y) \, i, \, j \in \{0, \, \ldots, \, 4\}, \, x \in \{1, \, \ldots, \, m\}, \, y \in \{1, \, \ldots, \, n\} \tag{2}
$$

 $N_{i,j}(x,y)$, calculated in (3), denotes the crosstalk noise introduced into the optical router located the optical signal travels from the *i*th port to the *j*th port. at (*x*, *y*) in the mesh-based HONoC and *Ki*,*j*,*^k* is the crosstalk noise introduced into the *k*th port when

at (,) in the mesh-based HONOC and ,, is the crosstalk noise intervals noise into the the the the the th

$$
N_{i,j}(x,y) = P_{in}^{0}(x,y)K_{i,j,0} + P_{in}^{1}(x,y)K_{i,j,1} + P_{in}^{2}(x,y)K_{i,j,2} + P_{in}^{3}(x,y)K_{i,j,3} + P_{in}^{4}(x,y)K_{i,j,4}
$$
 (3)

 $\frac{1}{2}$ is the ratio of the signal power to the noise power and can be written as The SNR is the ratio of the signal power to the noise power and can be written as

$$
SNR = 10 \log \left(\frac{P_S}{P_N} \right),\tag{4}
$$

al and P_N is the $\overline{\ }$ where P_S is the power of the optical signal and P_N is the power of noise.

3.2. Semiconductor Optical Amplifier

The SOA is mainly used for amplifying the input optical signals, switching and frequency conversion [\[23\]](#page-22-0). The SOA can be integrated into a CMOS-compatible chip and thus can be implemented in the HONoC by using silicon photonics technology. The operating principle of the SOA is that electrons and holes are injected into the n-type region and the p-type region respectively, causing population inversion in the active region. Population inversion leads to stimulated emission and therefore, a photon injected into the SOA triggers another photon with the same phase, wavelength and direction. Optical gain is obtained through this process.

In the active region, the gain per unit length of the SOA is called material gain (*g*) and is expressed in (5a) as a function of wavelength (*λ*) and current (*I*). As *L* increases, the SOA gain (*G*) increases accordingly. Here, *g* and *G* are in cm−¹ and dB units, respectively.

$$
g(\lambda, I) = \left[\Gamma a_1 n_0 \left\{ \frac{I}{I_0} - 1 \right\} - \alpha\right] \left[1 - \frac{2(\lambda - 1570)^2}{\Delta \lambda^2}\right] \tag{5a}
$$

$$
G(\lambda, I) = 10 \log_{10} \left(e^{L \ast g(\lambda, I)} \right)
$$
 (5b)

Γ, a_1 , n_0 , *L*, I_0 , *α* and $\Delta\lambda$ depend on the material and the structural characteristics of the SOA; the definitions and values of each parameter are shown in Table [1](#page-4-0) [\[24\]](#page-22-1). As in Equation (5), the SOA gain is proportional to the current. The operating voltage of the SOA and the wavelength are assumed to be 1.5 V and 1550 nm, respectively. In addition, it takes 20–50 ps for the SOA to reach the target gain. The SOA model in this study is simplified for the purpose of system-level analysis; therefore, the physical effect of the amplified spontaneous emission (ASE) introduced by the SOAs is not considered. Although ASE cannot be ignored in physical implementations with contemporary silicon photonics technology, this study focuses on the emerging architecture with a matured manufacturing process that enables feasible on-chip SOAs, assuming a negligible ASE compared to the cumulated crosstalk noise. Additionally, it is also assumed that the placement of SOAs can be over-layered on waveguides in the physical layout; therefore, SOAs do not affect the spaces between optical routers.

Parameter	Definition	Value
Г	Light confinement factor	0.4
a_1	Constant	6.7×10^{-16} cm ²
n ₀	Transparency carrier concentration	1.2×10^{18} cm ⁻³
	Length of the SOA active region	$10 \mu m$
I_0	Threshold input current	$5 \mu A$
α	Loss in the SOA active region	10 cm^{-1}
Λλ	SOA gain linewidth	95 nm

Table 1. Definitions and values of SOA parameters.

3.3. Constrained Optimization Problem in SOA-Enabled HONoC

In the HONoC, laser source power mainly depends on the total loss experienced by the optical signal in the longest path. The laser source power level is determined to ensure that the signal power at the receiver is greater than the sensitivity of the photodetector. Therefore, we reduce the burden of the laser source power by using the SOA, which is easy to integrate and of relatively low processing cost. Appropriately placing SOAs across the signal path is an effective way to save the laser source output power in large-scale HONoCs if the additional power consumption of SOAs is reasonably smaller than the reduced amount of the laser source power.

As the total power consumption of the SOA-enabled HONoC depends on the location, spacing and number of SOAs, an efficient SOA placement is needed to achieve the goal. Furthermore, the SOA placement also affects the worst-case SNR because SOAs can amplify the crosstalk noise power as well as the desired optical signal power. Therefore, the constrained optimization problem for implementing the SOA-enabled HONoC in an $m \times n$ mesh is defined as follows:

Minimize:
$$
\sum_{total} P_{laser} + \sum_{total} P_{SOA}
$$

Subject to: $P_{sensitivity} \le min \{PL_{(x_0,y_0)(x_1,y_1)}^{SOA}\}$ (6a)

$$
\min \left\{ SNR_{(x_0, y_0)(x_1, y_1)} \right\} \le \min \left\{ SNR_{(x_0, y_0)(x_1, y_1)}^{SOA} \right\} x_0, x_1 \in \{1, ..., m\}, y_0, y_1 \in \{1, ..., n\}
$$
\n(6b)

Here, *Plaser* is the laser source power consumption of the SOA-enabled HONoC, *PSOA* is the total power consumption of the SOAs, $P_{sensitivity}$ is the sensitivity of the photodetector, $PL_{(x_0,y_0)(x_1,y_1)}^{SOA}$ is the power loss of the optical signal traveling from the router (x_0, y_0) to the router (x_1, y_1) in the SOA-enabled HONoC, $SNR_{(x_0,y_0)(x_1,y_1)}$ is the SNR of the path from the router (x_0,y_0) to the router (x_1,y_1) in the conventional mesh-based HONoC and $SNR^{SOA}_{(x_0,y_0)(x_1,y_1)}$ is the SNR of the path from the router (x_0,y_0) to the router (x_1, y_1) in the mesh-based HONoC with SOAs.

4. Design Methodology for SOA-Enabled HONoC

As mentioned in Section [3.3,](#page-4-1) the insertion loss problem exacerbates as the network size scales up. On the other hand, the SOA placed to compensate for the insertion loss might adversely affect the SNR caused by unintended crosstalk noise amplification. Therefore, SOA placement considering the routing algorithm should be performed in terms of SNR. In this regard, we devise a design methodology of the SOA-enabled HONoC when XY routing is used in the mesh topology. XY routing has been reported to realize the best performance in terms of bandwidth, latency, load balancing and insertion loss in mesh-based HONoC [\[5\]](#page-21-14).

The proposed methodology for the SOA-enabled HONoC comprises three steps, as shown in Figure [4.](#page-6-0) In step 1, we propose an SOA placement algorithm considering insertion loss and SNR. In step 2, the worst-case crosstalk noise due to the SOA placement and the SOA gain to be realized to suppress the crosstalk noise amplification are presented. Based on the SNR analysis with segmented regions, we compare the SNR of the longest path according to the SOA gain and analyse the path in which SNR degradation occurs even if the SOA gain is controlled. In step 3, the algorithm for finding the minimum required SOA gain is developed to allocate the laser source output power independent of the network size.

Figure 4. Overall flow of the proposed design methodology. **Figure 4.** Overall flow of the proposed design methodology.

4.1. SOA Placement Algorithm for Laser Source Power Saving and SNR Enhancement 4.1. SOA Placement Algorithm for Laser Source Power Saving and SNR Enhancement

The SOA gain modelling in Equation (5) shows that using a single SOA is superior to using The SOA gain modelling in Equation (5) shows that using a single SOA is superior to using multiple SOAs in that it uses less bias current to obtain the same gain, as described in Equation (7). multiple SOAs in that it uses less bias current to obtain the same gain, as described in Equation (7).

$$
G(I_{total}) > \sum_{i=1}^{n} G(I_i), I_{total} = \sum_{i=1}^{n} I_i
$$
 (7)

dition to the goin efficiency of the SO_A , the email number of SO_A with high In addition to the gain efficiency of the SOA, the small number of SOAs with high gain is advantageous in terms of the area in which the SOA devices and control units are placed. However, this strategy should be validated. As the worst-case insertion loss determines the output power of laser sources and even one SOA has impact on various routing paths, an effective SOA placement rule \mathbf{h}_0 reduce the least cause paying using the expression number of \mathcal{E}_0 Λ_0 \mathcal{E}_0 is needed to reduce the laser source power using the appropriate number of SOAs. Considering the
. insertion loss due to the SOA placement in the routing path, the power consumption of the HONoC $\,$ can be effectively reduced by equalizing the maximum number of hops that do not pass through the SOA in all routing paths. We denote this maximum hop count without SOAs as *h*, which is determined μ indicating the SOA exering on the V axis and V axis respectively. by t_x and t_y , indicating the SOA spacing on the *X*-axis and *Y*-axis, respectively.

The accumulated crosstalk noise power at the destination depends on not only the number of SOAs but also the location of the SOAs. For example, as shown in Figure [5,](#page-7-0) the crosstalk power increases when the SOA is placed nearer to the destination rather than the source. In other words, to minimize the crosstalk power amplified through the SOAs in the optical signal path, SOA should be placed close to the source. In the mesh-based HONoC with XY routing, the optical signal is transmitted along the *Y*-axis after the *X*-axis. Hence, placing more SOAs on the *X*-axis is preferred to *Y*-axis in terms of SNR. Accordingly, we propose the SOA placement algorithm as follows. In this study, it is assumed that all SOA gains are the same for the convenience of design and analysis of the network.

Figure 5. Example of cumulated crosstalk noise according to the SOA placement. **Figure 5.** Example of cumulated crosstalk noise according to the SOA placement.

In Algorithm 1, the optimal SOA spacing in the *X*-axis (t_x) and that in the *Y*-axis (t_y) are selected according to the maximum number of hops among the possible routing paths without SOAs (*h*).
The contract to the maximum number of hops among the possible routing paths without SOAs (*h*). Therefore, the total number of SOAs placed in the network depends on the possible combinations of Therefore, the total number of SOAs placed in the network depends on the possible combinations of *t^x* and *t_y* with a consistent *h*. To minimize the additional power consumption of the SOAs, use of the sense when ϵ COAs $t_{\rm{max}}$ smallest number of SOAs should be pursued. If there are several cases with the same number of 50 A the narrowest *X*-axis spacing is chosen to improve the SNR (lines 1–10). After the spacing of the SOAs is determined, SOAs are placed on the link of the corresponding router (lines 11–23). smallest number of SOAs should be pursued. If there are several cases with the same number of SOAs,

24: **End for** *4.2. Worst-Case Crosstalk Noise and SNR Analysis*

23: =+௬

4.2. Worst-Case Crosstalk Noise and SNR Analysis For the analysis of the worst-case SNR in the proposed architecture, we use several definitions as follows:

 \bullet n_r : the total number of routers in the optical signal path

- *n^s* : the total number of SOAs in the optical signal path
- *tⁱ* : the number of the routers between the *i*th SOA and the (*i* − 1)th SOA in the optical signal path $(t_0 = 0, t_{n_s+1} = (\text{\# of routers between the last SOA and the destination}))$
- *P_{N_i*}: the crosstalk noise introduced into the *i*th router in the optical signal path
- L: router loss, *G*: SOA gain, *K*: crosstalk noise coefficient

To simplify the worst-case SNR equations, we make the following assumptions

- The insertion loss and the crosstalk noise coefficient are the same regardless of ports. $(L_{i,j} =$ $L, K_{i,j,n} = K$ $\mathbf{r} = \mathbf{r} \cdot \mathbf{r}$, the total signal path of routers in the optical signal path of \mathbf{r} ertion loss and the crosstalk noise coefficient are the sam $= K$)
- The mesh size is $m \times m$. ize is $m \times m$.

As the crosstalk noise introduced into each router is also amplified by the corresponding SOAs in the optical signal path, these effects are reflected in the SNR as in (8) . \overline{c} router loss, \overline{c}

$$
SNR = 10 \log \left(\frac{P_S G^{n_s} L^{n_r}}{\sum_{i=0}^{n_s} G^{n_s - i} \sum_{j=T_i+1}^{T_{i+1}} \left(P_{N_j} L^{n_r - j} \right)} \right) \text{ where } T_i = \sum_{k=0}^{i} t_k
$$
 (8)

As shown in Equation (8), it is inevitable that the crosstalk noise introduced into the optical signal passes through the SOAs located in the signal path. Therefore, to maximize the SNR, the worst crosstalk candidate must be suppressed before it leaks into the desired optical signal path.

The dominant crosstalk power is mainly caused by the crosstalk noise coming from the nearest ric abilitant crossiant power to manny caused by the crossiant node conting from the nearest
router in conventional HONoCs. However, in the SOA-integrated HONoC, the worst crosstalk candidates vary depending on the gain of the associated SOAs. If the SOA gain is larger than the insertion loss between the SOAs, the crosstalk noise introduced from a farther router has a greater impact on the SNR than that from a nearer router. For example, when there are the three crosstalk noise sources P_1 , P_2 and P_3 as shown in Figure [6,](#page-8-0) each crosstalk noise power can be expressed as in (9). If the total SOA gain is smaller than the total power loss ($\prod G_i L_i < 1$), the magnitude of each crosstalk noise power is ordered as $N_1 < N_2 < N_3$; otherwise, $N_1 > N_2 > N_3$. However, in the SOA-integrated HONoC, the
gain of the associated SOAs. If the SOA gain r_1 is conventional H_{ON} $\frac{1}{2}$ HO_N $\frac{1}{2}$ HO_N $\frac{1}{2}$ in the worst crosstalk crosstal

$$
N_1 = P_1 G_1 L_1 G_2 L_2 G_3 L_3 K \tag{9a}
$$

$$
N_2 = P_2 G_2 L_2 G_3 L_3 K \tag{9b}
$$

$$
N_3 = P_3 G_3 L_3 K \tag{9c}
$$

Figure 6. Example of crosstalk noise paths whose magnitude order varies depending on SOA gains.

Therefore, the worst crosstalk noise differs depending on the SOA gain even with the same noise to other adjacent signal paths, we introduce the concept of average insertion loss (*L_{avg}*) per SOA placement of SOAs. To quantify the effect of SOAs when the current signal path acts as a crosstalk as (10). Here, definitions of terms are as follows:

- $n_{r,c}$: the total number of routers in the crosstalk path
- \bullet $n_{s,c}$: the total number of SOAs in the crosstalk path

• *ti*,*^c* : the number of the routers between the *i*th SOA and the (*i* − 1)th SOA in the crosstalk path

$$
L_{avg} = \begin{cases} \n\frac{n_{s,c}}{n_{s,c}} \sqrt{\sum_{i \in \{2,\cdots,n_{r,c}\}} L_i} \sum_{i=1}^{n_{s,c}} t_{i,c} \neq n_{r,c} \\
\frac{n_{s,c}}{n_{s,c}} \sqrt{\sum_{i \in \{2,\cdots,n_{r,c}\}} L_i} \sum_{i=1}^{n_{s,c}} t_{i,c} = n_{r,c}\n\end{cases} \tag{10}
$$

To analyse the worst crosstalk candidates, we denote the router to be analysed as *RDUT* and the SOA farthest from the *RDUT* in the crosstalk-inducing path as *SOADUT*. In Equation (10), *Lavg* is determined by the power loss of an optical signal traveling from *SOADUT* to *RDUT* and the number of SOAs in that path. If an SOA is placed in the link directly connected to *RDUT*, this SOA is not included in the calculation for *Lavg* because incoming crosstalk noise passes through that SOA inescapably. With the SOA gain and the average insertion loss, the dominant crosstalk noise introduced into the router can be identified. The worst crosstalk noise due to the SOA gain can be expressed as Equation (11).

(1) $G > |L_{avg}|$, $P_N = P_{Laser}KL^{n_{r,c}}G^{n_{s,c}}$ (11a)

$$
(2) \tG \leq |L_{avg}|,
$$

$$
P_N = \begin{cases} P_{Laser}KL, & \sum_{i=1}^{n_{S,c}} t_{i,c} \neq n_{r,c} \\ P_{Laser}KLG, & \sum_{i=1}^{n_{S,c}} t_{i,c} = n_{r,c} \end{cases} \tag{11b}
$$

If the SOA gain is greater than $|L_{avg}|$, the crosstalk power originating from the farthest SOA is the largest. This worst crosstalk noise power increases as the SOA gain and mesh size increases and the SOA spacing decreases. Conversely, if the SOA gain is less than or equal to $|L_{avg}|$, the crosstalk noise from the nearest router is dominant. In this case, the crosstalk can be amplified or not, depending on the presence of the SOA in the corresponding waveguide.

Therefore, SOA gain control is required to minimize the dominant crosstalk before it leaks into the optical signal path. The average insertion loss is equal to the maximum gain of the SOA required to minimize the worst crosstalk as given in Equation (12).

$$
G_{max} = |L_{avg}| \tag{12}
$$

On the other hand, as the crosstalk noise introduced into the optical signal is amplified by the SOAs on the signal path, we analyse the specific cases in which SNRs are strongly affected by the SOAs.

4.2.1. SNR Analysis with Multiple SOAs Using Segmented Regions in Mesh-Based HONoCs

To facilitate the analysis of the SNR change resulting from SOAs and crosstalk, the entire on-chip optical network is partitioned into several sub regions. The criteria for region partitioning are that no sub region should contain any SOA inside and should be surrounded by the SOA-placed links. The sub region is denoted as *REG*(*i*, *j*) where *i* and *j* indicate the row and column position, respectively, as shown in Figure [7.](#page-10-0)

 θ substituting θ substituting to the SO₃ placement algorithm. **Figure 7.** Non-SOA sub regions (*REG*) according to the SOA placement algorithm.

optical signal to be routed will be subject to the same number of SOAs as the optical signal, whereas the other crosstalk noises introduced at nearby *REGs* to the destination node will be amplified to a lesser degree by the SOAs than by the optical signal under analysis. More specifically, we formulated the SNR of the optical signal path that travels from $REG(1,1)$ to $REG(n_x, n_y)$ in Equation (13a) for an SOA-enabled HONoC. For comparison purposes, the SNR for the non-SOA case is derived in Equation (13b). Equation (13b). It is apparent that the crosstalk noise introduced at the same *REG* with the source node of the

$$
SNR_1 = 10 \log \left(\frac{P_S L_{REG(1,1)}(n_x, n_y)}{\sum_{\forall i} \frac{N_{REG(1,i)} L_{REG(1,i+1)}(n_x, n_y)}{G^{(i-1)}} + \sum_{\forall j} \frac{N_{REG(j,n_x)} L_{REG(j+1,n_x)}(n_x, n_y)}{G^{(n_x - 1) + (j-1)}}} \right)
$$
(13a)

$$
SNR_2 = 10 \log \left(\frac{P_S L_{REG(1,1)(n_x,n_y)}}{\sum_{\forall i} N_{REG(1,i)} L_{REG(1,i+1)(n_x,n_y)} + \sum_{\forall j} N_{REG(j,n_x)} L_{REG(j+1,n_x)(n_x,n_y)}} \right) \tag{13b}
$$

 $i \in \{1, ..., n_x - 1\}, \quad j \in \{1, ..., n_y\}$

Here, the total crosstalk noise introduced into the optical signal when it passes through $REG(i, j)$ is represented by $N_{REG(i,j)}$ and the signal power loss that occurs when migrating from $REG(i_0, j_0)$
to $REG(i, i_0)$ is denoted by L to $REG(i_1, j_1)$ is denoted by $L_{REG(i_0, j_0)(i_1, j_1)}$. Additionally, n_x and n_y are the number of subregions divided by the SOA placed links in the X and X axes, respectively. divided by the SOA-placed links in the *X* and *Y* axes, respectively.

In the non-SOA enabled HONoC, the SNR decreases as the routing path length increases as a result of increased signal attenuation and more involved crosstalk noises. In the proposed architecture, the effect of additional crosstalk noise according to the path length increase decreases by the appropriate placement of SOAs to boost the SNR. As shown in Equation (13a), the noise terms decrease by G^0 , G^1 , ... and $G^{n_x+n_y-2}$, respectively. The SNR enhancement becomes more apparent as the number of b_{EC} increases. Therefore at the reuter level, we engly edge and that necess through the greatest REGs increases. Therefore, at the router level, we analysed a path that passes through the greatest number of *REC*_s and a path inside an *REC* $\frac{1}{2}$ number of *REG*s and a path inside an *REG*.

4.2.2. SNR Analysis of the Longest Path

As the worst-case SNR of the longest path can vary depending on the dominant crosstalk affected by the SOA gain, the SNR values of three representative cases are analysed as follows.

As the worst-case $\mathcal{A}_\mathcal{A}$ of the longest path can vary depending on the dominant control to do

(1) *Case* 1: $h = 0$, $G \le |L_{avg}|$

$$
SNR_1 \approx \frac{L/K}{\left[\left((GL)^{-1} + 2GL \right) \left(\sum_{i=0}^{2m-3} \frac{1}{(GL)^i} \right) - (GL)^{-(m-2)} + (GL)^{-(m-4)} \right]}
$$
(14a)

(2) Case 2:
$$
h = 0, G > |L_{avg}|
$$

$$
SNR_1 \approx \frac{L/K}{\left[\left((GL)^{-1} + (GL)^{m-1} \right) \left(\sum_{i=0}^{2m-3} \frac{1}{(GL)^i} \right) + \left((GL)^{m-1} + 1 \right) \left(\sum_{i=0}^{m-2} \frac{1}{(GL)^{2i}} \right) \right]}
$$
(14b)

 (3) *Case* 3: $h = 2m - 1$

$$
SNR_3 \approx \frac{L/K}{\left[(L^{-1} + 2L) \left(\sum_{i=0}^{2m-3} \frac{1}{L^i} \right) - L^{-(m-2)} + L^{-(m-4)} \right]}
$$
(14c)

The case when $h = 0$ indicates that SOAs are placed on all waveguides, whereas there is no SOA when $h = 2m - 1$. When $h = 0$, a REG consists of only one router. As shown in [F](#page-11-0)igure 8a of Case 1, the incoming crosstalk from the nearest router is dominant. In contrast, the crosstalk introduced from the farthest router causes the worst-case SNR in Case 2 a[s s](#page-11-0)hown in Figure 8b.

Figure 8. Worst-case SNR of the longest path in accordance with the SOA gain: (**a**) $h = 0$, $G \le |L_{avg}|$;
and (**b**) $h = 0$, $G \le |I|$ and (**b**) $h = 0, G > |L_{avg}|$.

same traffic pattern for the worst-case SNR in the network. If the *GL* of *SNR*₁ is replaced by *L*₁ and if the L of SNR_3 is L_3 , then L_1 is always less than L_3 . Therefore, SNR_1 is always greater than SNR_3 because the insertion loss of Case 1 is smaller than that of Case 3. As shown in Equation (14a) and (14c), *SNR*¹ and *SNR*³ have an identical form because of the

In contrast, some crosstalk components have the chance to be amplified more times than the optical signal by SOAs as described in Case 2. In Equation (14b), (*GL*)^{*m*−1} represents the worst $\frac{1}{2}$ crosstalk noise due to the SOAs and $(GL)^i$ indicates the ratio of the number of SOAs that the optical crosstalk noise due to the SOAs and () crosstalk noise due to the SOAs and (*GL*) *i* indicates the ratio of the number of SOAs that the optical

signal passes through to the number of the SOAs that the introduced crosstalk noise passes through. Therefore, if $(GL)^{m-1}$ is larger than $(GL)^i$, this implies that the number of SOAs that the crosstalk noise passes through is greater than the number of SOAs that the optical signal passes through. Consequently, SNR_2 severely degrades and eventually can be smaller than SNR_3 as the SOA gain grows.

4.2.3. SNR Analysis inside Regions without SOAs

As previously described, the SNR can be improved when the optical signal passes through multiple SOAs with suitable SOA gain control. However, if the optical signal path forms within a certain *REG*, the SNR can degrade compared to a non-SOA HONoC, even if the condition $G < |L_{avg}|$ is satisfied. ΔS previously described, the SNR can be improved when the optical signal passes through ΔS

When there is a path that travels along the boundary of *REG*, as shown in Figure 9, both the crosstalk noise occurring inside *REG* and the optical signal will not pass through any SOA, whereas the crosstalk noise originating from the outside of *REG* is amplified by SOAs.

Figure 9. Worst-case signal to noise ratio (SNR) of the non-SOA region surrounded by SOA-placed **Figure 9.** Worst-case signal to noise ratio (SNR) of the non-SOA region surrounded by SOA-placed links.

The worst-case SNR of the SOA-integrated HONoC under the corresponding traffic condition and that of the HONoC without SOAs under the same condition can be expressed as follows:

(1) Case 1:
$$
0 < h < 2m - 1
$$
, $G < |L_{avg}|$

$$
SNR_1 \approx \frac{L^{t_x+t_y-1}/K}{\left[\begin{array}{c} GL\left(2\sum_{i=1}^{t_x} L^{t_x+t_y-1-i} + \sum_{i=2}^{t_y} L^{t_y-i}\right) + 2\left(\sum_{i=1}^{t_x-2} L^{t_x+t_y-i} + \sum_{i=1}^{t_y-2} L^{t_y-i}\right) \\ + \left(L^{t_y+1} + L^{t_y} + L\right) + \left(\sum_{i=1}^{t_x+t_y-2} L^{t_x+t_y-2-i}\right)\end{array}\right] (15a)
$$

(2) Case 2:
$$
h = 2m - 1
$$

$$
SNR_2 \approx \frac{L^{t_x+t_y-1}/K}{\left[\frac{L\left(2\sum_{i=1}^{t_x} L^{t_x+t_y-1-i} + \sum_{i=2}^{t_y} L^{t_y-i}\right) + 2\left(\sum_{i=1}^{t_x-2} L^{t_x+t_y-i} + \sum_{i=1}^{t_y-2} L^{t_y-i}\right)}{+\left(L^{t_y+1} + L^{t_y} + L\right) + \left(\sum_{i=1}^{t_x+t_y-2} L^{t_x+t_y-2-i}\right)}\right]}
$$
(15b)

Case 1 represents the SOA-integrated HONoC and Case 2 denotes the HONoC without SOAs. In
Equation (15a), the first term in the denominator is the crosstalk noise introduced from outside of *REG*
and appellified by the SOA Case 1 represents the SOA-integrated HONoC and Case 2 denotes the HONoC without SOAs. In and amplified by the SOA gain (*G*). Assuming the same optical signal power for both cases, the SNR of the SOA-integrated HONoC is less than that of the HONoC without SOAs. Therefore, if the SOAs are not placed on every link, there is a path along which the SNR decreases.

4.3. Minimum SOA Gain and Laser Source Power Allocation

In a conventional mesh-based HONoC, a laser source power is applied considering the insertion In a conventional mesh-based HONoC, a laser source power is applied considering the insertion loss of the longest path and the receiver sensitivity. However, in the proposed SOA-enabled HONoC, a loss of the longest path and the receiver sensitivity. However, in the proposed SOA-enabled HONoC, new analysis of the worst-case insertion loss is required, as the optical signal is amplified whenever it passes through the SOA. If the total SOA gain is larger than the total insertion loss of the overall signal path, the signal power at the destination is greater than that at the source. Therefore, we present the allocation of the laser source power independent of the longest path considering the SOA placement, SOA gain and mesh size.

4.3.1. Minimum SOA Gain Allocation 4.3.1. Minimum SOA Gain Allocation

The SOA gain should be sufficiently large to compensate for the insertion loss between two The SOA gain should be sufficiently large to compensate for the insertion loss between two adjacent SOAs along the signal path. Thus, the laser source power need not be increased even if the adjacent SOAs along the signal path. Thus, the laser source power need not be increased even if the network size grows. Overall, the laser source power level is determined by the insertion loss of the network size grows. Overall, the laser source power level is determined by the insertion loss of the longest signal path that does not pass through any SOAs. longest signal path that does not pass through any SOAs.

As shown in Figure 10, the top-leftmost sub region *REG*(1, 1) occupies the largest area among As shown in Figure [10,](#page-13-0) the top-leftmost sub region (1,1) occupies the largest area among the sub regions because uniform placement of SOAs is performed from the top-leftmost corner of the network according to the proposed SOA placement algorithm. Assuming that the reference laser network according to the proposed SOA placement algorithm. Assuming that the reference laser source power level is calculated by considering the longest path formed within $REG(1,1)$, we can compute the minimum SOA gain to compensate for the insertion loss without increasing the laser compute the minimum SOA gain to compensate for the insertion loss without increasing the laser source power. source power.

Figure 10. Example of determining the minimum SOA gain considering traffic paths across the topleftmost non-SOA region. top-leftmost non-SOA region.

identify the larger insertion loss between Path B and Path C to compute the minimum SOA gain. Finally, the minimum SOA gain is chosen as the larger value between the insertion loss of Path X2 and that of Path Y2. To summarize, the minimum SOA gain can be expressed as follows: \overline{C} Once the laser source power is determined by the insertion loss of Path A, the next step is to

$$
G_{min} = max(L_{X2}, L_{Y2}), \qquad (16)
$$

where L_{X2} and L_{Y2} denote the insertion loss of Paths X2 and Y2, respectively. Considering mesh size and SOA spacing, we propose the following algorithm to calculate the minimum SOA gain:

As the optical signal passes through the router in a straight manner, Algorithm 2 checks the paths $(W \to E)$ and $(E \to W)$ in the *X*-axis and $(S \to N)$ and $(N \to S)$ in the *Y*-axis, respectively, to find the maximum power loss. Then, L_{X2} and L_{Y2} are independently calculated for each axis. If $n_x \geq 2$, the maximum hop count is the SOA spacing along the *X*-axis (*tx*) (lines 2–3). Otherwise, the hop count is the number of routers in the *X*-axis (n) excluding t_x (lines 4–6). The same process is performed on the *Y*-axis (lines 7–11) and the larger insertion loss between *LX*² and *LY*² is chosen as the minimum SOA gain (line 12).

4.3.2. Minimum Laser Source Power Allocation

When the SOA effectively compensates for the insertion loss along the optical signal path, the worst-case insertion loss depends on *h*, which is the maximum number of hops along which the optical signal does not pass through any SOAs. If $h = 0$, the maximum insertion loss case occurs when the length of the optical signal path is one hop as the optical signal is amplified just once. Otherwise, the worst insertion loss is among the sub-longest paths that are not amplified by the SOA.

When the waveguide crossing loss is -0.12 dB, the insertion loss of the input/output port in the 5×5 Crux router is shown in Table [2.](#page-15-1) The crux router is the optimized optical router for the insertion loss and the SNR when using X-Y routing [\[25\]](#page-22-2). A shown Table [2,](#page-15-1) the optical router has different insertion loss values depending on the signal flow at the input/output ports. Therefore, the total power loss depends on the routing path even if the number of hops that the optical signal travels along are the same. The worst-case insertion loss according to *h* considering the input and output port is as follows.

- $h = 0, 1$ $P_{worst\ IL} = (L_{In,E} + L_{W,Ei}) \text{ or } (L_{In,N} + L_{S,Ei})$ (17a)
- $h = 2$ P_{worst} $IL = (L_{In,E} + L_{W,N} + L_{S,E}$ *i*) (17b)

$$
(3) \quad h \geq 3
$$

$$
P_{worst_IL} = (L_{In,E} + L_{W,N} + L_{S,N} \times (h-2) + L_{S,Ej})
$$
\n(17c)

Here, the loss values are in dB units. When $h = 0$, the worst-case insertion loss is equal to the case of *h* = 1, as the SOA exists in all links; however, the SOA gain should be additionally considered when calculating the minimum laser source power. Therefore, the minimum laser source power according to *h* can be expressed as follows:

$$
P_{laser_min} = \begin{cases} P_{worst_IL} + P_{sensitivity} - P_{gain}, & h = 0\\ P_{worst_IL} + P_{sensitivity}, & h \neq 0 \end{cases}
$$
(18)

where $P_{worst~IL}$ is the worst-case insertion loss in the SOA-enabled HONoC, $P_{sensitivity}$ is the minimum optical power required by the photodetector and *Pgain* is the SOA gain.

Loss	Value (dB)	Loss	Value (dB)
$L_{In.W}$	0.50	$L_{E.W}$	0.38
$L_{In,E}$	0.88	$L_{E.N}$	0.50
$L_{In,N}$	0.88	$L_{E,S}$	1.00
$L_{In,S}$	0.63	$L_{E,Ej}$	0.63
$L_{W,E}$	0.38	$L_{N,S}$	0.38
$L_{W,N}$	1.00	$L_{N,E,i}$	0.50
$L_{W,S}$	0.50	$L_{S,N}$	0.38
$L_{W,E}$	0.88	$L_{S,E}$	0.88

Table 2. Loss value in 5×5 Crux router.

5. Simulation Results

We built up a C-based simulator to evaluate the SNR and the power consumption of the SOA-enabled mesh-based HONoC in comparison with an HONoC without SOAs. The typical values of insertion loss and crosstalk coefficient for system analysis are presented in Table [3.](#page-15-2) The chip size of 16×16 HONoC is assumed as 1 cm² and in other cases, the chip sizes are assumed to be proportional to the number of cores. It is reasonable to suppose that the chip size of the SOA-enabled HONoC is the same as that of the conventional one, as the SOA is small enough to be embedded in a waveguide without widening the space between photonic devices. The traffic patterns are generated using modified BookSim [\[26\]](#page-22-3) and Crux router is deployed because it is optimized for insertion loss and SNR in the mesh topology.

Table 3. Loss and crosstalk coefficient values.

Parameter	Value	Reference
Waveguide crossing loss	-0.12 dB	$\lceil 12 \rceil$
Propagation loss per cm	-0.274 dB/cm	$\lceil 15 \rceil$
Power loss of CSE in OFF state	-0.04 dB	$\lceil 13 \rceil$
Power loss of CSE in ON state	-0.5 dB	[9]
Power loss of PSE in OFF state	-0.005 dB	[9]
Power loss of PSE in ON state	-0.5 dB	[9]
Crossing crosstalk coefficient	-45 dB	[27]
Crosstalk coefficient of PSE in OFF state	-20 dB	[28]
Crosstalk coefficient of PSE in ON state	-25 dB	[28]

5.1. SNR Analysis

In the conventional $m \times n$ mesh-based HONoC, it is proved formally that the worst-case SNR is among the 1st, 2nd and 3rd longest paths and the best SNR performance is achieved when $m = n$ [\[19\]](#page-21-11). For fair comparison, symmetric mesh topologies are chosen for evaluations.

Figure [11](#page-16-0) shows worst-case SNR for the 1st, 2nd and 3rd longest paths according to the SOA gain when every link contains an SOA and the network size is 8×8 , 16×16 , 24×24 and 32×32 , respectively. When the SOA gain is 0 dB, the SNR values are the same as that for non-SOA HONoCs. The SNR gradually improves as the SOA gain increases, whereas it decreases at a certain threshold of the SOA gain. Furthermore, the SNR decreases further than that of the conventional HONoC without SOAs if the SOA gain increases even more.

Figure 11. Worst-case SNR for SOA-enabled HONoC according to the SOA gain for different network sizes: (a) 8×8 ; (b) 16×16 ; (c) 24×24 ; and (d) 32×32 .

[Figu](#page-16-1)re 12 shows the worst-case SNR of the SOA-enabled HONoC and that of the conventional HONoC according to the mesh size in an m \times m mesh. When the network size is larger than 12 \times 12, the noise power exceeds the signal power in the mesh-based HONoC. In addition, the SNR decreases drastically as the network size increases. However, in our proposed architecture, the SNR of the 1st, 2nd and 3rd longest paths are 1.19 dB, 0.89 dB and 0.28 dB when the network size is 24×24 . When the network size is 32×32 , the worst-case SNR is -1.05 dB and the SNR degradation is not large even if the network size is further increased. As the worst-case SNR determines the feasibility and the scalability of the HONoC, the proposed architecture shows a great advantage over the conventional HONoC architecture. t the two two size is further increased. As the worst-case over determines the reasionity and

5 10 15 20 25 30 35 Figure 12. SNR comparison for the 1st, 2nd and 3rd longest paths between SOA-enabled HONoC and **-20 Figure 12.** SNR comparison for the 1st, 2nd and 3rd longest paths between SOA-enabled HONoC and conventional HONoC (without SOAs) in accordance with the mesh size. conventional HONoC (without SOAs) in accordance with the mesh size.

paths with respect to the maximum hop count without SOAs, *h*. The number of SOAs is proportional Figure [13](#page-17-0) shows the SNR improvement in an average basis for the 1st, 2nd and 3rd longest

to the mesh size and is inversely proportional to *h*. When the mesh size is smaller and *h* is larger, the amplification of the optical signal is relatively small. On the contrary, the amplification of the **20** introduced crosstalk noise occurs frequently when the mesh size is larger and *h* is smaller. Therefore, the SNR improvement is closely related to the mesh size as well as *h*, which determines the number of **15** SOAs in the optical signal path. When the SOA placement is performed according to the optimal *h*, the SNR is improved by 4.26 dB, 8.91 dB, 13.69 dB and 18.65 dB when the network size is 8×8 , 16 \times 16, 24×24 and 32×32 , respectively. Therefore, the degradation of the worst-case SNR in the large-scale HONoC can be mitigated through the placement of SOAs. **5 SOR**
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SOA-enabled HONoC according to different h and network sizes. Figure 13. SNR improvement in an average basis for the 1st, 2nd and 3rd longest paths of the

enabled HONoC according to different ℎ and network sizes. SOA-placed links, in accordance with the SOA gain. As previously analysed in Section [4.2.2,](#page-10-1) there is a path in which the SNR decreases due to the several amplified crosstalk noises, compared to the equivalent optical signal power if the SOAs are not placed on all links ($h \neq 0$). Figure [14](#page-17-1) shows the worst-case SNR reduction of the non-SOA region surrounded by the

 $\rm SOA$ gain for the worst-case SNR.

worst-case SNR on the whole network differs depending on h . The SNR losses at the mark X are more as the SOA gain increases. This result denotes that the shorter path length is more affected by 2.06 dB, 3.96 dB and 2.26 dB when *h* = 2, *h* = 4 and *h* = 8, respectively. The SNR reduction is the the amplified crosstalk noise. However, as marked X in Figure [14,](#page-17-1) the appropriate SOA gain for the $\mathcal{L}_{\mathcal{A}}$ becomes smaller, the SNR of the SNR of the longest path traveling along the boundary decreases smaller When *h* becomes smaller, the SNR of the longest path traveling along the boundary decreases

smallest when $h = 2$; however, there are more paths in which such SNR degradation occurs in the network because the SOA spacing is narrower than in other cases. In other words, it means that SNR degradation may occur when SOA spacing is narrow but it does not occur in the same path when SOA spacing is wide. Therefore, $h = 8$ can be the best case considering both the magnitude of SNR reduction and the probability that these SNR-reduced paths will occur.

5.2. Power Consumption $\mathbf{53}$, \mathbf{D} _{conver}, \mathbf{C} _{such} more paths in which such such SNR degradation occurs in the such SNR degradation occurs in the such SNR degradation of the such SNR degradation occurs in the such SNR degradatio network because the SOA space than in other cases. In other cases, it means that SNR

Figure 15 shows the laser source power considering the mesh size, the placement of the SOA and the worst-case insertion loss. We assume that the efficiency of the laser source power is 20% [\[29\]](#page-22-6). If there is no SOA, the laser source power increases sharply as the mesh size increases. In the SOA-enabled HONoC, however, the laser source power is independent of the mesh size. Although additional power consumption due to the SOAs will be inevitable, it is expected to be a comparable trade-off owing to the laser source power savings that will be realized by incorporating this work.

FIGURE 15. Power comparison between a comparison between a conventional HONoC. The SOA-enabled HONoC. **Figure 15.** Laser source power comparison between a conventional HONoC (without SOA) and

in the conventional HONoC and the SOA-enabled HONoC are illustrated in Figure [16.](#page-19-0) We assume that the mesh size is 8×8 and SOAs are placed on every waveguide where the laser source power is the smallest. According to the previous studies, the power consumption of MR activation has been reported as 50 µW [\[30\]](#page-22-7). Simulation was performed on the HONoC without SOAs and the SOA-enabled HONoCs optimized for SNR and power consumption, respectively. Power consumption is reduced by 16.26–35.86% when focusing on the SNR and by 21.81–37.68% when focusing on the total power **12** and when optimizing for power consumption.With common traffic patterns for NoCs, the simulation results in terms of power consumption consumption. There is a slight difference in power consumption between when optimizing for SNR

Figure 16. Power consumption comparison between a conventional HONoC (without SOA) and SOA-enabled HONoC optimized for SNR and power consumption, respectively, for various traffic patterns.

Figure [17](#page-19-1) shows the total power consumption according to the maximum hop count of possible paths without SOAs (*h*) and different traffic patterns when network size is 8×8 . When $h = 0$, the laser source power can be reduced by the SOA gain; however, the efficiency of the SOA gain is low and the number of SOAs is the most. If $h \neq 0$, the number of SOAs in the network is reduced as *h* increases; however, the laser source power and single SOA power consumption increase.

Figure 17. Power consumption of an SOA-enabled HONoC according to the SOA spacing for various $\frac{1}{2}$ traffic patterns. traffic patterns.

When $h = 4$, the number of SOAs is twice as high as when $h = 8$ and the power consumption increases. Furthermore, in the case of $h = 2$, the number of SOAs is three times higher than when $h = 8$; however, the power consumption is reduced slightly or similar. Therefore, the total power consumption in relation to *h* is hard to predict because it is determined by the total sum of the power consumption per unit SOA, the number of SOAs in the network and the laser source power. Simulation results show that the power consumption is the largest when $h = 0$; therefore, placing SOAs on all links is inefficient in terms of power consumption. Contrarily, the minimum power consumption is achieved when $h = 2$ and the overall power consumption of the SOA-enabled HONoC is reduced by 32.17–45.49%, compared to the conventional HONoC.

Figure [18](#page-20-4) shows the comparison of power consumption between the SOA-enabled HONoC and the conventional HONoC according to the mesh size and the hop count between SOAs in a random uniform pattern. The increase in the power consumption in the proposed HONoC is relatively low compared to that in the conventional HONoC because the insertion loss is independent of the longest

path. As a result*,* the power saving realized by the proposed approach becomes prominent as the mesh size grows.

SOA-enabled HONoC in terms of mesh size and maximum hop count without SOAs. **Figure 18.** Power consumption comparison between a conventional HONoC (without SOA) and

enabled Honoc in terms of mesh size and maximum hop count with size and maximum hop count without SOAs. **6. Conclusions**

We proposed the SOA-enabled HONoC architecture to reduce the total power consumption and mitigate SNR degradation, considering network size scalability without increasing laser source output power. An SOA placement algorithm was developed for efficient laser source power reduction and SNR enhancement. We suggested the worst-crosstalk noise model and presented the attuning method for optimal SOA gain with respect to power consumption and SNR, respectively. We constructed a C-based simulator to evaluate the SOA-enabled HONoC with an associated SOA placement algorithm. **6. Conclusions** by an average of 4.26 dB and power consumption is reduced by 32.17% to 45.49% under various traffic patterns in the 8×8 network. The performance gaps regarding various SOA spacings were not significant; on the other hand, the improvement achieved in both SNR and power consumption was
. increased in proportion to the mesh size. Therefore, the proposed SOA-enabled HONoC can be a
increased in proportion to the mesh size. Therefore, the proposed SOA-enabled HONoC can be a scalable solution to cope with the performance degradation problem of large-scale HONoCs. Simulation results showed that the worst-case SNRs for the 1st, 2nd and 3rd longest paths are improved

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of the future semiconductor device various SOA spacing were not significant; on the other hand, the improvement achieved in bother ha of the future semiconductor device.

Conflicts of Interest: The authors declare no conflict of interest.

SOA-enabled HONoC can be a scalable solution to cope with the performance degradation problem derences **References**

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