



# *Article* **Printed Graphene Derivative Circuits as Passive Electrical Filters**

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**Abstract:** The objective of this study is to inkjet print resistor-capacitor (*RC*) low pass electrical filters, using a novel water-based cellulose graphene ink, and compare the voltage-frequency and transient behavior to equivalent circuits constructed from discrete passive components. The synthesized non-toxic graphene-carboxymethyl cellulose (G-CMC) ink is deposited on mechanically flexible polyimide substrates using a customized printer that dispenses functionalized aqueous solutions. The design of the printed first-order and second-order low-pass *RC* filters incorporate resistive traces and interdigitated capacitors. Low pass filter characteristics, such as time constant, cut-off frequency and roll-off rate, are determined for comparative analysis. Experiments demonstrate that for low frequency applications (<100 kHz) the printed graphene derivative circuits performed as well as the circuits constructed from discrete resistors and capacitors for both low pass filter and *RC* integrator applications. The impact of mechanical stress due to bending on the electrical performance of the flexible printed circuits is also investigated.

**Keywords:** inkjet printing; graphene; carboxymethyl cellulose; *RC* low pass filters; flexible electronics

### **1. Introduction**

Over the past decade printable electronics have received considerable attention among researchers for the fabrication of low cost passive filters and sensors. Since very small amounts of functional material can be deposited only at the required locations on the substrate, additive fabrication processes are often used. In particular, inkjet-printed functional materials can be self-assembled on non-absorbent substrates without the need of extreme temperature annealing or harsh chemical etching processes. The direct deposition process also allows various rigid (e.g., glass) and non-rigid (e.g., polymers, paper) substrates to be used. Furthermore, inkjet printing systems are computer-numerically controlled such that thin film circuits are fabricated without direct mechanical contact between the deposition tool and substrate. The use of computerized systems to design and fabricate printed devices also reduces human error and enables circuit or sensor customization. Compared to other alternatives such as photolithography, inkjet printing requires lower infrastructure costs and produces far less hazardous waste materials. In addition, prototyping and implementing design revisions can be made quickly without requiring the construction of new photolithography or stencil masks. This requirement is especially useful for creating mechanically flexible electronics for communication and signal processing circuits in customized wearable devices where a high degree of variation may exist in the circuit parameters. For example, radio frequency identification (RFID) antennas and passive electrical filters are an integral part of many sensors and signal processing systems. Small variations in circuit geometry define resonance frequency of RFID antennas while resistive and capacitive component parameters define cutoff frequency (*fc*) of passive electrical filters.

Although inkjet printing can simplify design and fabrication, the performance of printed circuits ultimately depends on the availability of functional materials. Inkjet-printed thin film electronics are

largely based on metal inks [\[1](#page-12-0)[–3\]](#page-12-1) deposited on rigid substrates such as glass or silica. However, alternative functional inks based on polymers [\[4](#page-12-2)[,5\]](#page-12-3), carbon nanotubes (CNTs) [\[6\]](#page-12-4), and graphene (G)/graphene oxide (GO) [\[7](#page-13-0)[,8\]](#page-13-1) have been demonstrated over the past decade. For example, Chen et al. presented a study based on inkjet printing of conductive poly(3,4-ethylenedioxythiophene)/polystyrene sulfonate (PEDOT/PSS) polymer to fabricate a low pass filter that was operational below the MHz range [\[9,](#page-13-2)[10\]](#page-13-3). In all cases the functional inks should exhibit low toxicity, good printability, and high mechanical/chemical stability. Furthermore, the ink synthesis methods should be simple and required equipment and raw materials must be financially feasible. Finally, the resultant printed thin film on a mechanically flexible substrate must exhibit acceptable electrical performance and excellent mechanical flexibility.

Based on these requirements, liquid phase exfoliation of G is a practical approach for synthesis functional inks. Pristine G is highly hydrophobic and has been shown to be successfully suspended in a handful of organic solvents. *N*-Methyl-2-pyrrolidone (NMP) [\[11\]](#page-13-4) and dimethylformamide (DMF) [\[12\]](#page-13-5) are examples of commonly used organic solvents to suspend graphene nanoparticles. These organic solvents are toxic to human health [\[13\]](#page-13-6), may affect fertility [\[14\]](#page-13-7), and some of them are suspected to have carcinogenic properties [\[15\]](#page-13-8). Several attempts have been made to utilize GO, a heavily oxidized, hydrophilic, and electrically insulating derivative of G, to fabricate thin film circuits. GO films can be heat treated to remove oxygen containing moieties to form reduced GO (rGO) and restore conductivity [\[16](#page-13-9)[,17\]](#page-13-10). Unfortunately, oxidation process permanently damages graphene lattice structure and fully reverting rGO to G is not possible at temperatures sustainable on flexible substrates.

In this research, a novel aqueous *carboxymethyl cellulose* (CMC) functionalized G ink has been developed to fabricate thin film circuits with inkjet printing technology. CMC is a non-toxic cellulose derivative that enables the naturally hydrophobic graphene flakes to be suspended in water solutions. CMC is also known to decompose efficiently under thermal treatment [\[18\]](#page-13-11) which can facilitate its partial removal from deposited films and improve the electrical properties of thin films. Preliminary experiments have shown that the proposed G-CMC ink could be used to fabricate interdigitated capacitors (IDCs) and resistors on mechanically flexible polyimide [\[19\]](#page-13-12). Although the fabrication of individual passive components—such as electrodes, inductors, resistors (*R*) and capacitors (*C*)—is an important step, a deeper understanding of the combined response behavior of several interconnected components is essential for designing more complex inkjet-printed devices. Furthermore, the electrical behavior of fabricated G-CMC devices under mechanical strain needs to be investigated. In this study, the low pass electrical filters are designed according the principles outlined in Section [2.](#page-1-0) The ink synthesis and inkjet fabrication process are briefly summarized in Section [3.](#page-5-0) The electrically conductive and printable G-CMC ink is prepared in-house using non-toxic chemicals and scalable processes. In Section [4,](#page-8-0) the performance of these printed passive filters is compared to their conventional counterparts. Changes in the frequency response of the G-CMC printed devices are further analyzed under mechanical strain. Finally, concluding comments are made in Section [5.](#page-11-0)

#### <span id="page-1-0"></span>**2. Passive Low-Pass Electrical Filters**

Electronic filters are designed using either passive components (e.g., resistors, capacitors, inductors) or active components (e.g., transistors, FETs and Op-amps). One of the most basic, and yet important, filters for communication and control circuits is the low-pass filter (LPF) that attenuates high frequency signals usually associated with carrier or noise. The cut-off frequency (*fc*) depends upon the components used in the design. For example, in low frequency applications  $(f < 100 \text{ kHz})$ , passive filters are often constructed using simple resistor-capacitor (*RC*) circuits while for higher frequency low-pass filters (*f* > 100 kHz) these circuits are usually constructed using passive resistors, capacitors and inductors (*RLC*).

The simplest LPF for low frequency applications is the first-order passive *RC* circuit shown in Figure [1.](#page-2-0) The resistance is independent to frequency variations in the a.c. signal whilst the capacitance changes with frequency. Since the circuit has only one reactive component, the capacitor, the circuit is called a one-pole or first-order filter. The capacitor requires a specified amount of time to charge and discharge through the resistor. At low signal frequencies there is sufficient time for the capacitor charge up to the input voltage ( $V_{in}$ ). However, at high frequencies the capacitor will not have enough time to fully charge before the input a.c. signal switches direction. To maintain the capacitance (*C*) value in the circuit, the capacitor will oppose these small fluctuations in current flow. The opposition to the current flow in the circuit is called impedance. In other words, the capacitive reactance  $(X_C)$  is inversely proportional to the frequency (*f*) applied to the circuit and can be described as inversely proportional to the frequency (*f*) applied to the circuit and can be described as

$$
X_C = 1/(2\pi f C) \tag{1}
$$

<span id="page-2-0"></span>

Figure 1. Passive first-order resistor-capacitor (RC) low-pass filter (RC-FO) for attenuating higher frequencies (*f* > *f c* ) or performing wave shaping (i.e., *RC* integrator circuit). frequencies (*f* > *fc*) or performing wave shaping (i.e., *RC* integrator circuit).

Since the first-order  $RC$  LPF ( $RC$ -FO) is constructed from one resistor  $(R)$  and one capacitor  $(C)$ , the overall impedance (*Z*) impedance can be calculated as the overall impedance (*Z*) impedance can be calculated as

$$
Z = \sqrt{R^2 + X_C^2} \tag{2}
$$

where impedance, resistance, and capacitive reactance are all given in ohms (Ω). Note that *R* is stable where impedance, resistance, and capacitive reactance are all given in ohms (Ω). Note that *R* is stable and fixed whereas the *XC* varies with respect to the input voltage signal. and fixed whereas the *X<sup>C</sup>* varies with respect to the input voltage signal.

The voltage drop across the capacitor (*Vout*) is dependent on capacitive reactance and circuit The voltage drop across the capacitor (*Vout*) is dependent on capacitive reactance and circuit impedance. The *RC* potential voltage divider equation can be given as impedance. The *RC* potential voltage divider equation can be given as

$$
\frac{V_{out}}{V_{in}} = \frac{X_C}{\sqrt{R^2 + X_C^2}} = \frac{X_C}{Z}
$$
 (3)

where *Vin* is the input voltage signal. By using Equation (3) it is possible to calculate *Vout* at any applied where *V*<sub>*in*</sub> is the input voltage signal. By using Equation (3) it is possible to calculate *V*<sub>*out*</sub> at any applied frequency (*f*).

The magnitude of the gain of the first-order RC LPF in decibels is given as  $\|H(f)\|_{dB}$  and can be calculated as calculated as

$$
|H(f)|_{dB} = 20 \log \frac{V_{out}}{V_{in}} \tag{4}
$$

The frequency at which the output of the design of the second by  $\frac{1}{2}$  denotes the cut-off in  $\frac{1}{2}$  denotes the cu frequency (*f* written as frequency ( $f_c$ ). The  $f_c$  for the electrical filter depends on the values of resistor and capacitor and can be  $\frac{1}{2}$ The frequency at which the output of the low pass filter is reduced by 3 dB is called the cut-off

$$
f_c = \frac{1}{2\pi RC} \tag{5}
$$

 $\frac{27}{\pi}$  where *C* is given in Farads, *R* in Ohms and *f<sub>c</sub>* in Hz. At *f<sub>c</sub>* the output signal (*V*<sub>*out*</sub>) is 70.7% of the input signal (*Vin*), and gradually decreases to zero as frequencies increase. This cut off point will occur when the resistance and the capacitive reactance are equal  $(R = X_C)$  and, at this point, the input signal is attenuated by −3 dB/decade. The gain will further decrease along with the output voltage after the cut off frequency where the slope reaches a roll-off point that occurs at ∆*L* = −20 dB/decade.

#### *Nanomaterials* **2018**, *8*, 123 4 of 14  $t_{\text{R}}$  and the capacitive reaction  $r_{\text{R}}$  and  $r_{\text{R}}$  and  $r_{\text{R}}$  and, the input signal is point, the i

The time constant (*τ*) of a series *RC* circuit is defined as the time taken by the capacitor to charge up to 63.2% of the final steady state value and given by

$$
\tau = 1/(2\pi f_c) = RC
$$
\n(6)

Note that the time constant is also defined as the time taken by the capacitor to discharge to 36.8% Note that the time constant is also defined as the time taken by the capacitor to discharge to of steady state value. 36.8% of steady state value.

In addition to attenuating high frequency sinusoidal signals, the basic *RC* LPF can act as a wave In addition to attenuating high frequency sinusoidal signals, the basic *RC* LPF can act as a wave shaping circuit for a square wave input. This type of LPF circuit, sometimes called a *RC* integrator shaping circuit for a square wave input. This type of LPF circuit, sometimes called a *RC* integrator circuit, can convert the square wave to a triangular waveform. At low frequencies the output signal circuit, can convert the square wave to a triangular waveform. At low frequencies the output signal will emulate the square wave input. However, as the frequency increases the output waveform takes on will emulate the square wave input. However, as the frequency increases the output waveform takes a triangular shape because of the capacitor's inability to instantly charge and discharge. The amplitude of the output signal will also decrease. If the  $\tau$  is long compared to the time period of the input waveform  $(V_{in})$ , then the resultant output waveform  $(V_{out})$  will be triangular in shape and the higher the input frequency the lower will be the output amplitude compared to input.

It in put frequency the lower will be the output amplitude compared to input.<br>Sometimes a single stage LPF may not enough to remove all unwanted frequencies, then second-order filters are used (Figure [2\)](#page-3-0). The second-order *RC* low pass filters (RC-SO) are constructed by cascading two first-order RC LPFs. In contrast to a first-order RC LPF, this type of filter produces a<br>diagonal control in the control of t slope of −40 dB/decade. It is possible to create even higher-order filters by connecting more first-order slope of −40 dB/ decade. It is possible to create even higher-order microsoft connecting more first-order RC LPFs resulting in a RC filter circuit known as an *n*<sup>th</sup>-order LPF with a roll-off slope of order filters are used (Figure 2). The second-order *RC* low pass filters (RC-SO) are constructed by *RC* LPFs resulting in a *RC* filter circuit known as an *n*th-order LPF with a roll-off slope of

$$
\Delta L_{Total} = n\Delta L = n(-20 \text{ dB/decade})
$$
\n(7)

<span id="page-3-0"></span>

**Figure 2.** Illustration of cascading two first-order RC low pass filters (RC-FO1 and RC-FO2) to obtain a a second-order *RC* low pass filter (RC-SO). second-order *RC* low pass filter (RC-SO).

Unfortunately, these higher-order *RC* LPFs can introduce problems because the gain and Unfortunately, these higher-order *RC* LPFs can introduce problems because the gain and accuracy of the final filter design declines with increasing order. When identical *RC* LFPs are cascaded in series, the output gain at the required cut-off frequency  $(f_c)$  is attenuated and the roll-off slope  $\Delta L_{Total}$ increases in relation to the number of stages  $(n)$ . For example, the cut-off frequency of a second-order  $PGL$ second-order *RC* low-pass filter (i.e., RC-SO) is given as *RC* low-pass filter (i.e., RC-SO) is given as

$$
f_c = \frac{1}{2\pi\sqrt{R_1 C_1 R_2 C_2}}
$$
\n(8)

Note that cascading passive *RC* LPFs to produce larger-order filters is difficult to accurately Note that cascading passive *RC* LPFs to produce larger-order filters is difficult to accurately create because the dynamic impedance of each constituent filter will affect the neighboring filters in the network. However, to reduce the impact of loading it is possible to adjust the impedance of each successive stage by a factor of 10; that is  $R_2 = 10R_1$  and  $C_2 = C_1/10$ .

In terms of printable electronics, the resistors are often given as conductive traces where the In terms of printable electronics, the resistors are often given as conductive traces where the resistivity varies with geometrical dimensions such as length, width and film thickness. Often, increase resistivity varies with geometrical dimensions such as length, width and film thickness. Often, in electrical resistivity is achieved by increasing the length of the trace. It is important to realize, however, that the droplet deposition printing methods do not create uniform thin films similar to those achieved using photolithography or chemical vapor deposition (CVD) methods. Consequently, highly precise resistance values are nearly impossible to obtain with inkjet printing.

There are different approaches to creating capacitors, but the most common printable design is the There are different approaches to creating capacitors, but the most common printable design is two-dimensional interdigitated capacitor (IDC). The IDC has a planar structure where the consecutive fingers are connected to positive and negative electrodes (Figure [3\)](#page-4-0). The capacitance is dependent upon the dielectric properties of the electrode surface and can change when the device is exposed to an external influence (e.g., material under test). The IDC electrodes can be printed directly on the substrate material and coated with a very thin passivation layer to prevent an electrical short when encountering the material under test (MUT). Changes in the MUT will alter the measured capacitance. For signal processing applications it is important to electrically isolate the capacitor to minimize the impact of environmental changes (i.e., *CMUT*→0). minimize the impact of environmental changes (i.e., *CMUT*→0).

<span id="page-4-0"></span>

**Figure 3.** Basic layout design and cross-sectional view of a planar graphene-carboxymethyl cellulose **Figure 3.** Basic layout design and cross-sectional view of a planar graphene-carboxymethyl cellulose (G-CMC) interdigitated capacitor (IDC) that responds to dielectric changes in the material under (MUT). test (MUT).

From a design perspective, the *total capacitance* (*CIDC*) of the IDC can be estimated by From a design perspective, the *total capacitance* (*CIDC*) of the IDC can be estimated by

$$
C_{IDC} = l(n-1)(C_{MUT} + C_{Passivation} + C_{Substrate})
$$
\n(9)

where *l* is the length of the finger electrode, *n* is the number of digits, and C<sub>MUT</sub>, C<sub>Passivation</sub>, and C<sub>Substrate</sub> are the partial capacitances due to the MUT, passivation layer and substrate, respectively.

The partial capacitance values are based on material permittivity and can be calculated as

$$
C_{MUT} + C_{Substrate} = \varepsilon_0 \left(\frac{\varepsilon_{MUT} + \varepsilon_{Substrate}}{2}\right) \frac{K\left[\sqrt{1 - \left(\frac{d}{b}\right)^2}\right]}{K\left[\frac{d}{b}\right]}
$$
(10)

$$
C_{Passivation} = \varepsilon_0 \varepsilon_{Passivation} \left(\frac{t}{d}\right)
$$
 (11)

where  $d$  is the gap between adjacent electrodes,  $b$  is the distance between electrode centers,  $t$  is the thickness of the printed G-CMC electrode,  $\varepsilon_0$  is the permittivity of free space,  $\varepsilon_{MUT}$  is the relative permittivity of the MUT,  $\varepsilon_{Passivation}$  is the relative permittivity of the passivation layer, and  $\varepsilon_{Substrate}$  is the relative permittivity of the polydimethylsiloxane (PDMS) substrate. The function *K*[*x*] is determined by using the complete elliptic integral of the first kind because it provides a good model for magnetic fields.

The total capacitance of the IDC will increase with smaller gaps ( $d$  and  $d_E$ ) but the minimum gap size is dictated by the smallest reproducible feature size. For inkjet printed circuits the minimum feature size is dependent, in large part, upon the nozzle diameter and resolution of the XY positioning system. The thickness of the conductor material (*t*) and its electrical resistivity (*ρ*) will also affect circuit performance. However, controlling the thickness of an inkjet-printed film is a challenge because of the inconsistent droplet deposition process. Finally, increasing the length of the fingers (*l*) will increase the overall capacitance but will significantly enlarge the physical dimensions of the printed IDC.

#### <span id="page-5-0"></span>**3. Materials and Methods**

#### *3.1. Ink Synthesis*

To synthesize G-CMC ink, 2 mg/mL 250 kDa CMC was dissolved in DI water under magnetic stirring. Into this solution, G nanoparticles (Graphene Supermarket-A12) were mixed at a concentration of 2 mg/mL. After a brief and gentle shake, the mixture was sonicated for 8 h in a 110 W output bath ultra-sonicator. Resulting G-CMC suspension was centrifuged at 2000 *g* for 15 min to precipitate large and unstable particles. This step was repeated for a second time. After sonication and centrifuge steps, some amount of free CMC persists in the solution. This unbound CMC can negatively affect film properties. To remove free CMC, the solution was centrifuged at 20,000 *g* for 30 min and supernatant was disposed. Sediment left at the bottom of centrifuge tube was re-suspended in DI water. High speed centrifuge step was repeated one more time to ensure complete removal of free CMC. Finally, the second step sediment was re-suspended in DI water at 4 times the initial concentration.

There are several frequently encountered problems with inkjet deposition of materials. The most common problems are poor wetting and coffee ring effect (CRE). Poor wetting is usually encountered with water based inks. Water has low viscosity  $(\sim 1 \text{ cP})$  and very high surface tension  $(\sim 70 \text{ mN/m})$ . In comparison, most flexible plastic substrates have relatively lower surface energy values that can range from 40 (Polyimide) to 18 (PTFE) mN/m. As a result, poor wetting and quick recession of contact line can occur when printing with plain water. It is also necessary to prevent formation of edge heavy deposition of particles which is commonly referred to as CRE (Figure [4\)](#page-6-0). CRE happens when a droplet has pinned contact lines and is evaporating [\[20\]](#page-13-13). Because droplet edges evaporate faster due to larger surface area and contact line cannot recede, liquid from droplet center has no choice but to flow towards the edges to maintain reduction in contact angle *θ*. This flow carries colloid particles and results in distorted film deposition, resembling coffee stains on a surface. Conversely, if contact lines can recede, contact angle *θ* would not change and flow towards the edges would not be necessary. Both poor wetting and CRE can be addressed by choosing the correct solvent. Hence, a binary solvent system was proposed previously [\[19\]](#page-13-12) where low rate of change in surface tension of binary system during evaporation can keep edge directed Marangoni flow under control. N-Propanol/water binary system has a relatively stable surface tension profile between 20% m/m to 100% m/m N-Propanol. As a result, surface tension gradient between droplet center and edge differs only slightly during evaporation. Moreover, smaller *θ* angle means less difference between evaporation rate of droplet center and edge. Hence, it was observed that surface tension gradient was not large enough to exacerbate CRE and films with good morphology could be achieved when  $28\%$  m/m N-Propanol was present in G ink. Based on this, all the devices presented in this study were inkjet printed with G-CMC ink containing 28% m/m N-Propanol.

<span id="page-6-0"></span>

**Figure 4.** Flow towards droplet edges can result in the coffee ring effect (CRE). Dotted arrows show **Figure 4.** Flow towards droplet edges can result in the coffee ring effect (CRE). Dotted arrows show the direction of initial flow. Due to decreasing *θ* angle during evaporation, liquid displacement from the direction of initial flow. Due to decreasing *θ* angle during evaporation, liquid displacement from center to edge occurs. On the other hand, if initial contact angle  $\theta$  is sufficiently small and there is no surface tension gradient between center and edge, droplet can evaporate with minimal in-droplet flow towards edges.

## *3.2. Inkjet Printing of Passive Electrical Filters 3.2. Inkjet Printing of Passive Electrical Filters*

For this investigative study, all graphene-based passive *RC* filters were fabricated through using For this investigative study, all graphene-based passive *RC* filters were fabricated through using a a custom-made inkjet printer. The printer features an XY positioning system capable of 2.5 µm step custom-made inkjet printer. The printer features an XY positioning system capable of 2.5 µm step size and 4800 mm/min feedrate. G-CMC ink was deposited by HP6602A print head. This print head is capable of jetting 160 pl of ink per droplet. Droplets were spaced 25  $\mu$ m and 180  $\mu$ m in vertical and horizontal directions, respectively. All circuitry was printed 10 times. Based on previous data, this horizontal directions, respectively. All circuitry was printed 10 times. Based on previous data, this would correspond to approximately 250 to 300 nm film thickness. Substrates (HN polyimide) were would correspond to approximately 250 to 300 nm film thickness. Substrates (HN polyimide) were cleaned by ethanol/water wash and air dried. Cleaned substrates were corona treated briefly and cleaned by ethanol/water wash and air dried. Cleaned substrates were corona treated briefly and submerged in a polyethyleneimine solution (0.75 g/100 mL PEI + 2.95 g/100 mL NaCl) for 20 min. submerged in a polyethyleneimine solution (0.75 g/100 mL PEI + 2.95 g/100 mL NaCl) for 20 min. Substrates were washed with DI water, air dried, and submerged into a solution of Polystyrene 4-Sulfonate (0.9  $g/100$  mL PS + 2.95  $g/100$  mL NaCl) for 20 min. Finally, substrates were washed with DI water, air dried, and submerged in PEI solution one last time. Resulting polyelectrolyte coating DI water, air dried, and submerged in PEI solution one last time. Resulting polyelectrolyte coating on on polyimide substrates facilitated homogeneous self-assembly of deposited particles. polyimide substrates facilitated homogeneous self-assembly of deposited particles.

Printed circuits were heat treated in an Argon environment at 320 °C for 30 min to partially Printed circuits were heat treated in an Argon environment at 320 ◦C for 30 min to partially decompose the CMC. Note that decomposition of CMC increases conductivity roughly 2 times. Once decompose the CMC. Note that decomposition of CMC increases conductivity roughly 2 times. Once heat treatment was finished, circuits were covered with a second layer of polyimide with silicone heat treatment was finished, circuits were covered with a second layer of polyimide with silicone adhesive on the side facing the *RC* components. Exposed contact pads were covered with small adhesive on the side facing the *RC* components. Exposed contact pads were covered with small amount of silver paste to allow stable connection to Agilent arbitrary waveform generator and amount of silver paste to allow stable connection to Agilent arbitrary waveform generator and Agilent DSO oscilloscope.

Several passive filters were inkjet-printed for analysis: first-order G-CMC low pass filter #1 (G-FO1), first-order G-CMC low pass filter #2 (G-FO2), and second-order G-CMC low pass filter (G-SO). The two first-order LPFs were variant designs. For G-FO2 and G-SO, filter circuits were fabricated with a different R values to demonstrate the degree of control on  $f_c$ . The R, C,  $\tau$  and  $f_c$ values for each inkjet-printed G-CMC filter are summarized in Table [1.](#page-7-0) Schematics of these all filter circuits are depicted in Figure [5.](#page-7-1) Each filter shares the same basic IDC capacitor design with 18 electrode fingers where  $w = 0.5$  mm,  $d = 0.5$  mm and  $l = 20$  mm. The entire IDC covers an area of  $30 \times 28$  mm<sup>2</sup>. The resistor *R* for variant designs was adjusted by simply increasing length and width of the resistive trace. The printed trace for the  $R_1$  resistor was 48 mm  $\times$  1.8 mm while  $R_2$  resistor was 43 mm  $\times$  1.8 mm.  $R_3$  and  $R_4$  resistors were 55 mm  $\times$  1.3 mm and 45 mm  $\times$  1 mm, respectively. Slight dimensional variations were observed in the actual printed films and these variations resulted in a ~2% difference from the desired resistance values. Note that from a design perspective, the resistance of the printed trace will increase with length or reduction in film thickness. For comparative purposes, two additional filter circuits were constructed on a breadboard using conventional  $\overline{R}$  and  $\overline{C}$  components.  $\overline{R}$  and  $\overline{C}$ values of these circuits were chosen to closely match the *R* and *C* components of G-CMC based component values of these circuits were chosen to closely match the *R* and *C* components of G-CMC based devices.

<span id="page-7-0"></span>**Table 1.** Resistance (R), capacitance (C), time constant ( $\tau$ ) and cut-off frequency ( $f_c$ ) for printed G-CMC passive filters.



<span id="page-7-1"></span>

(**a**) First-order G-CMC low pass filter #1 (G-FO1).





(**b**) First-order G-CMC low pass filter #2 (G-FO2).



(**c**) Second-order G-CMC low pass filter (G-SO).

Figure 5. First-order G-CMC low pass filters (a) G-FO1 and (b) G-FO2, and second-order G-CMC low pass filter (**c**) G-SO circuits and the corresponding inkjet-printed devices based on these designs. pass filter (**c**) G-SO circuits and the corresponding inkjet-printed devices based on these designs.

#### <span id="page-8-0"></span>**4. Results**  $T_{\rm eff}$  results was analyzed by supplying a sinusoidal by supply

The frequency response of the passive filter circuits was analyzed by supplying a sinusoidal signal with  $1 \text{ V}_{\text{pp}}$  amplitude. The output voltage was measured with the oscilloscope and mean  $V_{out}$  was recorded for each data point (10 Hz to 1 MHz). G-CMC based low pass filters exhibited diminishing  $V_{out}$  at their designated cut-off frequency (f<sub>c</sub>). G-FO1 had a calculated f<sub>c</sub> of ~30.5 kHz while the variant G-FO2 was fabricated with a different *R* value to observe the sensitivity of the device to changing component parameters. The  $f_c$  of G-FO2 was calculated to be 27.8 kHz. As expected,<br>the maximum of a fitting G-FO2 filtral additional director that have a P-Rath G-FO1 and G-FO2 methanology the measured *fc* of the G-FO2 filter had shifted due to the larger *R*. Both G-FO1 and G-FO2 performed satisfactorily as low pass electrical filters (Figure [6\)](#page-8-1). A conventional *RC* passive filter (RC-FO) was satisfactorily as low pass electrical filters (Figure 6). A conventional *RC* passive filter (RC-FO) was also constructed using the same *R* and *C* values for G-FO1. The frequency response of RC-FO and also constructed using the same *R* and *C* values for G-FO1. The frequency response of RC-FO and G-G-FO1 did not differ significantly with both filters exhibiting diminishing  $V_{out}$  near their designated *fc*. The roll-off rate was essentially identical for both G-FO1 and G-FO2, and slightly less than RC-FO The roll-off rate was essentially identical for both G-FO1 and G-FO2, and slightly less than RC-FO (Table [2\)](#page-8-2). However, modest irregularities in the gain were observed between 100 kHz and 1 MHz for both G-FO1 and G-FO2. The nonlinearities arise, in part, from the dimensional variations in the geometric parameters of the printed IDC capacitor which determine the natural resonance frequency geometric parameters of the printed IDC capacitor which determine the natural resonance frequency of the device. Small variations in the fabricated IDC capacitor due to the droplet deposition method (i.e., inkjet printing process) will produce nonlinearities in the frequency response. It is likely that the (i.e., inkjet printing process) will produce nonlinearities in the frequency response. It is likely that the increase in capacitance near the resonant frequency of the printed IDC capacitor may have contributed increase in capacitance near the resonant frequency of the printed IDC capacitor may have to the slight observed shift in gain between the conventional *RC* (RC-FO) and printed (G-FO1 and G-FO2) filter circuits.

<span id="page-8-1"></span>

<span id="page-8-2"></span>Figure 6. First-order low pass filter performance of RC-FO, G-FO1 and G-FO2. Note that the  $f_c$  was same for both G-CMC circuits and roll off rate was lower than the conventional *RC* low pass filter. same for both G-CMC circuits and roll off rate was lower than the conventional *RC* low pass filter.



G-CMC Printed *RC* Circuit 14.4 15.2

**Table 2.** Roll-off rate comparison of graphene-CMC passive filters. **Table 2.** Roll-off rate comparison of graphene-CMC passive filters.

A second-order passive filter (G-SO) was also fabricated and characterized separately (Figure 7). The calculated  $f_c$  for the G-SO was around 8 kHz. A small decrease was observed for G-SO  $V_{out}$ . Similar irregularity was also observed for second-order filter constructed from conventional *RC* components (RC-SO) as well, albeit at a smaller magnitude. The roll-off rates of the G-SO and RC-SO filters were<br>15.3 and 14.0 and 1.11 and 1.11 and 1.11 and 1.12 and 1.13 and 1.14 and 1.15 and 1.11 and 1.11 and 1.11 and 1. compared to the RC-SO despite similar component values. compared to the RC-SO despite similar component values.15.2 and 18.8 dB/decade, respectively. The *f<sup>c</sup>* of G-SO showed a slight shift (approximately 1 kHz)

<span id="page-9-0"></span>

Figure 7. Comparison of second-order G-CMC low pass filter performance with equivalent conventional RC circuit. Note that the second order filter roll-off rate was roughly twice of first order circuit. As expected,  $f_c$  value did not differ between first and second-order low pass filters. voltage signal was detectable beyond 600 kHz. The output voltage signal was detectable beyond 600 kHz. voltage signal was detectable beyond 600 kHz.

The transient response of both G-CMC and conventional passive electrical filters was also investigated using a square wave with 1V amplitude. Transient response was observed for both low (1 kHz) and high frequency regimes. For high frequency regime, which is defined here as frequencies at and beyond  $f_c$ , square wave inputs were effectively converted to triangular signals for both graphene and RC conventional filters. Hence, all filters performed as expected from RC integrator circuits. Change in waveform below and above  $f_c$  can be observed in Figure [8](#page-10-0).



(**b**) First-order G-CMC integrator (G-FO1). (**b**) First-order G-CMC integrator (G-FO1).

**Figure 8.** *Cont.*

<span id="page-10-0"></span>

Figure 8. Comparison of transient response for (a) first-order RC integrator; (b) first-order G-CMC integrator; (c) second-order RC integrator, and (d) second-order G-CMC integrator. As expected, output  $f_c$ , because the measurement of the set of the set of the set of the measurement of the expected,  $\exp$  and  $\exp$ (right-side). integration with the corder *RCC* integration order  $\int_C$ , and  $\int_C$ , and  $\int_C$  is expected to a dialigular order  $v = \sin(\theta)$ .

Finally, the inkjet-printed low pass filters were tested under mechanical stress. Inkjet-printed G-CMC films have already been shown to exhibit excellent flexibility under mechanical stress [\[19,](#page-13-12)[21\]](#page-13-14). Shape conformable and wearable electronics should not only perform under mechanical stress but also behave in predictable fashion. In the case of passive filters, a large variation in capacitance and/or  $\frac{1}{2}$  resistance of the circuit would cause a large shift in  $f_c$  during high strain. This could potentially cause device failures. To assess the  $f_c$  error margins due to bending stress, each device was analyzed while being rolled into a cylinder (Figur[e 9](#page-10-1)). The frequency response characteristics for each G-CMC printed filter (G-FO1, G-FO2, and G-SO) showed no observable changes while under strain.  $r_{\text{max}}$  of the circuit would cause a large shift in  $f_c$  during high strain. This could potentially cause  $\sigma$  for  $\sigma$ ,  $\sigma$  for  $\sigma$ , and  $\sigma$  so  $\sigma$ , showed no observable changes while and strain.

<span id="page-10-1"></span>

**Figure 9.** G-CMC filter devices were rolled into a cylinder during mechanical stress tests. There were were no measurable differences in frequency response behavior between rolled and unrolled states. As expected, the  $R$  and  $C$  component values of the individual filters were also unaffected from rolling. **Figure 9.** G-CMC filter devices were rolled into a cylinder during mechanical stress tests. There were **Figure 9.** G-CMC filter devices were rolled into a cylinder during mechanical stress tests. There

In contrast, it was possible to manipulate the frequency response of the filters by placing a foreign In contrast, it was possible to manipulate the frequency response of the filters by placing a object, such as the finger, on the surface of the IDC capacitor. The shift in the frequency response behavior, as shown in Figure [10,](#page-11-1) is expected because the capacitance of the IDC is based on the the dielectric properties of the printed film, substrate and surrounding media. For a finger placed on the dielectric properties of the printed film, substrate and surrounding media. For a finger placed on the IDC of the G-FO1 filter circuit, the cut-off frequency  $f_c$  decreased by  $\sim$ 5 kHz. In addition to increasing the capacitance of the IDC component, the foreign object (i.e., human finger) may have introduced stray capacitance and inductance that impacted the filter circuit and decreased the observed roll-off rate. The variations in dielectric properties of the surrounding media clearly change the response characteristics of passive filters constructed from printed IDCs. Future research activities will need to focus on the impact of dielectric sensing on the filter characteristics.

<span id="page-11-1"></span>

**Figure 10.** Change in the frequency response of the G-FO1 filter when capacitance is increased by **Figure 10.** Change in the frequency response of the G-FO1 filter when capacitance is increased by introducing a foreign object to the IDC. Note that  $f_c$  of G-FO1 is shifted from 30.5 kHz to 5 kHz when a a finger is placed on the IDC component. finger is placed on the IDC component.

## <span id="page-11-0"></span>**5. Discussion 5. Discussion**

In this study, several passive resistor-capacitor (RC) electrical filters were printed on In this study, several passive resistor-capacitor (RC) electrical filters were printed on mechanically flexible polyimide substrates using a customized inkjet printer and synthesized non-toxic aqueous graphene-carboxymethyl cellulose (G-CMC) ink. The fabrication and post-processing of electronic devices were achieved using simple tools; graphics software for device design, XY plotter style printer with HP6602A print head for ink droplet deposition, and a low temperature treatment to decompose the CMC in the printed films. The various G-CMC printed filters exhibited frequency response and transient behavior similar to conventional discrete component *RC* filter circuits. Minor variations arose because the printed circuit designs incorporated two-dimensional interdigitated capacitors (IDCs) instead of traditional parallel plate capacitors. Adjusting the design parameters enabled filters with different cut-off frequencies and roll-off rates to be fabricated. However, the quality of the printed films created for the circuits were influenced by the limitations of the inkjet printing process. Inkjet printing is a controlled droplet deposition method that produces a very different type of film than chemical vapor deposition (CVD) or photolithography. The print head nozzle size, resolution of the XY positioning system and droplet speed all play a role in the physical structure and dimensional accuracy of the resultant conductive film. Droplet deposition methods also require multiple passes over the same feature to ensure a contiguous film for unimpeded flow of electric charge. These factors will impact the observed electrical behavior of any inkjet printed circuit or sensor. In this regard, the conductive ink and printing methodology described in this paper is best suited for flexible electronics that do not require high precision. A further advantage is that the water based ink is synthesized from non-toxic, and likely biocompatible, materials. This presents new opportunities for novel wearable technologies or dissolvable electronics.

The proposed ink synthesis and droplet deposition methods are also transferable to other types of engineered materials, such as doped graphene (DG) and graphene/metal composite inks. It has been shown in past studies that the electrical conductivity of G-CMC films can be significantly increased through gold chloride (AuCl<sub>3</sub>) doping [\[22\]](#page-13-15) or directly printing G-CMC/silver nanoprism (AgNP) composite inks [\[23\]](#page-13-16). In addition, N-type DG has been shown to possess favorable electrical properties [\[24\]](#page-13-17) and the combined use of G and GO, which is considered a P doped material, has been used to create a Schottky barrier [\[25\]](#page-13-18). In this context, it may be possible to fabricate highly efficient optically transparent electrodes or simple diodes by printing various combinations of DG, G-AgNP, and G-CMC inks.

One of the greatest challenges in fabricating sophisticated electronic circuitry is overcoming the limitations of the inkjet printing process. The minimum line width, or feature size, that an advanced material printer can achieve is ~25 µm. Large character print heads, such as the HP6602A, tend to deposit larger ink droplets and therefore produce bigger features. During this study, it was observed that the minimum line width could be as large as  $500 \mu m$ , depending on substrate properties. Fidelity to design dimensions was also problematic due to the large droplet size. However, using alternative commercial printers with smaller droplet volumes (33 pl for HP54 print head) could allow for printing thinner and more robust lines. Nevertheless, the extremely small features achieved using lithography techniques are currently impossible with direct inkjet printing. To address these dimensional limitations, it is possible to combine inkjet printing and micromachining (e.g., laser or micromilling). Previous research has shown that it is possible to micromachine  $8 \mu m$  gaps on rGO films using a 120 femtosecond laser [\[21\]](#page-13-14). The micromachining operation was performed on polyimide substrates and no damage to polyimide was experimentally observed. Theoretically, it should also be possible to micromachine highly robust submicron features G-CMC films with a well optimized pulsed laser system. This would increase robustness of fabricated devices and substantially reduce error margin that stems from otherwise poorly controlled thin film geometry. Furthermore, by laser annealing, the heat treatment step can be eliminated, which would make it possible print on other types of plastics. Based on these preliminary observations, future work will focus on laser heating and micromachining of very fine features on G-CMC inkjet-printed films.

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**Author Contributions:** D.S. and G.K.K. conceived and designed the experiments, analyzed the data and wrote the paper. D.S. performed the experiments and G.K.K. contributed materials for this research.

**Conflicts of Interest:** The authors declare no conflict of interest.

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