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Half-Wave Phase Shift Modulation for Hybrid Modular Multilevel Converter with Wide-Range Operation

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Abstract: Hybrid modular multilevel converters (MMCs), which combine submodule chain links and device series switches, offer advantages such as lower costs and smaller volumes compared with MMCs. However, the hybrid MMCs only operate at a fixed modulation ratio, potentially compromising system adjustment ability. This paper presents a half-wave phase shift modulation (HPSM) strategy aimed at extending the operation range of a hybrid MMC. First, the commutation angle is introduced as a control variable to change the fixed voltage modulation ratio. The energy balance of the converter is completed by adjusting the commutation angle. Then, the operation performance for the half-wave alternating multilevel converter (HAMC) with the proposed HPSM strategy is analyzed. Finally, the full-scale simulations are carried out to verify the theoretical analysis and the feasibility of the proposed control strategy. Compared to the third-order harmonic current injection (THCI) strategy, HPSM reduces operating losses by 50% and demonstrates superior control performance.

Keywords: hybrid modular multilevel converter; energy balance; half-wave phase shift modulation



Citation: Ma, J.; Peng, Y.; Su, Z.; Gu, Y.; Ni, Q.; Yang, Y.; Wang, Y.; Liu, J.

Half-Wave Phase Shift Modulation for Hybrid Modular Multilevel Converter with Wide-Range Operation.

Electronics **2024**, *13*, 3556. <https://doi.org/10.3390/electronics13173556>

Academic Editor: Ahmed Abu-Siada

Received: 15 August 2024

Revised: 30 August 2024

Accepted: 4 September 2024

Published: 7 September 2024



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1. Introduction

The modular multilevel converter (MMC) emerged as the most attractive topology for medium and high-voltage applications, primarily owing to its scalability and superior harmonic performance [1–3]. Many high-voltage DC (HVDC) projects using MMC were developed in recent years, such as the ± 400 kV Rudong offshore wind power project and the ± 500 kV Zhangbei multiterminal DC grid [4,5]. In medium voltage applications, the excellent dynamic performance and robustness of the MMC solid-state transformers (SSTs) [6,7] are widely used in railway and ship systems [8,9]. Despite their advantages, these projects face challenges related to bulky footprints and high construction costs, caused by submodule (SM) capacitors accounting for almost half of the volume and cost of the MMC valve. Lowering SM capacitance and reducing SM counts garnered great attention for achieving a more compact MMC design. The exchange of AC and DC power in each arm is the primary factor determining the capacitance value, thus large floating capacitors in SMs remain necessary to limit DC voltage variations within an acceptable range [10]. Therefore, innovations in MMC topology to create a more compact structure are a key research trend aimed at significantly reducing the need for the floating capacitors.

Introducing device series switches in each phase of MMC led to the development of hybrid MMCs that can reduce the required SM number and offer a promising solution for lightweight MMC designs [11–17]. Various hybrid MMC topologies and operation principles were proposed. For example, the hybrid cascaded multilevel converter introduces full-bridge chain links (FBCLs) on the AC side of two-level converters [11]. For the same DC link voltage, this design reduces the number of SMs and floating capacitors to just

one-quarter of those required by conventional MMCs by utilizing FBCLs as active filters, eliminating the need for power conversion. However, the series connection of many devices limits the voltage and power levels of the converter. The alternate arm multilevel converter (AAMC) was proposed in [12], which uses the hybrid cascade of the FBCLs and device series switches as the phase arm. It alternately conducts switches in the upper and lower arms. This approach approximately halves the maximum output voltage of the FBCLs compared to conventional MMCs, thus requiring fewer SMs and smaller capacitors [13]. However, the AAMC operates only at a fixed point known as the “sweet spot” in natural balance. At the sweet spot, the AAMC operates at an over-modulation state of $4/\pi$, so that full-bridge SMs (FBSMs) are required to generate the negative arm voltages in the over-modulation mode. Additionally, the hybrid arm-multiplexing MMC (HAM-MMC) was developed, offering higher SM utilization efficiency and fault ride-through capability [14–16]. Nevertheless, this topology also suffers from a fixed operation point, which requires additional active control for energy balance. The half-wave alternating multilevel converter (HAMC) is proposed in [17], combining time-division multiplexing and alternating conduction. It achieves a reduction in at least 59% in the total number of SMs and significantly enhances the compactness of MMC designs. However, an energy balancing strategy is still required to enable wide-range operation.

Hybrid MMCs can effectively reduce the SM number by incorporating device series switches, thereby achieving a lightweight MMC. However, energy balancing issues often arise under the wide-range operation. Recent research primarily focused on expanding the operation range of these compact converters to address these challenges. Furthermore, HPSM increases the operation range of the converter without requiring additional hardware. In the “short-overlap” mode [18–20], switches in the upper and lower arms close simultaneously during their alternating conduction, forming a short-overlap process and a temporary DC path. In this mode, the “overlap” angular duration ranges from 15° to 18° , which still severely limits the AC output voltage range. A new overlap modulation method is proposed in [21]. This method extends the “overlap” angle duration to 60° , providing a path for circulating current to balance the converter energy at any time. However, this “extended overlap” modulation requires a higher number of FBSMs, which sacrifices the low device count feature of compact converters. In [22–24], the third-order harmonic current injection (THCI) method redistributes energy among the phases to balance the converter energy, but they require additional hardware to generate the injection current, increasing construction costs. Phase shift modulations are highly attractive for DC-DC converters [25,26], demonstrating robust performance characteristics under phase shift control.

Motivated by the above-mentioned challenges, this paper presents a half-wave phase shift modulation (HPSM) strategy. It combines the phase shift control concept to change the switching moment of the switch. Then, the HPSM strategy was derived to achieve energy balance in hybrid MMCs. This proposed strategy significantly expands the converter’s operation range without adding extra hardware. In this paper, the HPSM strategy is applied to the HAMC topology, which known for its highly compact structure among hybrid MMCs.

The paper is structured as follows: Section 2 introduces the HAMC topology and its operation principles. Section 3 analyzes the energy balance of HAMC and the THCI method. In Section 4, the HPSM strategy is presented to achieve energy balance of the arm with a wide-range modulation ratio. This strategy can reduce operating losses without additional hardware. Section 5 verifies the effectiveness of the topology and modulation strategy through simulations. Furthermore, the performance of the proposed strategy is compared with the THCI method. The scale-down prototype experiment in Section 6 validates the proposed strategy in HAMC. Finally, Section 7 presents the study conclusions.

2. Topology and Operation Principle of HAMC

2.1. Topology Description

Hybrid MMCs combine SM chain links with device series switches, bringing together features of MMC and two-level converter topologies. Hybrid MMCs maintain similar AC output characteristics to MMCs but require fewer SMs, making them a promising area of research topic for lightweight MMC designs. As a most compact hybrid MMC topology, HAMCs integrate the ideas of time-division multiplexing and alternate conduction. This innovative topology significantly reduces the weight and volume of MMCs. The HAMC is illustrated in Figure 1, where each phase is divided into four parts: upper arms, lower arms, multiplexed arms, and AC arms. The upper and lower arms consist of director switches (DSs), which are series-connected insulated gate bipolar transistors (IGBTs). The AC arms contain arm selection switches (SSs), also made up of series IGBTs. The two types of switches interact with each other to change the operation mode and alternate the current paths. The multiplexed arms consist of FBCLs to generate sinusoidal half waves. Without changing the modulation strategy, HAMC only operates in an over-modulation state, SM must choose FBSMs.

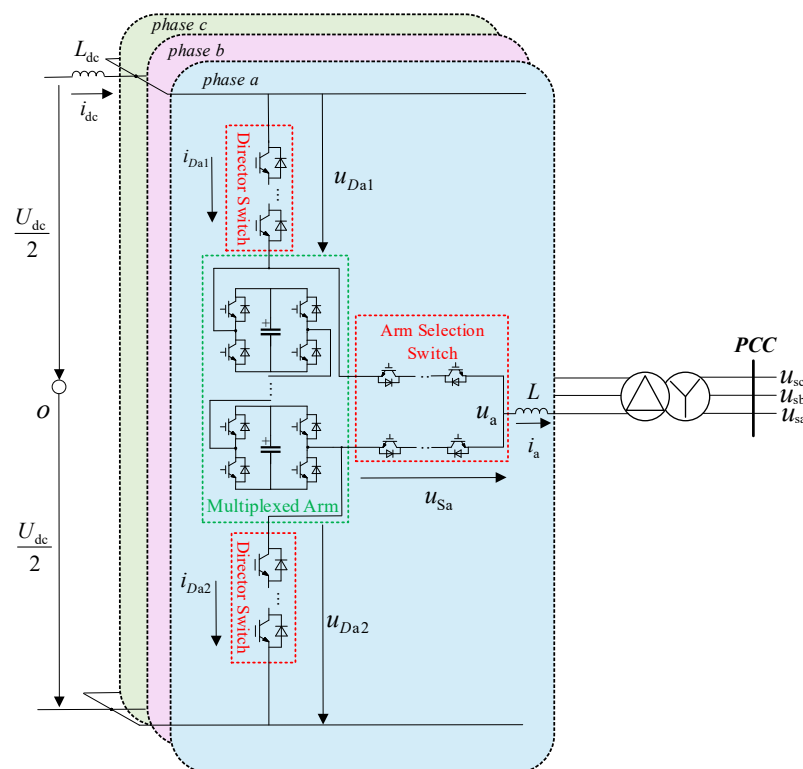


Figure 1. HAMC topology.

The number of SMs per phase in a traditional MMC is $2N$, while the AAMC nearly halves this requirement for the same rated power transmission. Furthermore, the HAM-MMC reduces the number of SMs by a quarter compared to traditional MMCs. Additionally, HAMC requires $0.5N$ SMs per phase specifically for shaping circuits in multiplexed arms, significantly contributing to reducing the number of required SMs.

2.2. Operation Principle

Under normal conditions, the DSs of each phase in an AAMC are switched based on the complementary power frequency of the phase voltage reference wave. In HAM-MMCs, the arm selection switches of each phase are switched according to the number of SMs required by the equivalent upper and lower arms. For the HAMC, the DS and arm selection switches need to cooperate to achieve AC and DC voltage conversion, with

the operation principle being similar to that of the AAMC. Taking phase a as an example, Figure 2 illustrates the HAMC operation. When the AC output voltage reference is positive, the phase unit operates in the upper arm-multiplexing mode, where S_{a2} and D_{a1} are closed, while S_{a1} and D_{a2} are open. Conversely, when the AC output voltage reference is negative, the phase unit operates in the lower arm-multiplexing mode, with S_{a1} and D_{a2} closed, and S_{a2} and D_{a1} open. This means that the signal of S_{a2} matches that of D_{a1} , and the signal of S_{a1} matches that of D_{a2} . These two sets of signals alternately complement each other, with the switching frequency being the power frequency.

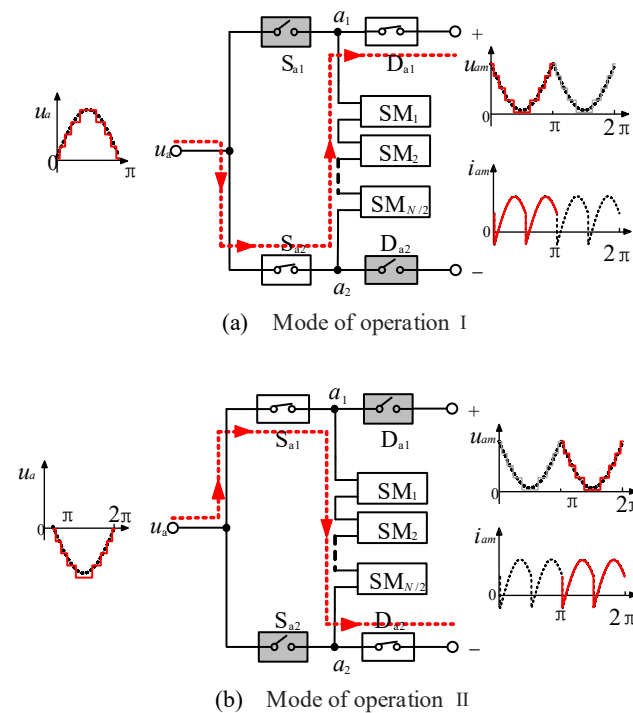


Figure 2. HAMC operation mode of phase a.

The switching function F_a of the phase a operation mode is defined as

$$F_a = \begin{cases} 1, & \text{Upper arm multiplexing mode} \\ 0, & \text{Lower arm multiplexing mode} \end{cases} \quad (1)$$

By controlling the switching of the SM, the voltage and current of the multiplexed upper and lower arms can be analyzed. The multiplexed arm voltage u_{am} and i_{am} current can be represented as (2), where u_a is the AC voltage, and i_a is the output current:

$$\begin{cases} u_{am} = \frac{U_{dc}}{2} + (-2F_a + 1)u_a \\ i_{am} = (2F_a - 1)i_a \end{cases} \quad (2)$$

Figure 3 shows the operation mode switching principle for each phase of HAMC, where the conduction angle of both the arm selection switch and the director switch within one cycle is π , and the switching frequency corresponds to the power frequency. The shaping circuit of the multiplexed arm alternates between two operation modes. Compared to AAMC, the HAMC can reduce the number of SMs by half through the cooperation of the DC upper and lower arm switches and the AC side switches. Each phase of HAMC operates alternately in two arm-multiplexing conduction modes, enabling full-cycle utilization of the shaping circuit.

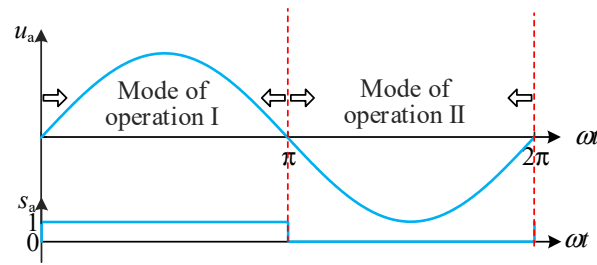


Figure 3. HAMC operation mode switching principle.

3. Energy Balancing Analysis and Control of HAMC

3.1. Energy Balancing Analysis

The exchange of AC and DC power is achieved through the use of submodule capacitors. The submodule capacitor energy balance forms the foundation for the stable operation of hybrid MMC. For HAMC, the submodules are concentrated on the multiplexed arm. Therefore, the energy absorbed and released by the multiplexed arm should be equal in one cycle. This section analyzes the energy balance condition of HAMC. The equations u_a and i_a of the voltage and current of phase a on the AC side can be expressed as follows:

$$\begin{cases} u_a = U_m \sin \omega t \\ i_a = I_m \sin(\omega t - \varphi) \end{cases} \quad (3)$$

where U_m and I_m are the fundamental frequency amplitudes of the AC phase voltage and phase current, respectively. ω is the fundamental angular frequency, and φ is the power factor angle.

According to Equations (2) and (3), the expression of u_{am} and i_{am} in a period can be obtained as follows:

$$\begin{cases} u_{am} = \begin{cases} \frac{U_{dc}}{2}(1 - m \sin \omega t), & 0 < \omega t < \pi \\ \frac{U_{dc}}{2}(1 + m \sin \omega t), & \pi < \omega t < 2\pi \end{cases} \\ i_{am} = \begin{cases} I_m \sin(\omega t - \varphi), & 0 < \omega t < \pi \\ -I_m \sin(\omega t - \varphi), & \pi < \omega t < 2\pi \end{cases} \end{cases} \quad (4)$$

where $m = 2U_m/U_{dc}$ is the voltage modulation ratio. The multiplexing arm should have no energy accumulation in one cycle.

$$\Delta E_{am} = \frac{1}{\omega} \int_0^{2\pi} u_{am} i_{am} d(\omega t) = \frac{U_{dc} I_m}{2\omega} (4 \cos \varphi - \pi m \cos \varphi) = 0 \quad (5)$$

From Equation (5), it can be inferred that HAMC needs to operate in overmodulation state. The voltage modulation ratio of $4/\pi$, which is consistent with AAMC. In this situation, HAMC operates at a “sweet spot” so that FBSMs are required to generate the negative arm voltages in the over-modulation mode in order to ensure the stable operation of HAMC and satisfy the system regulation requirements. It is necessary to introduce additional controls to improve the limited operation range of the converter.

3.2. Energy Balancing Control with Harmonic Injected Method

In order to eliminate the limitation of the sweet spot and achieve wide-range voltage regulation of the converter, in this section, the harmonic injection method is applied to HAMC and analyzed. This method relies on driving third-order harmonic currents through a current path created between the middle point of the DC side and the neutral point of a star transformer on the converter side.

Based on Equation (3), after harmonic injection, the modified voltage and current can be expressed as follows:

$$\begin{cases} u_a(t) = U_m \sin(\omega t) - U_{3m} \sin(3\omega t - \theta_u) \\ i_a(t) = I_m \sin(\omega t - \varphi) - I_{3m} \sin(3\omega t - \theta) \end{cases} \quad (6)$$

According to Equations (4) and (6), the expression of u_{am} and i_{am} in a period can be modified as follows:

$$\begin{cases} u_{am} = \begin{cases} \frac{U_{dc}}{2}(1 - m \sin \omega t) + U_{3m} \sin(3\omega t - \theta_u) = u_{am1}(t) + u_{a3}(t), & 0 < \omega t < \pi \\ \frac{U_{dc}}{2}(1 + m \sin \omega t) - U_{3m} \sin(3\omega t - \theta_u) = u_{am1}(t) - u_{a3}(t), & \pi < \omega t < 2\pi \end{cases} \\ i_{am} = \begin{cases} I_m \sin(\omega t - \varphi) - I_{3m} \sin(3\omega t - \theta) = i_{am1}(t) - i_{a3}(t), & 0 < \omega t < \pi \\ -I_m \sin(\omega t - \varphi) + I_{3m} \sin(3\omega t - \theta) = i_{am1}(t) + i_{a3}(t), & \pi < \omega t < 2\pi \end{cases} \end{cases} \quad (7)$$

Based on Equation (7), the energy accumulated by the modified multiplexed arm in one cycle is

$$\Delta E_{am} = E_{am11} - E_{am13} + E_{am31} - E_{am33} = U_m I_m \left[\frac{2}{m} \cos \varphi - \frac{\pi}{2} \cos \varphi - \frac{2}{3m} \frac{I_{3m}}{I_m} \cos \theta \right]. \quad (8)$$

λ represents the ratio of the third-order harmonic current to the fundamental amplitude:

$$\lambda = \frac{I_{3m}}{I_m}. \quad (9)$$

The third-order harmonic current amplitude ratio λ could be calculated by letting the energy accumulation in (10) equal zero, which is

$$\lambda = 3 \left(1 - \frac{\pi}{4} m \right) \frac{\cos \varphi}{\cos \theta}. \quad (10)$$

To minimize the size of the third-order harmonic component, let $\theta = 0$:

$$\lambda = 3 \left(1 - \frac{\pi}{4} m \right) \cos \varphi. \quad (11)$$

The current reference value for a given third-order harmonic injection device can be derived as

$$i_0(t) = 3i_{am}(t) = 3\lambda I_m \sin(\omega t). \quad (12)$$

This method can ensure the normal operation of the converter when the modulation ratio changes and maintain the overall energy balance of the multiplexed arm. However, there are two significant drawbacks: the need for an additional external current injection device and the potential for excessive harmonic currents to be injected. From Equation (11), it is evident that when the difference between the modulation ratio m and $4/\pi$ is large, the ratio of the third-order harmonic current to the fundamental amplitude λ increases. This can lead to the problem of larger harmonic currents being injected.

4. The HPSM Strategy of HAMC with Wide Operation

In order to achieve a wide range of voltage regulation, this paper presents a shaping modulation with adjustable commutation angle. Thus, the control freedom is increased to realize the energy balance of the arm. The energy balance of the arm controlled by the commutation angle is analyzed as follows.

The phase unit of the HAMC switches its operation mode at the zero-crossing point of the voltage reference wave. By introducing a commutation angle at switching that lags the

AC-side zero-crossing point, the energy balance problem can be eliminated. Taking phase a as an example, as shown in Figure 4, the expression of one period is

$$F_a = \begin{cases} 1 & \eta < \omega t < \pi + \eta \\ 0 & \pi + \eta < \omega t < 2\pi + \eta \end{cases} \quad (13)$$

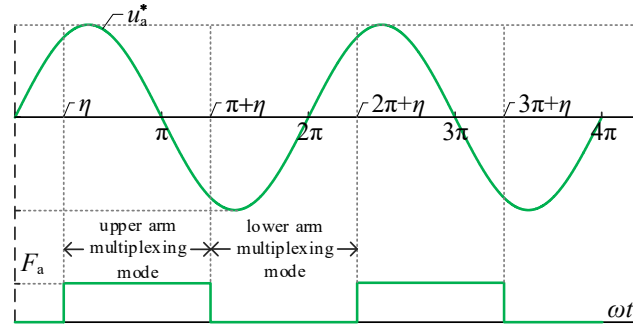


Figure 4. Principle of half-wave phase shift modulation.

From F_a and (2), it can be concluded that

$$\begin{cases} u_{am} = \begin{cases} \frac{U_{dc}}{2} + (1 - m \sin \omega t), & \eta < \omega t < \pi + \eta \\ \frac{U_{dc}}{2} + (1 + \sin \omega t), & \pi + \eta < \omega t < 2\pi + \eta \end{cases} \\ i_{am} = \begin{cases} I_m \sin(\omega t - \varphi), & \eta < \omega t < \pi + \eta \\ -I_m \sin(\omega t - \varphi), & \pi + \eta < \omega t < 2\pi + \eta \end{cases} \end{cases} \quad (14)$$

According to Equation (14), the energy accumulated by the multiplexed arm in one cycle is

$$\Delta E_{am} = \int_{\eta}^{2\pi+\eta} u_{am} i_{am} d(\omega t) = U_{dc} I_m (4 \cos(\eta - \varphi) - \pi m \cos \varphi). \quad (15)$$

In order to balance the capacitor voltage, the energy accumulation should be equal to 0. The relationship between m , φ , and η can be obtained as follows:

$$\eta = \varphi \pm \arccos\left(\frac{\pi}{4} m \cos \varphi\right). \quad (16)$$

When $|\eta|$ is greater than $\pi/2$, the maximum value of the multiplexed arm voltage u_{am_max} is only related to m and can reach a maximum of $(1 + 4/\pi) U_{dc}/2$. The larger the u_{am_max} , the greater the number of SMs required for the multiplexed arm. To minimize the increased device cost owing to the use of HPSM, only the commutation angle with the smaller absolute value given by (16) is considered.

$$\eta = \begin{cases} \varphi + \arccos\left(\frac{\pi}{4} m \cos \varphi\right) & -\pi < \varphi < 0 \\ \varphi - \arccos\left(\frac{\pi}{4} m \cos \varphi\right) & 0 < \varphi < \pi \end{cases} \quad (17)$$

According to (17), η is a function of φ of period π . Taking $\varphi \in [0, \pi]$ as an example, the change of η with φ and m is shown in Figure 5. When m remains constant, η increases monotonically with φ on $[0, \pi]$. When $\varphi \in [0, \pi/2]$, η increases monotonically with m on $[0, 4/\pi]$; when $\varphi \in [\pi/2, \pi]$, η decreases monotonically with m on $[0, 4/\pi]$. The minimum value of η is $-\pi/2$ when $m = 0$ and $\varphi = 0$, and the maximum value is $\pi/2$ when $m = 0$ and $\varphi = \pi$. The overall range of values is $[-\pi/2, \pi/2]$.

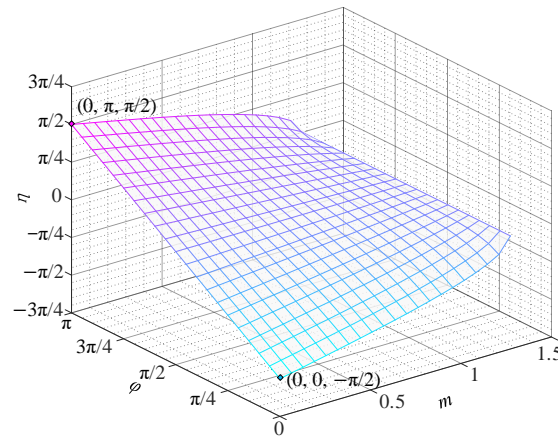


Figure 5. Variation in the commutation angle with the power factor angle and the modulation ratio.

Based on the above analysis, the expression for the maximum value of the voltage u_{am_max} on the multiplexed arm can be obtained as

$$u_{am_max} = \frac{U_{dc}}{2} (1 + m \sin|\eta|). \tag{18}$$

The changes of u_{am_max} with φ and m can be obtained from Equations (17) and (18), as shown in Figure 6. When $m = 0.9$, $\varphi = 0$ or π , the maximum value of u_{am_max} is $0.82U_{dc}$. Therefore, the multiplex arm requires cascading of $0.82N$ SMs, and the rated voltage of the SM capacitors is U_{cn} .

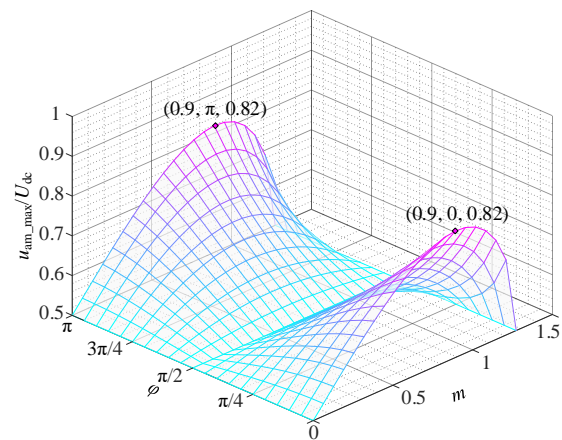


Figure 6. Variation in the maximal multiplexed arm voltage with power factor angle and modulation ratio.

With HPSM, the voltage modulation ratio range of HAMC is changed from $4/\pi$ to $[0, 4/\pi]$, thereby expanding the regulating range of the converter. A half-bridge submodule (HBSM) can be used when the voltage modulation ratio is $[0, 1]$. It can lead to a 23.7% reduction in the number of IGBTs, while further reducing operating losses.

5. Simulation Results of HAMC

In order to verify the effectiveness of the proposed control strategy in HAMC. This paper builds a simulation model of HAMC using Matlab/Simulink in Figure 7. By employing the HPSM strategy, the voltage modulation ratio m is adjusted to 0.9. The specific simulation parameters are shown in Table 1.

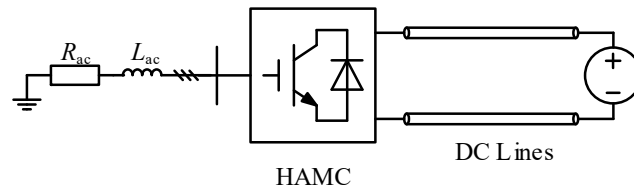


Figure 7. Structure of the simulated systems.

Table 1. Parameters of Simulation System.

Parameters	Symbol	Values
Dc-link voltage	U_{dc}	20 kV
Number of SMs per phase	N	10
SM capacitor	C	3 mF
SM capacitor reference voltage	U_c	1 kV
Load resistance	R_{load}	84 Ω
Power factor	$\cos\varphi$	0.95

5.1. Steady State Characteristics

As illustrated in Figure 8, the AC output waveforms, DC current, and SM capacitor voltage are generated by HPSM-HAMC in this simulation. Under nearest level modulation (NLM), the HAMC outputs a 19-level phase voltage step wave. The waveforms of phase currents and phase voltages in the AC side exhibit high quality with very low amplitude harmonics. The HAMC has a low submodule capacitor voltage ripple with a capacitor voltage peak-to-peak of 42.8 V. Therefore, with a 59% reduction in sub-module usage, the HAMC with HPSM can have better ac voltage output capability and lower SM capacitance size requirements.

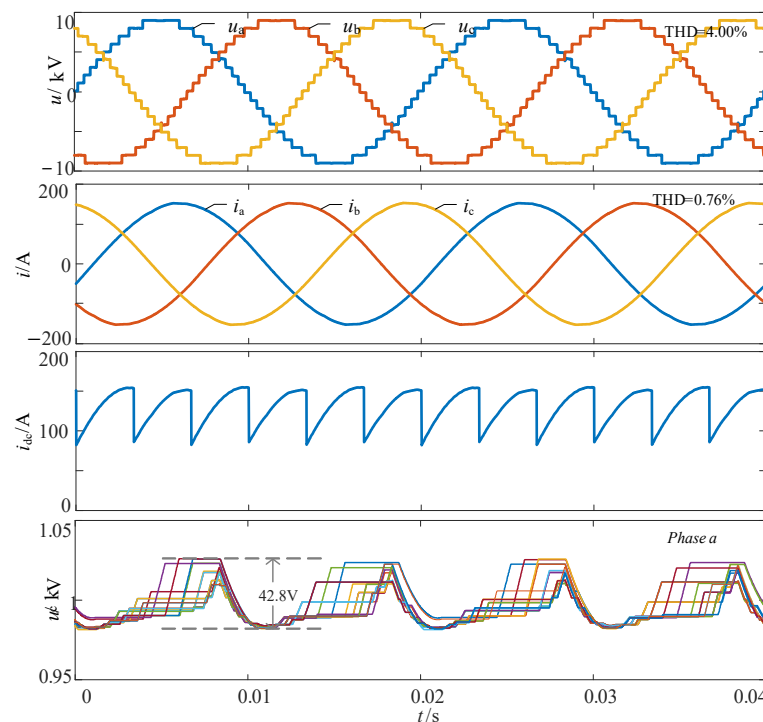


Figure 8. Phase and sub-module voltages for HAMC.

The AC load in the simulation model built in this paper is an inductive load and $\cos\varphi = 0.95$. The commutation angle η of the HPSM is obtained as -29.6° when $m = 0.9$. Therefore, the operating mode switching point of the HAMC phase unit will be 29.6° ahead of the

phase voltage reference wave over zero. The voltage and current simulation results for the HAMC multiplexed arm are shown in Figure 9, and the simulation results for the director switch voltage and the arm selection switch voltage. From Figure 9a, it can be seen that the upper arm conduction multiplexing mode is triggered “in advance”. The multiplexed arm no longer outputs a negative voltage, and its maximum output voltage is related to the commutation angle η . From Figure 9b,c, S_{a1} and D_{a2} are simultaneously switching, S_{a2} and D_{a1} are simultaneously switching, the switch in the conduction state provides a path for the dc current, and the switch in the turn-off state withstands reversed voltage.

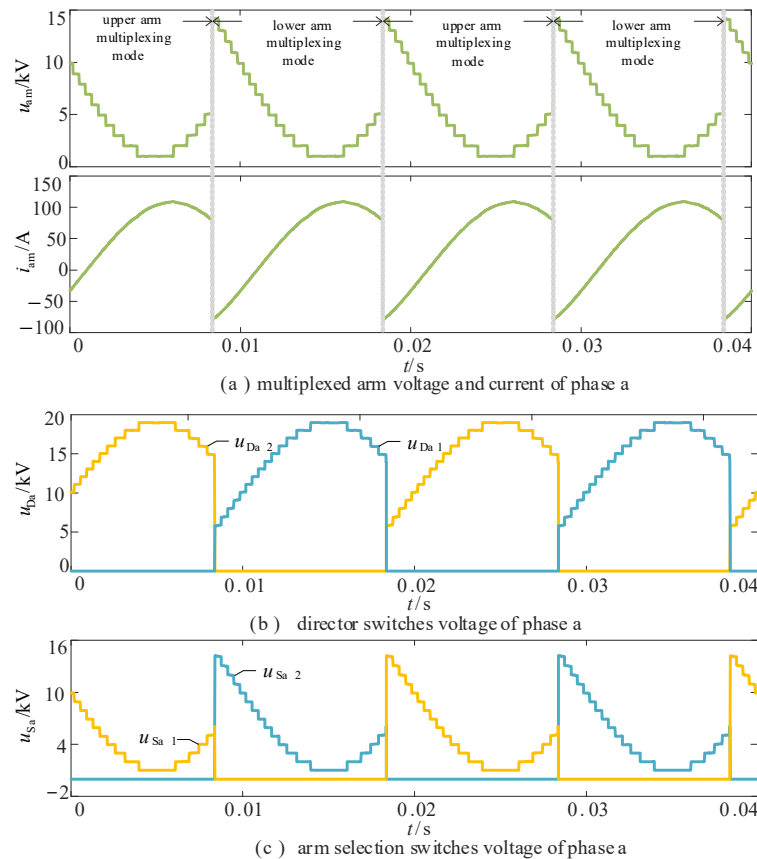


Figure 9. Simulation results of the multiplexed arm and switches of HAMC.

5.2. Dynamic Characteristics

To further verify the feasibility of the strategy proposed in this paper, the dynamic simulation results of HAMC are given in Figure 10. The initial working condition is the same as that of Figure 8, the simulation conditions are set as follows:

- (1) The voltage modulation ratio m is set to drop by 20% at 0.2 s, and the voltage modulation ratio m is recovered at 0.6 s.
- (2) The load resistance is set to increase by 25% at 1.0 s, and the load resistance is restored to its original value at 1.4 s.

From Figure 10a,b, the ac current and output power change steadily when the voltage modulation ratio m and load resistance suddenly change, and there is no significant surge current or power. As shown in Figure 10c, the multiplexed arm sub-module capacitor voltage still has good equalization characteristics when the disturbance occurs and always stays around the rated value of 1 kV. Therefore, the strategy proposed in this paper makes the converter smoothly transition to a new stable operation state with good dynamic characteristics when the working condition changes.

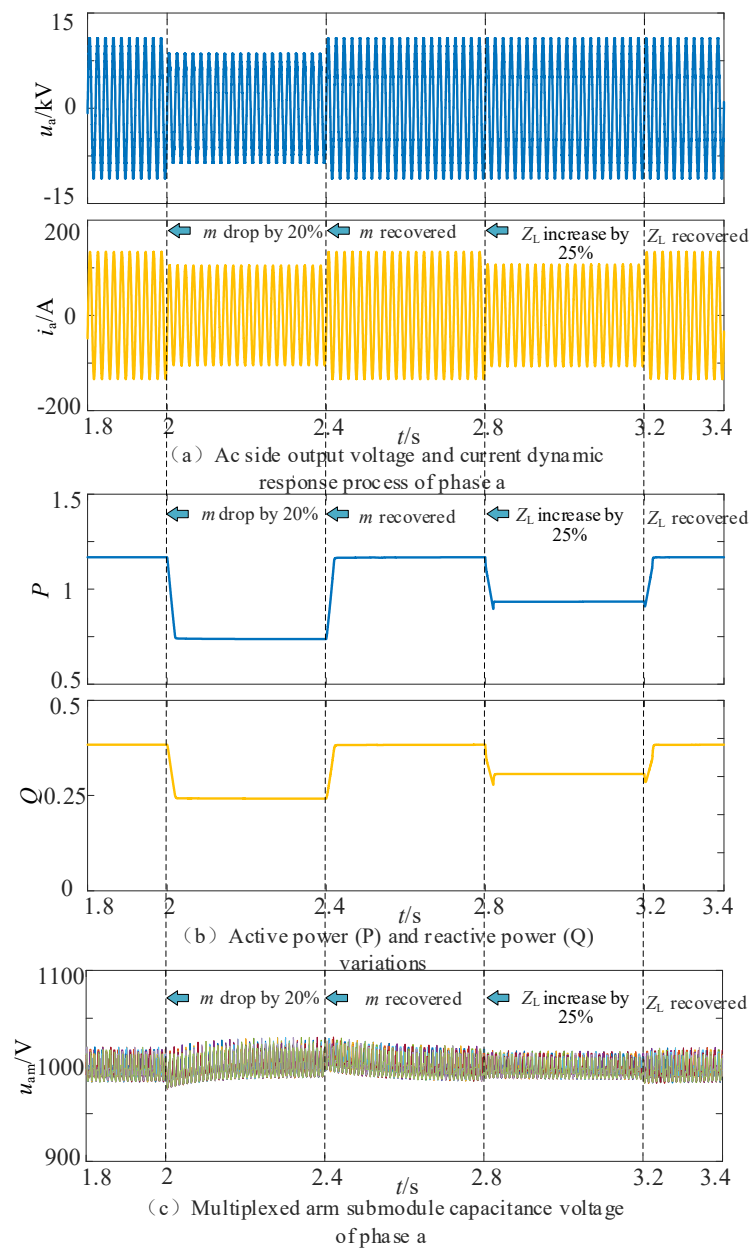


Figure 10. Dynamic simulation results of HAMC.

5.3. Performance Comparison with THCI

The operating loss is an important standard to evaluate the economy and efficiency of the converter. For the HVDC converter, the operating loss consists of the conduction loss and switching loss of IGBTs and diodes. The detailed calculation method can be referred to in [27]. Under the above simulation conditions, HPSM is compared with THCI in this section. The resulting operating loss of the converter is shown in Table 2.

For the control strategy proposed in this paper, the switching loss of the DS and arm SS is about 1% of the conduction loss they generate, which is relatively small. In contrast to the third-order harmonic injection method, the increased harmonic current is continuously injected into the arm, greatly increasing the device loss. Then, there is a comparison of the performance of the two control strategies. The comparison is analyzed in terms of modulation range, total harmonic distortion, complexity, and operating loss.

Table 2. HAMC operating loss for two control strategies.

Control Strategy	Device	Conduction Loss/%	Switching Loss/%	Operating Loss/%
HPSM	SM	0.7980	0.0594	0.8574
	DS	0.4232	0.0037	0.4269
	SS	0.2143	0.0041	0.2184
	Total	1.4265	0.0672	1.4937
THCI	SM	1.0016	0.0430	1.0446
	DS	0.6496	0.0132	0.6628
	SS	0.5770	0.0102	0.5872
	Total	2.2282	0.0288	2.2946

Table 3 provides the output phase voltages of HAMC with THCI and HPSM strategies at a constant power factor ($\cos \varphi = 0.95$) across different modulation ratios. This further validates the wide-range voltage regulation capability of the two strategies. According to the analysis in (12), λ increases with the modulation range for THCI, requiring the injection of a large amount of additional current. Conversely, HPSM reduces cost by eliminating the need for additional devices while maintaining good voltage output capability.

Table 3. Comparison of control variables between HPSM and THCI under different voltage modulation ratios.

m	$\eta/^\circ$	λ	Phase Voltage THD/%	
			THCI	HPSM
0.7	−40.3	1.284	5.79	5.20
0.8	−35.2	1.060	5.15	4.59
0.9	−29.60	0.836	4.51	4.00
1.0	−23.50	0.623	4.08	3.65
1.1	−16.65	0.389	3.97	3.23

As seen in Table 4, both control strategies are effective in expanding the operation range of the converter. However, the THCI strategy has a high operating loss and requires additional injection devices. While HPSM is more complex than THCI, its overall performance is significantly better. Based on the above table, the following conclusions can be drawn. HPSM, compared to THCI at the same voltage modulation ratio, exhibits lower THD, indicating better AC harmonic characteristics and higher waveform quality. The lower operating loss implies improved economic, efficiency, and greater practical engineering prospects.

Table 4. Performance for two control strategies.

Symbol	HPSM	THCI
m	$[0, 4/\pi]$	$[0, 4/\pi]$
THD	Low	Low
Control complexity	High	Low
Operating loss	Low	High

6. Experimental Validations of HAMC

In order to verify the effectiveness of the proposed strategy in HAMC, a scaled-down HAMC is built, as shown in Figure 11. The experimental parameters are listed in Table 5. The prototype control system is implemented using a real-time digital controller (RTU-BOX-204), where the NLM is implemented for the HAMC.

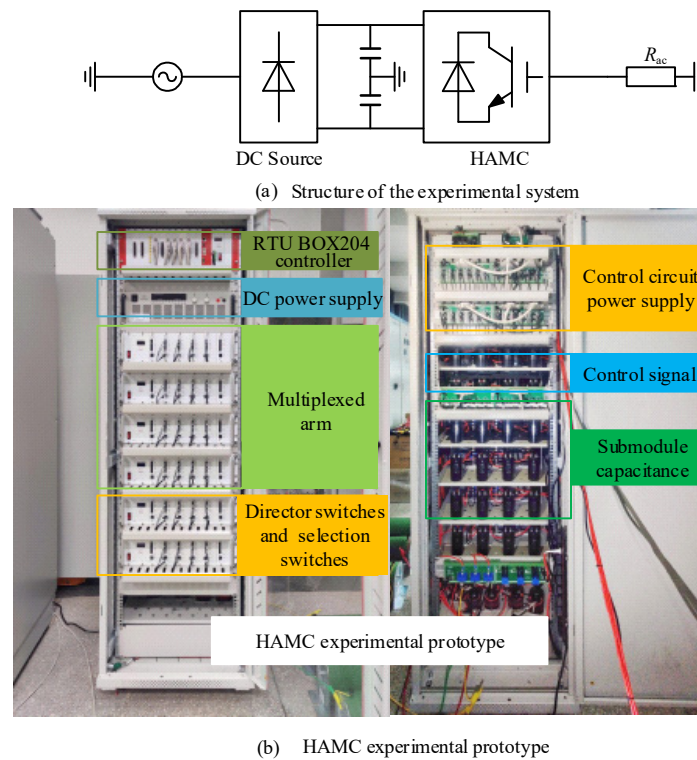


Figure 11. Experimental prototype of HAMC.

Table 5. Experimental parameters of the HAMC.

Parameters	Symbol	Values
Rated active power	P_N	2.75 kW
Rated frequency	f_N	50 Hz
Control circle	T_c	0.05 ms
DC bus voltage	U_{dc}	300 V
Number of SMs per phase	N	6
SM capacitor	C	4.34 mF
SM capacitor reference voltage	U_c	1 kV
AC Load resistance	R_{load}	10 Ω
Voltage modulation ratio	m	0.9

As for the steady-state experiment, the operation results are shown in Figures 12 and 13. It can be seen from Figure 12 that the AC voltage and ac current are in phase a. As shown in Figure 12, the HAMC with six submodules per phase can output 13-level AC voltage, the experimental waveforms of HAMC's output AC voltage and current have low harmonic distortion rate, indicating good waveform quality. This demonstrates the feasibility of the proposed strategy. The voltage and current of the HAMC multiplexed arm are shown in Figure 13a, and it can be seen that the multiplexed arm no longer outputs a negative voltage, and its maximum output voltage is related to the commutation angle η . The voltage and current of the multiplex arm still fluctuate at double frequency. The submodule capacitance voltage can also remain stable, with small charge and discharge fluctuations within each cycle. This fully demonstrates the effectiveness of the energy balance strategy.

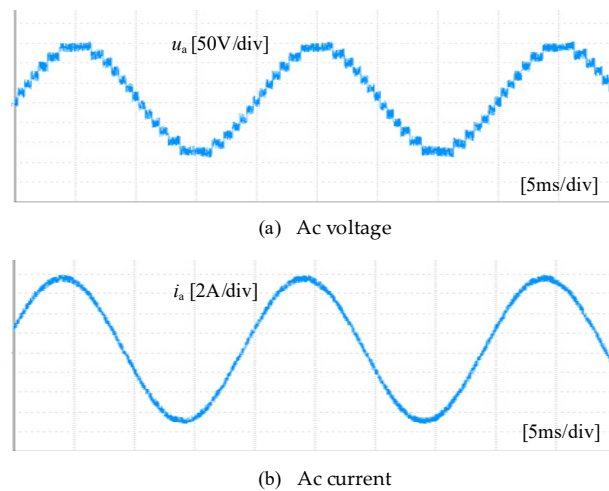


Figure 12. Experimental Ac voltage and current waveforms of HAMC.

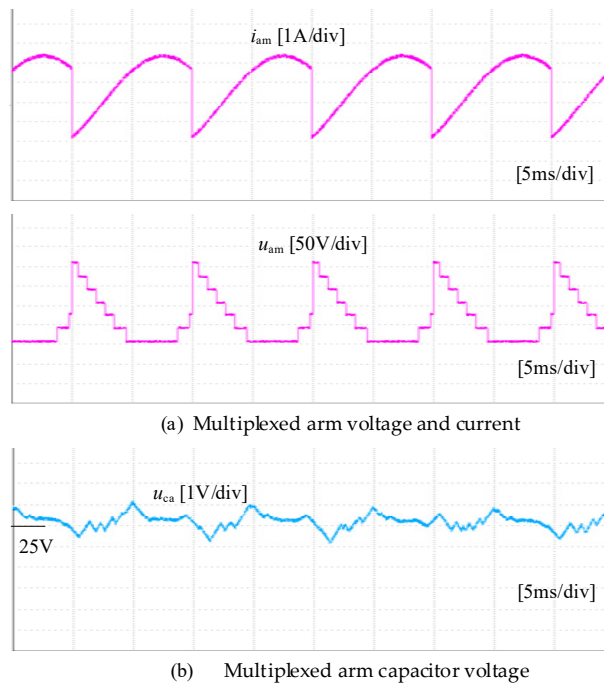


Figure 13. Multiplexed arm voltage, current, and capacitor voltage of phase a.

7. Conclusions

Hybrid MMC topologies garnered significant attention as promising MMC alternatives for HVDC applications, though they face energy balancing issues. Based on phase shift control, an energy balancing method for hybrid MMCs is proposed in this paper to expand the operation range. To achieve full-range operation of HAMC, the commutation angle is introduced as a controllable variable. The HPSM strategy controls the commutation angle to match the voltage modulation ratio with the power factor under different operation conditions. The proposed strategy is verified by building simulation models and scale-down experimental prototypes. The following conclusions were drawn:

- (1) Taking the compact HAMC topology as an example, the proposed HPSM strategy can ensure energy balance of the arm with a wide-range modulation ratio and enable the converter with voltage regulation capability.
- (2) With the HPSM strategy, simulated results show that the HAMC can achieve similar power quality, full-range voltage regulation, capacitor voltage, and dynamic response with the conventional MMC.

- (3) Performance comparison illustrated that the HPSM reduces operating loss by 50% compared to the THCI method, while no additional hardware is required.

Author Contributions: J.M. and Y.P. proposed the research topic and designed the model. Z.S. and Y.W. were responsible for guidance, giving constructive suggestions, and revising the paper. Y.G. and Q.N. performed the simulations and analyzed the data. Y.Y. and J.L. improved the manuscript and corrected spelling and grammar mistakes. All authors have read and agreed to the published version of the manuscript.

Funding: This work was supported by the Science and Technology Project of State Grid Zhejiang Electric Power Co., Ltd. (5211DS24000D).

Data Availability Statement: The data presented in this study are available on request from the corresponding author.

Conflicts of Interest: Authors Junchao Ma, Yan Peng, Ying Yang and Jianing Liu were employed by the company State Grid Zhejiang Electrical Power Co., Ltd. Research Institute. Authors Yilei Gu and Qiulong Ni were employed by the company State Grid Zhejiang Electrical Power Co., Ltd. The remaining authors declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

Nomenclature

HAMC	Half-wave alternating multilevel converter
SM	Submodule
M	Voltage modulation ratio
F_a	Switching function
u_{xm}	Multiplexed arm voltage ($x = a, b, c$) (V)
i_{xm}	Multiplexed arm current ($x = a, b, c$) (A)
u_x	AC phase voltage ($x = a, b, c$) (V)
i_x	AC phase current ($x = a, b, c$) (A)
U_{dc}	DC voltage (V)
U_m	Fundamental frequency amplitudes of the AC phase voltage (V)
I_m	Fundamental frequency amplitudes of the AC phase current (A)
ΔE_{am}	Multiplexing arm energy accumulation
φ	Power factor angle
U_{3m}	Triple frequency amplitudes of the AC phase voltage (V)
I_{3m}	Triple frequency amplitudes of the AC phase current (A)
θ	Third-order harmonic phase
λ	Ratio of the third-order harmonic current to the fundamental amplitude
i_0	Third-order harmonic injection current (A)
η	Phase shift angle

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