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**Abstract:** Offshore wind farms connected in series, with each wind turbine connected in series with one another, enhance the coupling between them. Significant differences in wind speeds between neighboring DC wind turbines (DCWTs) might result in a substantial disparity in the output voltage, hence posing a risk of overvoltage. Nevertheless, implementing voltage-limiting configurations for DCWTs might lead to the dissipation of wind energy, thereby diminishing the wind farm's capacity to deliver electricity. This work introduces a half-bridge voltage balancing circuit (HVBC) topology as a solution to the issue of DCWT output voltage changes affecting the stable operation of wind farms. The proposed HVBC topology is designed specifically for large-capacity series-connected all-DC wind farms where wind speed variations occur. This design achieves power decoupling for seriesconnected all-DC wind farms by providing current compensation to the series-connected DCWTs. A control strategy is devised by examining the decoupling principle and operational characteristics of the HVBC. A 60 kV/48 MW tandem-type all-DC wind farm model consisting of six DCWTs in series is built in Matlab/Simulink. The model is then simulated to evaluate its performance under conditions of unequal wind speed, rapid changes in wind speed, and wind turbine failure shutdown. This research verifies the feasibility of the HVBC topology and improves the stability of the series-type all-DC wind farm.

**Keywords:** series-connected all-DC wind farm; half-bridge voltage balancing circuit; current compensation; power decoupling; modular DC/DC converter

#### **1. Introduction**

Wind energy is a highly clean and eco-friendly form of energy. Due to the growing scarcity of land resources, the advancement of offshore wind power has emerged as a crucial route for the future development of wind power  $[1-3]$  $[1-3]$ . Aggressively advancing the development of offshore wind power is a crucial step towards achieving the "dual-carbon" objective and transitioning to renewable energy sources. The Global Wind Energy Council's (GWEC) Global Wind Power Report 2024 states that a total of 117 GW of new wind power was installed worldwide in 2023, resulting in a cumulative installed capacity of 1 TW [\[4\]](#page-19-2). A total of 10.8 GW of offshore wind power was installed, contributing to a cumulative installed capacity of 75.2 GW [\[4\]](#page-19-2). The advancement of offshore wind power towards deep and distant seas with increased capacity has rendered the traditional method of AC energy pooling and transmission ineffective due to issues such as reactive current and overvoltage. Consequently, the construction of all-DC wind farms, utilizing DC pooling and transmission, has emerged as the primary direction for the future development of offshore wind power [\[5–](#page-19-3)[7\]](#page-19-4).

Wind power systems in the DC domain can be classified into parallel and series types based on the manner of pooling energy [\[8–](#page-19-5)[10\]](#page-19-6). Series-connected all-DC wind power



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systems raise the voltage level to the transmission voltage by connecting DCWTs in series with each other [\[11\]](#page-19-7). However, the arrangement of individual DCWTs in a wind farm in a series of connections intensifies the coupling between them, resulting in the degradation of the autonomous control of each DCWT [\[12,](#page-19-8)[13\]](#page-19-9). The decoupling of control for seriesconnected all-DC wind farms is highly important for the coordinated control of DCWTs and the establishment of voltage restrictions for DCWTs to avoid overvoltage occurrences [\[14\]](#page-19-10). There are two main approaches to solving the DCWT output voltage balance in seriesconnected all-DC wind farms.

The first method is by limiting the voltage or limiting the power [\[15](#page-19-11)[,16\]](#page-19-12). When the voltage across the DCWTs reaches a limit value, it is not increased, or when the output power of the DCWTs reaches a limit value, it is not increased. In [\[17\]](#page-19-13), voltage fluctuations are suppressed by adding energy storage at the DC bus to absorb high-frequency power fluctuations. In [\[18\]](#page-19-14), the range of DC bus voltage fluctuations is limited by transient rotor power feedback. However, after limiting the DC bus voltage, the current saturation characteristic caused during grid faults leads to transient instability [\[19\]](#page-19-15). The authors of [\[20](#page-19-16)[,21\]](#page-19-17) in the literature initially analyzed the coupling characteristics between DC wind turbines in series-type full-DC wind farms. They also explored the control methods for wind turbines in series-type DCWTs operating under voltage-limited conditions. This method, though, ensures that the DCWT is not at risk of overvoltage and avoids damage to the DCWT under unstable wind speed conditions. However, this method must give up part of the wind energy when limiting the voltage or limiting the power, resulting in part of the wind energy being wasted and also limiting the power generation capacity of the DCWT. The literature in [\[22\]](#page-19-18) describes how the DC bus voltage and output power of each DCWT are regulated by adjusting the reference unit's mode to ensure equal values for each DCWT. This approach potentially mitigates the impact of the interconnection properties of DC wind farms that are connected in series. However, its effectiveness strongly depends on the operational condition of the reference unit, and the stability of the wind farm operation is compromised. In their studies, the authors of [\[23,](#page-19-19)[24\]](#page-19-20) suggested a strategy for controlling the speed of a series-connected DCWT to mitigate wind abandonment. This strategy involves storing and releasing energy through the rotor of the wind turbine. However, a limitation arises when the wind turbine reaches its maximum speed, as the rotor is unable to store any more energy, failing the variable speed control. The authors of [\[25\]](#page-19-21) proposed the use of a DC collector to mitigate the impact of power fluctuations from wind turbines on their output voltage. However, the stabilizing effect of the wind turbine outlet voltage is not evident. While the solutions effectively mitigate the risk of series-connected DCWT overvoltage through voltage-limiting control, they significantly decrease the usage of wind energy. In addition, with the increase in the installed capacity of wind farms, the impact of common mode voltage on wind turbines cannot be ignored [\[26\]](#page-19-22).

The second method is to provide a compensation current to the series-type DCWT by controlling the shunt circuit [\[27\]](#page-19-23), which realizes the maximum power-tracking of the wind turbine in the case of wind speed inconsistency and ensures voltage balance at the machine end. However, this scheme is difficult to adapt to the direction of the large-scale and large-capacity development of offshore wind farms due to the limitation of the voltage stress of the switching devices in the shunt circuit. In their study, the authors of [\[27\]](#page-19-23) introduced a novel configuration for connecting multiple DC wind farms in series. This configuration allows for the efficient tracking of maximum power from wind turbines, even in situations where wind speeds are not consistent. Additionally, it ensures that the voltage at the output of the wind turbines remains balanced by managing the shunt circuits to compensate for any current variations. Nevertheless, this solution is difficult to adapt to the development direction of large-scale and large-capacity offshore wind farms, and new solutions need to be researched.

This research presents a solution to the above issues by introducing a half-bridge voltage balancing circuit (HVBC) topology and its corresponding control method for high-capacity series-connected all-DC wind farms. Additionally, the working principle of this topology is thoroughly analyzed. The design of an all-DC wind farm, capable of operating reliably under varying wind speeds, has been completed based on this topology. Furthermore, the viability of the half-bridge voltage balancing circuit has been confirmed by simulation.

The rest of this article is structured as follows. Section [2](#page-2-0) describes the topology of HVBC and its role in series-type all-DC wind farms. Section [3](#page-5-0) analyzes the working principle of HVBC in terms of its operating status in different situations. Section [4](#page-8-0) describes the control strategy of an HVBC-based series-type full-DC wind farm. Section [5](#page-12-0) performs a simulation and discusses the simulation results. Section [6](#page-18-0) concludes the paper.

#### <span id="page-2-0"></span>**2. Design Solutions for All-DC Wind Farms Connected in Series**

*2.1. Series-Connected All-DC Wind Farm Topology Based on HVBC*

The conventional offshore wind farm is connected in series and operates on DC. It comprises DCWTs directly connected in series. Owing to the nature of the series circuit, the current flowing out of each DCWT connected in series is the same, resulting in a significant relationship between the output voltage and output power of the DCWT. The output power of each DCWT is mostly determined by wind speed. When the wind speed is equal, or the difference in wind speed is minimal, each wind turbine can achieve the same or similar DCWT output power by adjusting the pitch angle. This allows the export voltage of each DCWT to reach the same or a similar level, ensuring that the series-type all-DC wind farms operate smoothly. Nevertheless, if the wind speed discrepancy in a series-connected all-DC wind farm is significant, the DCWTs cannot achieve the desired similarity in outlet voltage by altering their pitch angle. Under these circumstances, the voltage exerted on the DCWTs will be greater when wind speeds are higher than when wind speeds are lower. This increased voltage can potentially result in DCWT failures.

The relationship between the output power and output voltage of a traditional seriestype all-DC wind farm with *n* DCWTs can be expressed as

$$
i_{\rm DC} = \frac{P_1}{U_{\rm wt1}} = \frac{P_2}{U_{\rm wt2}} = \dots = \frac{P_{n-1}}{U_{\rm wt(n-1)}} = \frac{P_n}{U_{\rm wtn}}
$$
(1)

where  $i_{DC}$  is the output current of the series-connected all-DC wind farm, and  $P_i$  and  $U_{wt}$ are the output power and output voltage of the *i* th DCWT, respectively.

Equation (1) demonstrates that the output power of each DCWT in the series-type all-DC wind farm is exactly proportional to the output voltage. Therefore, the *U*wt*<sup>i</sup>* value of the *i* th DCWT can be represented as

$$
U_{\text{wt}i} = \frac{P_i}{\sum\limits_{i=1}^{n} P_i} U_{\text{DC}}
$$
 (2)

where  $U_{DC}$  is the output voltage of the all-DC wind farm, the value of which is controlled by the onshore converter station. Consequently, the output voltage of the DCWT depends on its power level.

The output power of a wind turbine directly correlates to the wind speed at its location under the management of maximum power tracking. The variation in wind velocity at the precise location of each wind turbine inside a wind farm results in a disparity in the generated power of each turbine. When there is a significant power difference, the difference in the output voltage of the wind turbines increases accordingly. This can result in overvoltage issues for wind turbines operating at higher wind speeds, potentially causing damage and posing a serious threat to the safe and stable operation of wind farms. To address overvoltage in wind turbines resulting from wind speed variations, current research has primarily focused on implementing voltage-limiting control for wind turbines connected in series. This approach enhances the safety of wind turbine operation but also results in significant wind energy wastage.

This study presents a solution to the overvoltage issue in series-connected DC wind turbines caused by varying wind speeds. The proposed solution is a half-bridge voltagebalancing circuit for large-capacity series-connected all-DC wind farms. This topology achieves power decoupling for all-DC wind farm series-connected configurations at the achieves power decoupling for all-DC wind farm series-connected configurations at the action of point accompany for an 2 C with them series connected comparations at the DCWT port. It further stabilizes and boosts voltage at the output of the HVBC by connecting a DC/DC module. Figu[re](#page-3-0) 1 displays the new wind farm topology, a series-type configuration using HVBC. figuration using HVBC.

<span id="page-3-0"></span>

**Figure 1.** Series-connected all-DC wind farm topology based on HVBC. **Figure 1.** Series-connected all-DC wind farm topology based on HVBC.

As shown in Figure 1, if we consider a series-connected wind farm consisting of *n* DCWTs, there should be *n* − 1 HVBCs. Additionally, the corresponding modular DC/DC system should have *n* − 1 DC/DC modules. Each HVBC contains four Insulated Gate Bipolar Transistors (IGBTs) with anti-parallel diodes. The input terminal voltage,  $U_{ri}$ , of each DC/DC module is equal to the output terminal voltage of the HVBC connected in parallel with it, which is double the DCWT's *U*<sub>wti</sub>. The grid-side inverter controls the parallel with it, which is double the DCWT's *U*<sub>wti</sub>. The grid-side inverter controls the As shown in Figure [1,](#page-3-0) if we consider a series-connected wind farm consisting of *n* output terminal voltage,  $U_{DC}$ , of the modular DC/DC converter.

and it, which is doubled to the distribution of the grid-side of two adjacent DCWTs to convert<br>An HVBC is linked in parallel to the output ports of two adjacent DCWTs to convert the voltage change resulting from the difference in wind speed into a change in output current. This is achieved by creating a current pathway for the two surrounding DCWTs. This adjusts the current compensation of the DCWT, ensuring that the current magnitude  $\frac{1}{100}$ of each DCWT in the series-connected an-DC wind farm remains dianceted by the other<br>DCWTs when maximum power tracking is attained. This decouples the power in seriesconnected all-DC wind farms, allowing series-connected wind turbines to attain their highest power production while maintaining balanced voltage conditions. The output of of each DCWT in the series-connected all-DC wind farm remains unaffected by the other

each HVBC is linked in parallel with a DC/DC module, including two dual active bridge (DAB) DC/DC converters [\[28,](#page-20-0)[29\]](#page-20-1) connected in series. The inputs of each DC/DC module are separate, and the outputs are connected in series to create a novel modular DC/DC are separate, and the outputs are connected in series to create a nover modular DC/DC<br>system. The modular DC/DC converter primarily ensures voltage equilibrium and voltage amplification at the output of the HVBC while also enhancing fault isolation in all-DC wind farms that are connected in series.

#### *2.2. Principle of Power Decoupling for Series-Connected All-DC Wind Farms Based on HVBC 2.2. Principle of Power Decoupling for Series-Connected All-DC Wind Farms Based on HVBC*

As an example, Figure [2](#page-4-0) illustrates the analysis of the HVBC decoupling concept by As an example, Figure 2 illustrates the analysis of the HVBC decoupling concept by considering two DCWTs with identical parameters connected in series. considering two DCWTs with identical parameters connected in series.

<span id="page-4-0"></span>

**Figure 2.** Topology of two DCWTs linked in series based on HVBC. **Figure 2.** Topology of two DCWTs linked in series based on HVBC.

As per the design requirements, the output terminal voltage of each straight DCWT a series-type all-DC wind farm should be identical, i.e.,  $\frac{1}{\sqrt{1}}$ As per the design requirements, the output terminal voltage of each straight DCWT in

$$
U_{\text{wt1}} = U_{\text{wt2}} = \frac{U_{\text{r}}}{2} = \frac{P_{1}}{I_{\text{wt1}}} = \frac{P_{2}}{I_{\text{wt2}}}
$$
(3)

 $\overline{18}$   $\overline{18}$ of DCWT1 and DCWT2, respectively. where *U*<sup>r</sup> is the output terminal voltage of the HVBC, and *P*<sup>1</sup> and *P*<sup>2</sup> are the output power

The relationship between the output current of the DCWT and the compensating current *I<sub>∆</sub> of the HVBC in Figure 2 is determined by the Kirchhoff's Current Law (KCL).* 

$$
I_{\Delta} = I_{\text{wt1}} - I_{\text{wt2}} \tag{4}
$$

The expression for the compensating current  $I_\Delta$  can be derived from Equations (3) and (4).

$$
I_{\Delta} = \frac{P_1}{U_{\text{wt1}}} - \frac{P_2}{U_{\text{wt2}}} = \frac{1}{U_{\text{r}}/2} (P_1 - P_2) \tag{5}
$$

<sup>Δ</sup> =−= − 1 2 <sup>1</sup> ( ) *P P I PP* assumed to remain constant. In other words, the expression 1/(*U*r/2) can be represented by 1 2 2 2 2 2 assumed to remain constant. In other words, the expression 1/(U<sub>r</sub>/2) can be represented by<br>a constant value, *A*. The compensating current *I*∆ can be expressed as the power difference The DC<sub>/</sub> DC<sub>/</sub> DC<sub>/</sub> DC<sub>/</sub> D<sub>C</sub>/DC<sub>/</sub> at the output of the output of the MVBC, which is the output of the HVBC, which is the material of the MVBC, which is the material of the MVBC, which is the material of the MVBC, whic The DC/DC module controls the voltage,  $U_r$ , at the output of the HVBC, which is ∆*P* between two adjacent DCWTs:

$$
I_{\Delta} = A(P_1 - P_2) = A \cdot \Delta P \tag{6}
$$

 $E$ quation (6) demonstrates that the compensation current  $I<sub>∆</sub>$  is directly proportional to ∆*P* between two neighboring DCWTs. Therefore, the variation in output power resulting from different wind speeds at the two DCWT locations can be converted into the compensation current value. When there is a disparity in power between two DCWTs, the higher-power DCWT is compensated by the current through the energy storage inductor, *L*, which maintains a constant ratio of power to current. Currently, the DCWT maintains a steady output voltage to prevent overvoltage. When subjected to the HVBC, each DCWT<br>can produce electricity at its maximum canacity, enhancing the excepl power concretion can produce electricity at its maximum capacity, enhancing the overall power generation the DCWT, induced in series.<br>
the DCWT, induced by the connected in series.

> Using DCWT1 as a case study, the fluctuation ∆*U*wt of the terminal voltage *U*wt1 of the DCWT, induced by the compensating current *I*∆, can be mathematically represented as as

$$
\Delta U_{\rm wt} = \frac{T_{\rm s}^2 U_{\rm wt1}}{C_{\rm wt} L} \tag{7}
$$

where *T<sup>s</sup>* is the control period; *C*wt is the capacitance at the output of the DCWT; and *L* is the energy-storage inductance of the HVBC. Once the percentage of voltage ripple is established, the value of inductor *L* can be determined. The following Figure 3 illustrates tablished, the value of inductor *L* can be determined. The followi[ng](#page-5-1) Figure 3 illustrates the the effect of different inductance values on the HVBC control performance. effect of different inductance values on the HVBC control performance. where *Ts* is the control period; *C*wt is the capacitance at the output of the DCWT; and *L* is where  $t_s$  is the control period;  $C_{wt}$  is the capacitance at the output of the DCWT; and L

<span id="page-5-1"></span>

**Figure 3.** Effect of different inductance values on the control performance of HVBC. **Figure 3.** Effect of different inductance values on the control performance of HVBC.

According to the above figure, as the value of L increases, the voltage fluctuation at ends of the DCWT decreases, and the stabilization time shortens. However, the increased inductance will increase the hardware cost, and the inductor core will occupy a large space. Therefore, the value of *L* is taken as 10 mH in this study. According to the above figure, as the value of L increases, the voltage fluctuation at the

## <span id="page-5-0"></span>large space. Therefore, the value of *L* is taken as 10 mH in this study. **3. HVBC's Operating Principle**

 $D_1 \sim D_4$ ), two identical freewheeling inductors ( $L_1$  and  $L_2$ ), an energy storage inductor (*L*), and a fly-across capacitor ( $C_{\rm f}$ ) connected across the ends of the switching tubes ( $VT_2$  and *D*<sub>1</sub>). Cf mitigates the voltage shocks experienced by the switching tubes, enhancing the  $V_{\text{L}}$ Five S stability. Trypolitectically, the wind speed is higher at DCWT1 diam at DCWT2. In this scenario, the output power of DCWT1,  $P_1$ , is greater than the output power of DCWT2, *P*<sub>2</sub>. At this moment, the HVBC supplies a compensating current *I*∆ to DCWT1, ensuring that the power and current of both DCWTs satisfy the following Equation: As depicted in Figure [2,](#page-4-0) the HVBC comprises four power-switching tubes  $(VT_1 \sim VT_4)$ , HVBC's stability. Hypothetically, the wind speed is higher at DCWT1 than at DCWT2. In

$$
\frac{P_1}{(I_{\text{wt1}} + |I_{\Delta}|)} = \frac{P_2}{I_{\text{wt2}}} = \frac{U_{\text{r}}}{2}
$$
(8)

Conversely, the HVBC supplies *I*<sup>∆</sup> to DCWT2, ensuring that the requirements are met:

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$$
\frac{P_1}{I_{\text{wt1}}} = \frac{P_2}{(I_{\text{wt2}} + |I_{\Delta}|)} = \frac{U_{\text{r}}}{2} \tag{9}
$$

There are three possible scenarios regarding the power difference between two adjacent<br>
positions of the compensation currents in the compensation currents in the compensation currents in the compensation currents in the c DCWTs: In the first scenario,  $P_1$  exceeds  $P_2$ . *L* supplies a compensating current to DCWT1, where  $I_{\Delta} > 0$ . The second scenario occurs when  $P_1$  is lower than  $P_2$ . In this scenario, *L* supplies a compensating current to DCWT2, where  $I_\Delta < 0$ . The third scenario occurs when the value of  $P_1$  is equivalent to  $P_2$ . In this scenario, *L* does not supply the compensatory current, where  $I_{\Delta} = 0$ . Switching can be achieved by adjusting the carrier phase shift angle, attributable to the opposing directions of the compensation currents in the first two situations. In the third scenario, the two DCWTs can independently balance voltage without involving the HVBC in the requlation process: thus, we have not analyzed this scenario. involving the HVBC in the regulation process; thus, we have not analyzed this scenario.

The circuit characteristics of the HVBC dictate that switching tubes VT1 and VT4, as well as VT2 and VT3, operate complementarily.<br>The time of the time intervals *the mass forms* of the four person sprittling the four derivers on the four period

Figure [4](#page-6-0) illustrates the waveforms of the four power-switching tubes during operation. The time intervals  $t_0$   $\sim$   $t_4$  represent one complete operating cycle. The four periods,  $t_0 \sim t_1$ ,  $t_1 \sim t_2$ ,  $t_2 \sim t_3$ , and  $t_3 \sim t_4$ , correspond to the four switching modes of the HVBC powerswitching tubes, as indicated in Table [1.](#page-6-1) The number 0 indicates disconnection, and the number 1 indicates that the tubes are turned on. number 1 indicates that the tubes are turned on.

<span id="page-6-0"></span>

**Figure 4. Figure 4. Figure 4. Figure 4. C Figure 4.** Operating waveform of a power-switching tube.

<b>Switching Modes</b> and On-Time Periods	Mode 1 $t_0$ ~ $t_1$	Mode 2 $t_1 - t_2$	Mode 3 $t_2$ ~ $t_3$	Mode 4 $t_3 - t_4$
$VT_1$				
$VT_2$				
$VT_3$				
$VT_4$				

<span id="page-6-1"></span>**Table 1.** The HVBC switching tubes' operating modes.

Figure [5](#page-7-0) illustrates the operational condition of the HVBC during the four switching modes when two nearby DCWTs operate in the first scenario.

In mode 1,  $VT_1$  and  $VT_3$  are active, diverting the current entering the HVBC at node *c*. and then travels to node *a* via switching tube *VT*<sub>3</sub>. The compensating current *I*<sub>∆</sub> is diverted are active, allowing currents to pass through capacitor  $C_f$  and inductor  $L_2$ . These currents, The diverted current flows through inductor  $L_1$  and capacitor  $C_f$  to reach nodes *a* and *d*, respectively. The current passing through inductor  $L_2$  and capacitor  $C_f$  combines at node *d* at node *a*, and inductor  $L_2$  is in a state of renewed current. In mode 2, both  $VT_1$  and  $VT_2$ along with the current passing through switching tube  $VT_1$ , converge at node  $c$ . From there, they flow through inductor *L*<sup>1</sup> and switching tube *VT*2, sequentially, and converge at

node *a*. At node *a*,  $I_{\Delta}$  is diverted away. In mode 3,  $VT_2$  and  $VT_4$  are active, allowing the current to pass through diode  $D_4$  and inductor  $L_2$ . This current then converges at node *d* and continues to flow through capacitor  $C_{\rm f}$  toward node  $c$ . Next, the electric current passes through inductor  $L_1$  and switching tube  $VT_2$ , sequentially, before coming together at node  $a$ , where it diverts  $I_\Delta$ . In mode  $4$ ,  $VT_3$  and  $VT_4$  conduct, causing the current to flow through diode *D*<sup>4</sup> and inductor *L*2. This current converges at node d and then passes through diode  $D_3$ , capacitor  $C_f$ , and inductor  $L_1$ , converging at node *a*. At node *a*,  $I_\Delta$  is diverted, while  $inductor L<sub>2</sub> enters a state of the current.$ 

<span id="page-7-0"></span>

Figure 5. The operating state of HVBC in the first scenario. (a) Operating status of mode 1; (b) operating status of mode 2; (c) operating status of mode 3; (d) operating status of mode 4.

<span id="page-7-1"></span>Figure 6 illustrates the operational states of the HVBC during the four switching modes when two nearby DCWTs operate in the second scenario. *a*, *I* <sup>Δ</sup> is diverted, while inductor *L*2 enters a state of renewed current. Figure 6 indicates the operational states of the HVBC during the four switching



Figure 6. The operating state of HVBC in the second scenario. (a) Operating status of mode 1; operating status of mode 2; (**c**) operating status of mode 3; (**d**) operating status of mode 4. (**b**) operating status of mode 2; (**c**) operating status of mode 3; (**d**) operating status of mode 4.

When two neighboring DCWTs operate in the second scenario, the current flow in When two neighboring DCWTs operate in the second scenario, the current flow in the HVBC can be compared with the first scenario and, thus, will not be reiterated.

Under the HVBC's action, the output terminal voltages of each DCWT in a series-Under the HVBC's action, the output terminal voltages of each DCWT in a seriesconnected all-DC wind farm are identical. Considering two DCWTs connected in series, as seen in Figure [2,](#page-4-0) the cumulative power generated by the wind farm can be calculated as follows:

$$
P_{\Sigma} = P_1 + P_2 = I_{\rm r} U_{\rm r} \tag{10}
$$

The output power of the two DCWTs is represented as The output power of the two DCWTs is represented as

$$
\begin{cases}\nP_1 = I_{\text{wt1}} U_{\text{wt1}} = I_{\text{wt1}} \frac{U_{\text{r}}}{2} \\
P_2 = I_{\text{wt2}} U_{\text{wt2}} = I_{\text{wt2}} \frac{U_{\text{r}}}{2}\n\end{cases} \tag{11}
$$

The output current of the series cluster, *I*r, created by the two DCWTs linked in series can be calculated using Equations (10) and (11). *Electronics* **2024**, *13*, 3839 9 of 20

$$
I_{\rm r} = \frac{I_{\rm wt1} + I_{\rm wt2}}{2} \tag{12}
$$

The above equation can be expanded to a series cluster of *n* DCWTs. When *n* DCWTs are connected in series, the DC  $I_{rs}$  output from the series cluster can be expressed as

$$
I_{rs} = \frac{I_{\text{wt1}} + I_{\text{wt2}} + \dots + I_{\text{wtn}}}{n} = \frac{1}{n} \sum_{i=1}^{n} I_{\text{wti}}
$$
(13)

Then, the output power of the series-type all-DC wind farm is e series-type all-DC wind farm is series-type all-DC wind farm is

$$
P_{\sum} = \left(\frac{1}{n}\sum_{i=1}^{n} I_{\text{wt}i}\right)U_{\text{rs}} = \frac{U_{\text{rs}}}{n}\sum_{i=1}^{n} I_{\text{wt}i} = \sum_{i=1}^{n} P_{i} \tag{14}
$$

where  $U_{\text{rs}}$  is the output voltage of a series cluster consisting of *n* DCWTs connected in series.

series.<br>Equation (14) demonstrates that when the series-type all-DC wind farm is connected Leplation (14) demonstrates that when the series type an De wind farm is connected<br>to the HVBC, each DCWT can achieve maximum power output. The total power output of the wind farm is the sum of the output power of each DCWT, resulting in improved power generation efficiency.

# <span id="page-8-0"></span>**4. Control Strategy Based on HVBC for Series-Connected All-DC Wind Farms 4. Control Strategy Based on HVBC for Series-Connected All-DC Wind Farms**

Figure [7](#page-8-1) illustrates a comprehensive control method for an HVBC-based series-type Figure 7 illustrates a comprehensive control method for an HVBC-based series-type all-DC wind farm. all-DC wind farm.

<span id="page-8-1"></span>

**Figure 7.** Overall control strategy for series-connected all-DC wind farms based on HVBC. **Figure 7.** Overall control strategy for series-connected all-DC wind farms based on HVBC.

control, HVBC control, and modular DC/DC converter control. The functions to be achieved include voltage equalization the control of a series-connected DCWT using an HVBC under varying wind speeds; the active power control of the DCWT output; the maximum wind energy tracking control of the permanent magnet synchronous generator<br>(DMCC) wind energy tracking control of the permanent magnet synchronous generator (PMSG); the input end and voltage equalization at the output end. Using a machine-side AC/DC converter allows the PMSG to achieve maximum wind energy tracking control and manage the active power output of the DCWT. The HVBC equalizes the output voltage of the series-connected DCWTs using a double closed-loop regulation system that controls the rolage outer loop and the current miler loop. The DAB-based modular DC/DC converter<br>ensures that the voltage is equalized at the output of each DC/DC module and stabilizes the voltage at the output of the HVBC by stabilizing  $U_{\text{ri}}$ . The overall control method for wind farms consists of three components: DCWT (PMSG); and implementing a modular DC/DC converter to control voltage stabilization at voltage outer loop and the current inner loop. The DAB-based modular DC/DC converter

#### *4.1. HVBC's Control Strategy*

The HVBC decouples the power–current in series-type all-DC wind farms by supply-<br>The HVBC decouples the power–current in series-type all-DC wind farms by supplying a compensation current to maintain voltage balance in the series-connected DCWTs. As the DC/DC converter regulates the voltage at the HVBC's output, control must be applied to the output voltage of only one DCWT in each HVBC. The control object in the HVBC is the output terminal voltage  $U_{\text{wt}}$  of a DCWT. When  $U_{\text{wt}}$  exceeds the reference voltage  $U_{\text{wt}}$ <sup>\*</sup>, the HVBC injects a compensating current into the series-type all-DC wind farm through *L*. Conversely, if the *U*wt value of the DCWT is lower than *U*wt\*, the HVBC will extract a compensating current from the series-connected all-DC wind farm through *L*. The HVBC *compensioning current from the series connected an <i>D C Mna farm arough 2. The 111DC* can be regulated using a closed-loop control approach, which comprises a voltage outer loop and a current inner loop. Thus, the optimal control approach for an HVBC should be a dual closed-loop system comprising an outer loop for voltage control and an inner loop for current control. This HVBC control strategy is depicted in Figure [8.](#page-9-0)

<span id="page-9-0"></span>

Figure 8. HVBC's control strategy.

transmitted through the outer-loop PI controller to derive the reference value  $I^*_{\Delta}$  of the compensation current. The disparity between the compensation currents  $I_{\Delta}$  and  $I_{\Delta}^{*}$  is then transmitted through the inner-loop PI controller to obtain the duty cycle signal*, d*. The<br>drive signal of the HVBC is acquired using PWM modulation The discrepancy between the machine-end voltage  $U_{\text{wt}}$  of the DCWT and  $U_{\text{wt}}^*$  is drive signal of the HVBC is acquired using PWM modulation.<br>The expressions for the voltage outer loop. PL, and the cu

the signal of the HVBC is acquired using PWM modulation.<br>The expressions for the voltage outer loop,  $PI_1$ , and the current inner loop,  $PI_2$ , are<br>the expressions for the voltage outer loop,  $PI_{1}$ , and the current inner shown in Equation (15). The control block diagram of the HVBC, depicted in Figure [8,](#page-9-0) can<br>be drawn as per Figure 9.<br> $\int PI_1 = \frac{sK_{p1} + K_{i1}}{s}$ be drawn as per Figure [9.](#page-9-1) *sK K*

<span id="page-9-1"></span>

**Figure 9.** Control diagram of HVBC. **Figure 9.** Control diagram of HVBC.

 *s* The error between the actual and given voltage values at both ends of the DCWT as shown in the following equation. passes through the PI controller to obtain the reference value of the compensation current,

$$
I_{\Delta}^{*} = (U_{wt1} - U_{wt2}^{*}) \cdot \frac{sK_{p1} + K_{i1}}{s}
$$
 (16)

Using Mason's formula, the closed-loop transfer function of the HVBC control block diagram can be obtained, as shown in Equation (17).

$$
\Phi(s) = \frac{I_{\Delta}(s)}{I_{\Delta}^*(s)} = \frac{K_{p2}s + K_{i2}}{TLs^3 + Ls^2 + K_{p2}s + K_{i2}}\tag{17}
$$

According to the Laws criterion, the conditions shown in Equation (18) need to be satisfied to ensure that the system is stabilized.

$$
K_{i2} < \frac{1}{T} K_{p2} \tag{18}
$$

Taking the HVBC operating mode shown in Figure [5a](#page-7-0) as an example, the relationship between the power difference between two DCWTs and *d* can be analyzed. According to Kirchhoff's Voltage Law (KVL),

$$
U_{L} + U_{L1} = L \frac{di_{\Delta}}{dt} + L_{1} \frac{di_{L1}}{dt} = U_{wt1}
$$
\n(19)

$$
U_{C_f} = U_{L1} \tag{20}
$$

Joining the above two equations, the electric current passing through the fly-across capacitance  $C_f$  can be expressed as

$$
I_{C_f} = C_f \frac{du_{C_f}}{dt} = C_f L_1 (d \frac{di_{L1}}{dt} / dt)
$$
 (21)

The preceding equation can be integrated to obtain the following result:

$$
I_{C_f} \cdot (dT)^2 = C_f L_1 \cdot I_{L1} \tag{22}
$$

This is obtained via generalized nodal analysis:

$$
I_{VT_1} = I_{L1} + I_{C_f} = I_{\Delta}
$$
\n(23)

From Equations (22) and (23), the relationship between  $I_{L1}$  and  $I_{\Lambda}$  can be expressed as

$$
I_{L1} = \frac{(dT)^2}{C_f L_1 + (dT)^2} I_{\Delta} \tag{24}
$$

The preceding Equation can be integrated to obtain

$$
U_{\text{wt1}} \cdot dT = L \cdot I_{\Delta} + L_1 I_{L1} \tag{25}
$$

Per Equations (6), (24), and (25), the relationship between the ∆*P* and *d* values of two neighboring DCWTs can be derived as

$$
\Delta P = \frac{(dT)^3 + C_f L_1 dT}{(L + L_1)(dT)^2 + C_f L L_1} \cdot \frac{U_{\text{wt1}} U_{\text{r}}}{2} \tag{26}
$$

The Equation above has a strictly increasing trend when *d* is within the interval [0, 1], attaining its maximum value when  $d = 1$ . By appropriately adjusting the parameters of the HVBC, it is possible to increase the power difference between two adjacent DCWTs beyond the rated power of a single DCWT. Thus, in the event of failure, if one of the DCWTs is disconnected, it can still maintain the efficient functioning of the series-connected DC wind farm. Furthermore, the wind farm's stability increases as more DCWTs are connected in series, enabling more DCWTs to be disconnected without affecting operations.

## 4.2. DCWT's Control Strategy

<span id="page-11-0"></span>Figure 10 illustrates the DCWT control strategy [\[30\]](#page-20-2).



Figure 10. DCWT's control strategy.

*a* person of the modular DC/DC Control method. After *n*<sup>\*</sup> is compared with the rotational speed *a* detected by the ground concern the reference when for the ground *i* is con he obtained through PI regulation. The reference value for the *d*-axis current,  $i_d^*$ , is set to zero. The values  $i_q^*$  and  $i_d^*$  are obtained through coordinate transformation. These values **d** through SVPWM modulation. The PMSG's action **<sup>+</sup> <sup>+</sup> <sup>+</sup>** PI **<sup>+</sup>** *U*dc*\** - maximum wind energy. coordinate transformation. The voltage values *u*<sub>α</sub> and *u*<sub>β</sub> are obtained in the *αβ* coordinate voltage value of the PMSG stator output. This allows the PMSG to optimally track the EVERTUAL TO THE **EXECUTER CONDUMNS** TO THE VEHICLE THE VALUE OF T detained through SVPWM modulation. The PMSG's active power regulation is achieved The reference value for rotational speed, denoted by  $n^*$ , is determined using the *n* detected by the speed sensor, the reference value for the *q*-axis current,  $i_q^*$ , can be represent the difference between the current inner loop decoupling controls. The generator stator *d*-axis and *q*-axis voltage reference values  $u_d^*$  and  $u_q^*$  are obtained after the *dq-αβ* by monitoring the stator voltage reference value and adjusting it based on the three-phase

# … *4.3. Control Strategy for Modular DC/DC Converters 4.3. Control Strategy for Modular DC/DC Converters*

Figure [11](#page-11-1) depicts the control method, assuming that the modular DC/DC converter  $\frac{1}{2}$  *n* DAB modu  $\alpha$  *m*  $D$ *l*  $\alpha$  modules in total. - comprises *m* DAB modules in total. comprises *m* DAB modules in total.

<span id="page-11-1"></span>

 $\frac{1}{\sqrt{2}}$  trategy adopting the same shift ratio. This will impact the modular the modu **Figure 11.** Control strategy for modular DC/DC converters. **Figure 11.** Control strategy for modular DC/DC converters.

As a sub-module of the modular DC/DC converter, a dual active bridge isolation converter is utilized in considering the DCWT startup procedure and the isolation requirements. Equalizing each module's output voltage and maintaining a steady input voltage are the control goals of the modular DC/DC converter. Owing to the variations in each module's specifications, an imbalanced voltage output will result from each module's control strategy adopting the same shift ratio. This will immediately impact the modular DC/DC converter's stability. Therefore, equalization control must be added to each DAB module, and each DAB module's shift ratio can be acquired by correcting this ratio [\[31](#page-20-3)[,32\]](#page-20-4).

As illustrated in Figure [11,](#page-11-1) *U*<sub>ini</sub> and *U*<sub>dci</sub> stand for the input and output voltage values of the *i* th DAB module, respectively;  $U_{DC}$ <sup>\*</sup> represents the wind farm's output voltage reference;  $U_{DC}$  represents the wind farm's actual output voltage; and  $U_{rs}$  represents the HVBC's total voltage value. The shift ratio *d<sup>i</sup>* of the *i* th DAB module can be obtained by adding the modified shift ratio  $d_{si}$  from the voltage equalization link to the common shift ratio  $\tilde{d}_c$  from the voltage stabilization link. PWM modulation is then used to extract the control signal from the switching tube of the DAB module.

#### *4.4. Control Flowchart of HVBC-Based Series-Type All-DC Wind Farm 4.4. Control Flowchart of HVBC-Based Series-Type All-DC Wind Farm*

Taking two DCWTs in series in Figure 2 as an example, the wind speeds of the two Taking two DCWTs in series in Figure [2](#page-4-0) as an example, the wind speeds of the two DCWTs can be denoted as  $v_{w1}$  and  $v_{w2}$ , and the active power emitted can be denoted as  $P_1$ and *P*2. The remaining quantities are labeled in Figure 2. The voltages of the two DCWTs and *P*2. The remaining quantities are labeled in Figure [2.](#page-4-0) The voltages of the two DCWTs are assumed to be equal under the initial conditions, i.e.,  $U_{wt1} = U_{wt2}$ . The control flowchart of the HVBC-based series-connected all-DC wind farm is shown in Figure [12](#page-12-1) when the wind speed varies. wind speed varies.

<span id="page-12-1"></span>

**Figure 12.** Control flowchart of HVBC-based series-type all-DC wind farms. **Figure 12.** Control flowchart of HVBC-based series-type all-DC wind farms.

### <span id="page-12-0"></span>**5. Simulation Verification 5. Simulation Verification**

This study constructed a 60 kV/48 MW series-type all-DC wind farm model using the This study constructed a 60 kV/48 MW series-type all-DC wind farm model using the Matlab-2022b/Simulink simulation platform to confirm the viability and efficacy of the Matlab-2022b/Simulink simulation platform to confirm the viability and efficacy of the designed HVBC. The model is shown in Figure 13. Table 2 displays the primary design designed HVBC. The model is shown in Figure [13.](#page-13-0) Table [2](#page-13-1) displays the primary design parameters. parameters.

<span id="page-13-0"></span>parameters.<br>Parameters.



Figure 13. Simulink modeling of a 60 kV/48 MW series-type all-DC wind farm based on HVBC.

Parts	<b>Parameters</b>	Value
Wind farm	Number of DCWT $n$	6
	Rated capacity of wind farm S (MW)	48
	DC transmission voltage $U_{DC}$ (kV)	60
	Rated wind speed $v_w$ (m/s)	12
<b>DCWT</b>	Stand-alone capacity $S_{wt}$ (MW)	8
	Rated output voltage $U_{\text{wt}}$ (kV)	3
<b>HVBC</b>	Energy-storage inductor $L(mH)$	10
	Freewheeling inductors $L_1$ , $L_2$ (mH)	
	Fly-across capacitor $C_f$ (mF)	30
	Switching frequency $f_{sw}$ (kHz)	10
Modular DC/DC converter	Number of single modules $m$	5
	Number of DAB modules in a single module $x$	2
	Single module output voltage $U_{dc}$ (kV)	12

<span id="page-13-1"></span>**Table 2.** Design parameters of HVBC-based series-type all-DC wind farms.

Excluding the measurement module, our Simulink consists of five main modules, i.e., the module for generating wind speed, the module for a series wind farm consisting of six DCWTs, the module for the HVBC, the control module for the HVBC, and the module for the modular DC/DC converter.

This simulation primarily validates the stability of the designed wind farm in the presence of uneven wind speeds, abrupt changes in wind speeds, and wind turbine failure shutdown situations. This research establishes a comparative simulation to emphasize the stabilizing impact of HVBCs on the output voltage of in-series DCWTs.

#### *5.1. Comparison Simulation*

To demonstrate the effectiveness of the HVBC, we simulate a hypothetical scenario of wind speed variations at a wind farm. It is hypothesized that at 0.8 s, the wind speed at each DCWT location transitions from the reported wind speed to the following speeds: 13 m/s, 12 m/s, 11 m/s, 10 m/s, 9 m/s, and 8 m/s.



<span id="page-14-0"></span>The simulated waveforms of each DCWT's output voltage in a conventional series-The simulated waveforms of each DCWT's output voltage in a conventional seriesconnected all-DC wind farm are shown in Figure 14a. Figure 14b shows the output current connected all-DC wind farm are shown in Fig[ure](#page-14-0) 14a. Fi[gur](#page-14-0)e 14b shows the output current of the series cluster consisting of DCWTs coupled in a series configuration. of the series cluster consisting of DCWTs coupled in a series configuration.

Figure 14. Simulation of a conventional series-type all-DC wind farm. (a) Output voltage of DCWTs; (**b**) output current of series clusters. (**b**) output current of series clusters.

According to the simulated waveforms shown in Figure 14, the output voltage of the the wind farm remains constant. This is due to the control exerted by the grid-connected inverter. At 0.8 s, the wind speed of the entire wind farm declines, reducing the electricity generated. Consequently, the output current of the series cluster is reduced from 2667 A to 2040 A. When the output current of a series cluster falls, the output voltage of some of the DCW is in the cluster suddenly increases. This leads to a maximum overvoltage of the original  $1 \, \text{W}$  significantly impacting the original dable functioning of the of approximately 1 kV, significantly impacting the safe and stable functioning of the wind farm. According to the simulated waveforms shown in Figure [14,](#page-14-0) the output voltage of to 2010 A. When the output current of a series cluster hans, the output voltage of some<br>of the DCWTs in the cluster suddenly increases. This leads to a maximum overvoltage wind farm.  $\lim_{t\to 0}$  shows the simulated waveforms of the output voltage of  $\lim_{t\to 0}$  in a contract  $\lim_{t\to 0}$ 

Figure [15](#page-14-1) shows the simulated waveforms of the output voltage of each DCWT in a series-connected all-DC wind farm under the influence of an HVBC. series-connected all-DC wind farm under the influence of an HVBC.

<span id="page-14-1"></span>

**Figure 15.** Simulation of HVBC-based series-type all-DC wind farms. **Figure 15.** Simulation of HVBC-based series-type all-DC wind farms.

Currently, owing to the influence of the HVBC, the output voltage of each DCWT in the wind farm experiences minor turbulence and then rapidly settles at approximately 3 kV.<br>The LWBC mitiastas the naturial far assembles a second by shownt shapese in wind grass d In certain wind turbines inside the all-DC wind farm, enhancing its safety and stability. According to the simulated waveforms, the wind speed suddenly changes at 0.8 s. The HVBC mitigates the potential for overvoltage caused by abrupt changes in wind speed

#### $T_{\rm 1.6}$  mitigates the potential for overvoltage caused by abrupt changes in windows i *5.2. Operating with Unequal Wind Speeds*

nd the wind speed at each DCWT location is assumed to be consistently maintained at The wind speed at each DCWT location is assumed to be consistently maintained at bility. 14 m/s, 13 m/s, 12 m/s, 11 m/s, 10 m/s, and 9 m/s. Given that the wind speeds at DCWT1 *5.2. Operating with Unequal Wind Speeds*  three DCWTs are generating power at their rated capacity. Theoretical estimates for theto DCWT3 are equal to or higher than the rated wind speeds they are designed for, the first



<span id="page-15-0"></span>output power of the remaining DCWTs are 7.2 MW, 5.8 MW, and 4.4 MW. Figure [16](#page-15-0) shows the simulated waveforms for operations under varying wind speed conditions.

**Figure 16.** Operating with unequal wind speeds. (**a**) Simulated waveforms of each DCWT output **Figure 16.** Operating with unequal wind speeds. (**a**) Simulated waveforms of each DCWT output voltage; (**b**) simulated waveforms of HVBC supplying compensation current to series DCWT. voltage; (**b**) simulated waveforms of HVBC supplying compensation current to series DCWT.

Figure [16a](#page-15-0) demonstrates that the HVBC system can stabilize each DCWT's output<br>relaxe at about 2 kV confirming that the HVBC proposed in this study effectively stabilized the voltage of series-connected DCWTs in the presence of unequal wind speeds. voltage at about 3 kV, confirming that the HVBC proposed in this study effectively stabilizes

According to Figure 16b, the power differential can be estimated to be zero because DCWTs 1, 2, and 3 all function at their rated state. Consequently, the compensating currents *I*<sub>∆1</sub> and *I*<sub>∆2</sub> provided by the first two HVBCs can be maintained at a stable<br>colse also the Consequently curing laters as DCWT2 and DCWT4 is 0.0 MW Based as Figure 1 and 2 an results depicted in Figure 16b exhibit a high degree of proxim[ity](#page-15-0) to the corresponding theoretical values. Furthermore, the compensation current  $I_{\Delta 4}$  generated by the fourth<br>HVBC is pearly equivalent to the compensation current  $I_{\Delta 4}$  produced by the fifth HVBC owing to the comparable power discrepancy between DCWTs 4 and 5, as well as between value close to 0. The power disparity between DCWT3 and DCWT4 is 0.8 MW. Based on<br>Execution (5) it can be informed that the compensation *current 4 is* 267.A. The simulation HVBC is nearly equivalent to the compensation current *I*<sub>∆5</sub> produced by the fifth HVBC, and the comparable power discrepancy between  $DCWTEA$  and  $5$  as well as between  $DCWTs$  5 and 6. Examining the relationship between the output power of each  $DCWT$  and the compensation current confirms that the amount of compensation current is directly proportional to the ∆*P* value between the outputs of two adjacent DCWTs.

## *5.3. Operating during Rapid Changes in Wind Speed 5.3. Operating during Rapid Changes in Wind Speed*

<span id="page-15-1"></span>Wind speed variations at six DCWTs in a hypothetical series-type all-DC wind farm Wind speed variations at six DCWTs in a hypothetical series-type all-DC wind farm are shown in Figure 17. are shown in Figure 1[7.](#page-15-1) 



**Figure 17.** Rapid changes in wind speed in series-connected all-DC wind farms. **Figure 17.** Rapid changes in wind speed in series-connected all-DC wind farms.

<span id="page-16-0"></span>

**Figure 18.** Operation under conditions of rapid changes in wind speed. (**a**) Simulated waveforms of **Figure 18.** Operation under conditions of rapid changes in wind speed. (**a**) Simulated waveforms of each DCWT output voltage; (**b**) simulated waveforms of each DCWT output power; (**c**) simulated each DCWT output voltage; (**b**) simulated waveforms of each DCWT output power; (**c**) simulated waveform of compensation current supplied by HVBC to series DCWTs. waveform of compensation current supplied by HVBC to series DCWTs.

Figure [18a](#page-16-0) demonstrates that when there is a fast shift in wind speed in a seriesconnected all-DC wind farm, the output voltage of each DCWT fluctuates. However, the output terminal voltage of the DCWTs immediately stabilizes at around 3 kV, owing to the HVBC's influence. This confirms that the HVBC suggested in this study effectively stabilizes the voltage of a series-connected DCWT in an all-DC wind farm when there are rapid wind speed variations.

Figure [18b](#page-16-0) demonstrates that the output power of each DCWT can precisely and fectively adjust to wind speed variations. The wind farm's DCWTs can achieve maximum effectively adjust to wind speed variations. The wind farm's DCWTs can achieve maximum power output even when there are wind speed changes. This confirms that the HVBC can power output even when there are wind speed changes. This confirms that the HVBC can successfully decouple the power of all series-connected all-DC wind farms and effectively enhance their power generation efficiency.

Figure [18c](#page-16-0) shows that the amplitude of the compensating current is directly proportional to the power differential between two adjacent DCWTs. If the power differential between two adjacent DCWTs is positive, then the compensation current provided by the HVBC to the series-connected DCWTs is also positive. If the power difference between two adjacent DCWTs is negative, the compensation current's amplitude is also negative. This indicates that the HVBC draws the compensation current from the in-series DCWTs.

#### *5.4. Operating with Wind Turbine Failure Shutdown*

When a DCWT of a wind farm malfunctions or undergoes repair, it is necessary to deactivate the DCWT. Imagine that, one second ago, each DCWT was operating at its rated state, and after one second, DCWT2 was taken out of operation, owing to a fault. Currently, DCWT2 produces no output power, while the other DCWTs continue to output power at their rated levels. The simulated waveforms of the output voltage of each DCWT in the wind farm are depicted in Figure [19.](#page-17-0)

The simulation results in the above figure show that when one DCWT in the seriesconnected all-DC wind farm is shut down, the output terminal voltage of each remaining DCWT fluctuates, but it soon stabilizes at about 3 kV. In addition, the voltages at the ends of the capacitors connected to DCWT2 are clamped by the HVBC at about 3 kV.

<span id="page-17-0"></span>

in the wind farm are depicted in Figure 19.

2940

Figure 19. The operation of each DCWT in a wind farm when one DCWT is shut down.  $\mathbf{A}$ 

and DCWT4 are deactivated, their output power decreases to zero, while the output power of the remaining DCWTs remains unaffected. Figure 20 shows the simulated waveforms of the output voltage for each DCWT in the wind farm. At 1 s, DCWT2 and DCWT4 are deactivated because of a malfunction. When DCWT2

*U*wt5

<span id="page-17-1"></span>

Figure 20. The operation of each DCWT in a wind farm when two DCWTs are shut down.

 $\frac{1}{2}$ connected all-DC wind farm are deactivated, the output terminal voltage of each remaining The simulation results in the Figure above show that when two DCWTs in a series-regulated by the HVBC at around 3 kV. Comparing Figure [19](#page-17-0) with Figure [20](#page-17-1) shows that as the number of DCWTs in the wind farm cut-out operation increases, the voltage fluctuation DCWT fluctuates momentarily but rapidly stabilizes at approximately 3 kV. Additionally, be we indeterminist but tapidly stabilizes at approximately 5 KV. Additionally,<br>the voltages at both terminals of the capacitors connected to DCWT2 and DCWT4 are at both ends of the DCWT at the moment of cut-out also increases. This is caused by an<br>in magnetic that the name difference helpes with project heritor DCWT. increase in the total power difference between the neighboring DCWTs.

<span id="page-17-2"></span>Figure [21](#page-17-2) shows simulated waveforms of the output voltage and output power of each DCWT in the wind farm when DCWTs 2, 3, and 4 are simultaneously deactivated after 1 s.



**Figure 21.** The operation of each DCWT in a wind farm when three DCWTs are shut down. (**a**) **Figure 21.** The operation of each DCWT in a wind farm when three DCWTs are shut down. (**a**) Simulated waveforms of each DCWT output voltage; (**b**) simulated waveforms of each DCWT output power.

Based on the modeled findings depicted in the Figure above, it can be inferred the HVBC effectively limits the voltage at the terminals of the capacitors linked to DCWTs 2, 3, and 4 to approximately 3 kV. After removing DCWTs 2, 3, and 4 from operation, their power output decreases to zero. However, the remaining DCWT continues to output power at the rated level. This confirms that the HVBC effectively decouples power and ensures that the remaining DCWTs are unaffected by removing the others. The DCWT can effectively maintain a stable output voltage throughout the cutover operation, enhancing the stability and reliability of the wind farm. When comparing Figure [20](#page-17-1) with Figure [21a](#page-17-2), we can see that although the number of DCWTs taken out of operation increases, the voltage fluctuation of each at the moment of cut-out decreases. This is because the total power difference between neighboring DCWTs decreases.

#### <span id="page-18-0"></span>**6. Conclusions**

This study proposed an HVBC topology and control strategy applicable to largecapacity series-connected all-DC wind farms. We did so to adapt to the large-capacity and large-scale development of new energy sites represented by wind energy. This research can help full-DC wind farms realize safer and more reliable operations, help construct new large-scale energy bases, and promote the transition of global energy to renewable energy.

To maintain voltage balance between the output terminals of each DCWT in a seriesconnected all-DC wind farm, enhance operational stability, and prevent a decrease in power generation efficiency due to the addition of voltage-limiting settings, this study introduced a novel circuit topology called a half-bridge voltage balancing circuit, designed for use in series-connected all-DC wind farms. First, the operational mechanism of the HVBC was examined, and then a control method was designed to ensure that the output voltage of each DCWT is balanced while the farm works under various conditions. Finally, the viability of the HVBC topology was confirmed via simulation. The conclusions are as follows:

- 1. The suggested HVBC design efficiently decouples power in a DCWT series, ensures balanced output voltage across each DCWT, and mitigates the risk of overvoltage in the DCWT series caused by wind speed variations.
- 2. After the HVBC is connected to the series-connected all-DC wind farm, each DCWT can realize its maximum power output under output voltage balance conditions. This enhances the overall power generation efficiency of the wind farm.
- 3. When a section of the DCWTs in a series-connected all-DC wind farm is taken out of operation owing to a fault or for maintenance purposes, the HVBC can limit the capacitive voltage at the DCWT output to its normal operating value. This enhances the stability and dependability of the wind farm's operations.

The HVBC topology proposed in this paper has good application prospects for seriestype all-DC wind farms. Further research can be conducted by considering two perspectives: first, research on applying HVBCs to offshore all-DC wind farms with higher voltage levels, and second, in-series–parallel-type all-DC wind farms can be considered to adapt to the development of large-scale and large-capacity offshore all-DC wind farms.

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