

Article **Large Signal Stability Analysis of Hybrid AC/DC Microgrids When a Single-Phase-to-Ground Fault Occurs**

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Abstract: Islanded hybrid AC/DC microgrids lack support for a large grid, and the negative incremental impedance of constant power loads (CPLs) aggravates the poor anti-disturbance capability of the system. When a single-phase ground fault (SPGF) occurs, the amount of fault impulse power that islanded AC/DC hybrid microgrids can stably withstand and when the protection equipment can work are both unknown. In this paper, the method of symmetrical components is utilized, and high-signal stability criteria for islanded hybrid AC/DC microgrids when a SPGF occurs are derived based on the mixed potential theory. The proposed criteria place quantitative constraints on the power of the PV unit, DC/AC converter current inner-loop proportional parameters, inductors, and inductor equivalent resistance, as well as energy storage unit power, CPL power, capacitors, DC bus voltage, line equivalent resistance, line equivalent inductance, equivalent inductance in the faulty branch, equivalent resistance in the faulty branch, positive-sequence equivalent impulse power of the SPGF, and zero-sequence equivalent impulse power. Furthermore, the maximum impulse power of a SPGF that islanded hybrid AC/DC microgrids could stably withstand is also presented, providing guidelines for protection equipment to decide when to work. In addition, the allowable maximum CPL power that islanded hybrid AC/DC microgrids could steadily support as a SPGF occurs is deduced, and the power is usually adopted to determine the states of an energy storage unit and load shedding in advance. Simulation and experimental validations prove the correctness of the derived high-signal stability criteria.

Keywords: islanded AC/DC hybrid microgrids; maximum impulse power of single-phase-to-ground fault (SPGF); constant power loads (CPLs); high-signal stability; mixed potential function

1. Introduction

Islanded AC/DC hybrid microgrids consist of various distributed generations (DGs), an AC bus, AC loads, a DC bus, DC loads, energy storage systems, and many interconnection converters, and they integrate both the advantages of AC microgrids and DC microgrids [\[1,](#page-20-0)[2\]](#page-20-1). All tightly regulated converters and motors are regarded as constant power loads (CPLs), and the negative incremental impedance of CPLs might significantly increase disturbances and lead to instability [\[3–](#page-20-2)[5\]](#page-20-3). The voltage and frequency of islanded AC/DC hybrid microgrids are usually supported by grid-forming converters replacing large grids. Furthermore, a large number of power electronic devices are utilized instead of traditional generators in microgrids, and small inertia and a poor anti-interference ability are both introduced [\[6,](#page-20-4)[7\]](#page-20-5). Consequently, islanded AC/DC hybrid microgrids are extremely sensitive to disturbances. Actually, large severe disturbances are mostly caused by faults. Particularly, the single-phase ground fault (SPGF) accounts for the largest percentage of faults and is the most common [\[8](#page-20-6)[–11\]](#page-20-7). However, when a SPGF occurs, the amount of fault impulse power that islanded AC/DC hybrid microgrids could stably withstand is unknown; when the protection equipment would work is not definite, and the amount of CPL power that islanded AC/DC hybrid microgrids could support is also not clear. To

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avoid a blackout, it is very important to determine how to guarantee the stable operation of islanded AC/DC hybrid microgrids when an SPGF occurs [\[12](#page-20-8)[,13\]](#page-20-9).

In fact, the transient stability analysis of power systems under fault conditions is widely performed [\[14](#page-20-10)[–16\]](#page-20-11). References [\[14](#page-20-10)[,15\]](#page-20-12) studied the transient stability of permanent magnet synchronous generators and doubly fed wind turbines when severe grid faults appeared. Reference [\[16\]](#page-20-11) considered the transient stability of voltage source converters with adaptive phase-locked loops under grid fault and fault recovery conditions. Based on the data distribution of transient zero-sequence currents, reference [\[17\]](#page-20-13) detected a SPGF under three-phase voltage imbalance conditions in neutral inefficiently grounded distribution systems. Reference [\[18\]](#page-20-14) proposes a novel SPGF detection method according to single-phase instantaneous reactive power theory and instantaneous active power theory to discriminate the line state. Reference [\[19\]](#page-20-15) analyzed the SPGF characteristics in modular multilevel converter-based high-voltage direct-current (MMC-HVDC) converter stations and proposed an overvoltage suppression stabilization control strategy based on zero-sequence voltage modulation. These studies propose many mature methods to increase the stability for large power systems under various fault conditions, especially SPGF. Unfortunately, due to the small inertia and poor anti-interference ability of microgrids, these mature methods are not applicable to analyzing microgrids' stability under fault conditions.

Investigating the stability of microgrids while considering fault influences is particularly important. When severe three-phase symmetrical faults appear, the synchronization stability of grid-connected converters under weak grid conditions has been described [\[20\]](#page-20-16). Reference [\[21\]](#page-20-17) proposes the fault ride-through (FRT) control for voltage-controlled inverters in distributed generation units. Obviously, faults significantly affect the stability of islanded microgrids without the support of large grids. Reference [\[22\]](#page-20-18) studied the stability of islanded microgrids with shunt synchronous generators and virtual synchronous generators (SG-VSGs) when three-phase short-circuit faults occur. Reference [\[23\]](#page-20-19) presents current- and voltage-limiting strategies to enhance the fault ride-through (FRT) capability of islanded microgrids under symmetrical and asymmetrical fault conditions. Reference [\[24\]](#page-21-0) explores the fundamental issues of converter control for islanded microgrids under faulty conditions. In [\[25\]](#page-21-1), a superimposed phase current scheme with voltage constraint elements is proposed for identifying faults in islanded microgrids. In [\[26\]](#page-21-2), a self-propelled artillery fault line identification method based on the construction of high-frequency zero-sequence voltage and zero-sequence current is proposed for islanded microgrids. The mentioned studies are all focused on fault identification and converter control, and the typical characteristics of CPLs are not taken into account. Furthermore, it is extremely necessary to consider the fault influences, the micro source features, the CPLs, and the energy storage during microgrid stability analysis procedures.

On the other side, a high-signal stability analysis is the most typical stability method for microgrids. Based on the Lyapunov function, a high-signal stability criterion of DC microgrids is derived in [\[27\]](#page-21-3). Reference [\[28\]](#page-21-4) also utilized the Lyapunov function to construct a high-signal model of microgrids and obtained an asymptotic stability domain to illustrate parameter stability influences. Reference [\[29\]](#page-21-5) constructed the neural Lyapunov function of microgrids and obtained the stability region. Reference [\[30\]](#page-21-6) optimized the polynomial droop controller and estimated the operation region for DC microgrids. Reference [\[31\]](#page-21-7) approximated the nonlinear model of microgrids with a TS fuzzy model and obtained the stability domain using an LMI tool. Particularly, the Brayton–Moser mixed potential function method is generally adopted to analyze the high-signal stability of microgrids. For AC/DC hybrid microgrids, reference [\[32\]](#page-21-8) derived and optimized a unified stability criterion while considering the battery operation mode and the converter output current limitation based on the mixed potential function. References [\[33,](#page-21-9)[34\]](#page-21-10) present an absolute stability condition and a region of asymptotic stability of DC microgrids using the mixed potential function. As mentioned, the characteristics of CPLs, energy storage units, and

converter parameters are all considered in the high-signal stability of microgrids; however, the fault influences are not taken into account.

When a SPGF occurs, asymmetrical effects are introduced to the microgrids; consequently, the modeling of the islanded AC/DC hybrid microgrids becomes quite difficult. In this paper, to guarantee the stability operation of islanded AC/DC hybrid microgrids when a SPGF occurs, the method of symmetrical components is used, and large-signal stability criteria are derived based on mixed potential theory. The positive-sequence power of the PV unit, DC/AC converter current inner-loop proportional parameters, inductors, and inductor equivalent resistance, as well as energy storage unit power, CPL power, capacitors, DC bus voltage, the line equivalent resistance, line equivalent inductance, the equivalent inductance in the faulty branch, the equivalent resistance in the faulty branch, the positivesequence equivalent impulse power of the SPGF, and zero-sequence equivalent impulse power are all considered. The contributions of this paper are summarized as follows:

- (1) Based on the structure and control strategies, the method of symmetrical components is utilized, and the positive-sequence equivalent model and zero-sequence equivalent
microgrides when a SPGF occurs are derived in the derived in t model of islanded AC/DC hybrid microgrids when a SPGF occurs are derived in the dq rotating frame separately.
- dq folding frame separately.

(2) Large signal stability criteria of islanded hybrid AC/DC microgrids when a SPGF occurs are obtained using mixed potential theory. curs are obtained using mixed potential theory.
- (3) The maximum impulse power of a SPGF that islanded hybrid AC/DC microgrids (3) The maximum impulse power of a SPGF that islanded hybrid AC/DC microgrids could stably suffer is derived and provides quantitative guidelines for protection could stably suffer is derived and provides quantitative guidelines for protection equipment to decide when to work. equipment to decide when to work.
- (4) The allowable maximum CPL power that islanded hybrid AC/DC microgrids could (4) The allowable maximum CPL power that islanded hybrid AC/DC microgrids could stably support as a SPGF occurs is deduced, and the power is usually adopted to stably support as a SPGF occurs is deduced, and the power is usually adopted to determine the power of the energy storage unit and load shedding in advance. determine the power of the energy storage unit and load shedding in advance.

2. Equivalent Modeling of Islanded AC/DC Hybrid Microgrids 2. Equivalent Modeling of Islanded AC/DC Hybrid Microgrids

2.1. Structure and Control Strategies of Islanded AC/DC Hybrid Microgrids 2.1. Structure and Control Strategies of Islanded AC/DC Hybrid Microgrids

A representative islanded AC/DC hybrid microgrid is shown in Figure 1. The PV A representative islanded AC/DC hybrid microgrid is shown in Figure [1.](#page-2-0) The PV generation unit, the energy storage unit, a bidirectional DC/AC converter, CPLs, and resistive loads are all included. The energy storage unit is composed of a buck–boost converter and batteries and is connected to the AC bus through the cascaded bidirectional DC/AC converter. The CPLs are represented by a closed loop-controlled buck converter DC/AC converter. The CPLs are represented by a closed loop-controlled buck converter and a resistor, and the power consumption of the CPLs is constant. and a resistor, and the power consumption of the CPLs is constant.

Figure 1. The structure of islanded AC/DC hybrid microgrids.

DC/DC converters cascaded with AC/DC converters are utilized in PV units. The DC/DC converter boosts the DC voltage, and the AC/DC converter achieves the power output.

The control flow of the bidirectional buck–boost converter in the energy storage unit is shown in Figure [2.](#page-3-0) The PI control strategies are utilized in both the outer voltage controllers and inner current controllers. When i_b is positive, the battery is working in a discharging state, and when i_b is negative, the battery is working in a charging state. Firstly, the reference voltage $v_{\text{d}cref}$ and the actual voltage v_{dc} are compared. If the battery is in a discharging state, and the calculated reference current i_{bref} of the battery is positive, and when $v_{dcref} > v_{dc}$ occurs, i_{bref} is increased to maintain v_{dc} equal to $v_{\text{d}cref}$. On the contrary, if the battery is in a charging state, and the calculated reference current *i_{bref}* of the battery is negative, and when $v_{\text{d}cref} > v_{\text{dc}}$, i_{bref} is also increased, the absolute value of the charging current i_{bref} is decreased to increase the DC bus. Furthermore, when $v_{\text{d}c\text{ref}} < v_{\text{dc}}$, similar regulation procedures are also performed.

battery is in a charging state, and the calculated reference current *ibref* of the battery is neg-

Figure 2. The control flow of the bidirectional buck-boost converter in the energy storage unit.

The control flow of the bidirectional DC/AC converter is shown in Figur[e 3](#page-3-1). Q/V and P/f droop controls are used to obtain the reference voltages v_{dref} and v_{qref} at the d-axis and q -axis, respectively. The abc-dq and dq-abc coordinate transformations are both adopted. Actual AC voltage v_{abc} and current i_{abc} are used to obtain active power p and reactive power q . Outputs of the low-pass filter are \overline{p} and \overline{q} , respectively, and m and n are the droop coefficients. P_n , Q_n , f_n , and V_n are the reference active power, reference reactive power, reference frequency, and reference voltage, respectively. The v_d and v_q are actual voltages
at the d avis and a avis, respectively. The inclusion and in age the reference survents at the at the d-axis and q-axis, respectively. The i_{dref} and i_{qref} are the reference currents at the d-axis and q-axis, respectively. The i_d and i_q are actual currents at the d-axis and q-axis, respectively. The k_{ip} and k_{ii} are proportionality and integration coefficients of the inner μ current loop. The relationship of the inner current loop is shown as (1).

$$
\begin{cases}\nv_d = -\left\{ k_{ip} \left(i_{dref} - i_d \right) + k_{ii} \int \left(i_{dref} - i_d \right) dt \right\} + \omega \, Li_q \\
v_q = -\left\{ k_{ip} \left(i_{qref} - i_q \right) + k_{ii} \int \left(i_{qref} - i_q \right) dt \right\} - \omega \, Li_d\n\end{cases} \tag{1}
$$

Actual AC voltage *vabc* and current *iabc* are used to obtain active power *p* and reactive power

Figure 3. Figure 3. The control flow of the bidirectional DC/AC converter. The control flow of the bidirectional DC/AC converter.

2.2. The Model of CPLs, PV Units, and Energy Storage Units

The current and voltage of CPLs are *i* and *v*, respectively. The power consumption of CPLs is P_0 , and the model of the CPLs is shown in Figure [4.](#page-4-0)

Figure 4. The model of CPLs. **Figure 4.** The model of CPLs. **Figure 4.** The model of CPLs.

CPLs is *P*0, and the model of the CPLs is shown in Figure 4.

The PV unit is modeled as a power source, and the power is PG, shown in Figure 5. The control strategy of the two photovoltaic power generation units on the AC side is the same. The output power of P_G is always positive, and is not negative.

Figure 5. The equivalent model of PV unit. **Figure 5.** The equivalent model of PV unit. **Figure 5.** The equivalent model of PV unit. **Figure 5.** The equivalent model of PV unit.

The batteries and cascaded buck-boost converter are modeled as a power source, and the power is P_b , shown in Fig[ur](#page-4-2)e 6. If P_b is positive, the batteries are discharging. On the contrary, if P_b is negative, the batteries are charging.

Figure 6. The equivalent model of batteries and cascaded buck-boost converter.

2.3. The Equivalent Model of Bidirectional DC/AC Converter in dq Rotating Frame

The Equivalent Helicky J Blueckellin B ByTE Benefith in the Federal S Frame
The traditional topology of a bidirectional DC/AC converter is shown in Figure 7 $e_{k(t)}$ ($k = a, b, c$) are the voltages of the three-phase AC source, L denotes the equivalent inductance of the line, R denotes the equivalent resistance of the line, C_{dc} is the DC capacitor, R_L is the resistive load, and v_{dc} denotes the DC bus voltage. The traditional topology of a bidirectional DC/AC converter is shown in Figure 7.

The switching function s_k is introduced and is defined as:

$$
s_k = \begin{cases} 1 \text{ the upper device} \\ 0 \text{ the lower device} \end{cases} \tag{2}
$$

The bidirectional DC/AC converter is expressed as:

$$
\begin{cases}\nL\frac{di_a}{dt} + Ri_a = e_a - (v_{dc}s_a + v_{NO}) \\
L\frac{di_b}{dt} + Ri_b = e_b - (v_{dc}s_b + v_{NO}) \\
L\frac{di_c}{dt} + Ri_c = e_c - (v_{dc}s_c + v_{NO})\n\end{cases}
$$
\n(3)

Figure 7. Traditional topology of the bidirectional DC/AC converter. **Figure 7.** Traditional topology of the bidirectional DC/AC converter.

The abc-dq coordinate transformation is utilized, and the transformation matrix is shown as follows:

$$
C_{3s2r} = \frac{2}{3} \left[\begin{array}{cc} \sin \omega t & \sin(\omega t - \frac{2\pi}{3}) & \sin(\omega t + \frac{2\pi}{3}) \\ \cos \omega t & \cos(\omega t - \frac{2\pi}{3}) & \cos(\omega t + \frac{2\pi}{3}) \end{array} \right] \tag{4}
$$

 T bidirectional DC/AC converter is expressed as: The mathematical model of the DC/AC converter is as follows:

$$
\begin{cases}\nC\frac{dv_{dc}}{dt} = \frac{3}{2} \left(i_q s_q + i_d s_d \right) - i_L \\
L\frac{di_d}{dt} - \omega L i_q + Ri_d = e_d - v_{dc} s_d \\
L\frac{di_q}{dt} + \omega L i_d + Ri_q = e_d - v_{dc} s_q\n\end{cases}
$$
\n(5)

 $v_q = s_qv_{dc}$, according to the principle of power conservation, and $i_q = 0$; then, the DC current is a photon of from (5) and is suppressed as follows: This paper considers the condition that the power factor is 1. Let $v_d = s_d v_{dc}$ and $v_q = s_q v_{dc}$, according to the principle of power conservation, and $i_q = 0$; then, the DC current i_0 is obtained from (5) and is expressed as follows:

$$
i_0 = \frac{v_d i_d}{v_{dc}}\tag{6}
$$

⁷, the equivalent model of a bidirectional in a dq rotating frame on the condition that the power factor is 1 is obtained and shown in
Figure 8 gure 7, the equivalent model of a bidirectional D

n the condition that the power factor is 1 is obtainend

n the condition that the power factor is 1 is obtainend

n Based on (6) and Figure 7, the equivalent model of a bidirectional DC/AC converter Based on (6) and Figur[e 7](#page-5-0), the equivalent model of a bidirectional DC/AC converter Fig[ur](#page-5-1)e 8.

dt **Figure 8.** The equivalent model of a bidirectional DC/AC converter in a dq rotating frame on the **Figure 8.** The equivalent model of a bidirectional DC/AC converter in a dq rotating frame on the *L Li Ri e v s* condition that the power factor is 1. condition that the power factor is 1.

d q d dc q dt + + =− *2.4. The Equivalent Model of Islanded AC/DC Hybrid Microgrids in DQ Rotating Frame 2.4. The Equivalent Model of Islanded AC/DC Hybrid Microgrids in DQ Rotating Frame*

Based on the equivalent models of Figures $4-6$ and 8 , the equivalent model of the islanded AC/DC microgrids in Figure [1](#page-2-0) is obtained and shown in Figur[e 9](#page-6-0). The PV unit is modeled as a power source, P_{G1} or P_{G2} . L_1 denotes the line equivalent inductance, and R_1 is the parasitic resistance of the line. R_L is the AC resistive load. L_s and C_s are the AC filter parameters of the bidirectional DC/AC converter, and *Rs* is the parasitic resistance of the parameters of the bidirectional DC/AC converter, and *R^s* is the parasitic resistance of the inductor. *C_{dc}* is the DC capacitor. The power consumption of the CPLs is P_0 . The power of the energy storage unit is P_b . The positive or negative values of P_b indicate the discharging or charging states of batteries. or charging states of batteries.

Figure 9. The equivalent model of islanded AC/DC hybrid microgrids. **Figure 9.** The equivalent model of islanded AC/DC hybrid microgrids.

The equivalent model of the islanded AC/DC hybrid microgrids in Figure 9 is only The equivalent model of the islanded AC/DC hybrid microgrids in Figure 9 is only
applicable to symmetrical systems. Unfortunately, when a SPGF occurs, islanded AC/DC hybrid microgrids are unsymmetrical. How to derive the equivalent model of islanded hybrid microgrids are unsymmetrical. How to derive the equivalent model of islanded AC/DC hybrid microgrids when SPGF occurs becomes a big issue. AC/DC hybrid microgrids when SPGF occurs becomes a big issue.

2.5. The Equivalent Model of Islanded Hybrid AC/DC Microgrids When SPGF Occurs 2.5. The Equivalent Model of Islanded Hybrid AC/DC Microgrids When SPGF Occurs

The method of symmetrical components describes unsymmetrical three-phase vectors that could be decomposed into three symmetrical vectors, and these symmetrical vectors are unique. Consequently, the method of symmetrical components is usually used for unsymmetrical systems to obtain symmetric components in positive-sequence, negative-unsymmetrical systems to obtain symmetric components in positive-sequence, negativesequence, and zero-sequence separately [35]. sequence, and zero-sequence separately [\[35\]](#page-21-11).

When a SPGF occurs, based on the method of symmetrical components, positivesequence components, zero-sequence components, and negative-sequence components of hybrid AC/DC microgrids are all derived, respectively. hybrid AC/DC microgrids are all derived, respectively.

As shown in Figure [9,](#page-6-0) when a SPGF occurs in phase A of *PG*1, the power injecting the ground fault point is *S^p* and is shown as follows:

$$
S_p = P_A + P_B + P_C = \text{Re}\left(\begin{bmatrix} V_A & V_B & V_C \end{bmatrix} \begin{bmatrix} I_A \\ I_B \\ I_C \end{bmatrix}^*\right) = V_P^T I_P^*
$$
(7)

In (7), V_A , V_B , and V_C are three-phase fault voltages, $V_P^T = [V_A \quad V_B \quad V_C]$, I_A , I_B , and I_C are three-phase fault injection currents, $I_P^T = [I_A \quad I_B \quad I_C]$, $S_p = \text{Re}[V_A I_A^* + V_B I_B^* + V_C I_C^*]$, T represents the matrix transpose, * represents the phase conjugate, and Re denotes the real part of *S*.

The conversion matrix from voltages to sequential components is shown as follows:

$$
\begin{bmatrix}\nV^P \\
V^N \\
V^0\n\end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & a & a^2 \\ 1 & a^2 & a \\ 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} V_a \\ V_b \\ V_a \end{bmatrix}
$$
\n(8)

The superscripts *P*, *N*, and 0 indicate positive-sequence, negative-sequence, and zerosequence, respectively; $a = e^{j120^\circ}$.

Based on (7), the relationships are obtained as follows:

$$
S_P = \text{Re}(V_A I_A^* + V_B I_B^* + V_C I_C^*) = \text{Re}\Big[3V^P(I^P)^* + 3V^N(I^N)^* + 3V^0(I^0)^*\Big] = P^P + P^N + P^0
$$
 (9)

Islanded hybrid AC/DC microgrids are often three-phase, three-wire systems; when a Islanded hybrid AC/DC microgrids are often three-phase, three-wire systems; when SPGF occurs in phase A of the PV unit, the voltage and current vectors of the grounding a SPGF occurs in phase A of the PV unit, the voltage and current vectors of the grounding point are shown in Figure [10.](#page-7-0) point are shown in Figure 10.

P N

0

Figure 10. The voltage and current vectors of the grounding point when phase A is grounded. **Figure 10.** The voltage and current vectors of the grounding point when phase A is grounded.

The voltages and currents of the SPGF point are as follows: The voltages and currents of the SPGF point are as follows:

$$
\begin{bmatrix} V_{af} \\ V_{bf} \\ V_{cf} \end{bmatrix} = \begin{bmatrix} 0 \\ \sqrt{3}V_a e^{j - \frac{5}{6}\pi} \\ \sqrt{3}V_a e^{j\frac{5}{6}\pi} \end{bmatrix}, \begin{bmatrix} I_{af} \\ I_{bf} \\ I_{cf} \end{bmatrix} = \begin{bmatrix} 3I_a e^{j - \frac{1}{2}\pi} \\ \sqrt{3}I_a e^{j - \frac{1}{3}\pi} \\ \sqrt{3}I_a e^{j - \frac{2}{3}\pi} \end{bmatrix}
$$
\nBased on (9), (10), the following case desired.

te following are defived. (10), the following are derived:

Based on (8)–(10), the following are derived:
\n
$$
\begin{bmatrix} V^P \\ V^N \\ V^0 \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & 1 & 1 \\ a^2 & a & 1 \\ a & a^2 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ \sqrt{3}V_A e^{j\frac{5}{6}\pi} \\ \sqrt{3}V_A e^{j\frac{5}{6}\pi} \end{bmatrix} = \begin{bmatrix} V_A e^{j0} \\ 0 \\ V_A e^{j\pi} \end{bmatrix}
$$
\n(11)

$$
\begin{bmatrix} I^P \\ I^N \\ I^0 \end{bmatrix} = \frac{1}{3} \begin{bmatrix} 1 & 1 & 1 \\ a^2 & a & 1 \\ a & a^2 & 1 \end{bmatrix} \begin{bmatrix} 3I_A e^{j - \frac{1}{2}\pi} \\ \sqrt{3}I_A e^{j - \frac{1}{3}\pi} \\ \sqrt{3}I_A e^{j - \frac{2}{3}\pi} \end{bmatrix} = \begin{bmatrix} \sqrt{2}I_A e^{j - \frac{3}{4}\pi} \\ I_A e^{j - \frac{1}{2}\pi} \\ 2I_A e^{j - \frac{1}{2}\pi} \end{bmatrix}
$$
(12)

The negative-sequence voltage of the SPGF point is zero, and consequently, the fault impulse power of the fault point is only related to the positive-sequence power P^P and zero-sequence power P^0 , and it is shown as follows: zero-sequence power *P*0, and it is shown as follows:

$$
S_p = 3\begin{bmatrix} V^P & V^N & V^0 \end{bmatrix} \begin{bmatrix} I^P \\ I^N \\ I^0 \end{bmatrix}^* = P^P + P^0 \tag{13}
$$

Based on (13) and the equivalent model in Figure [9,](#page-6-0) the positive-sequence equivalent model of the islanded AC/DC hybrid microgrids when a SPGF occurs is derived and shown in Figure $11.$ L_f is the equivalent inductance of the faulty branch, and C_f is the equivalent capacitance of the faulty branch. alent capacitance of the faulty branch. model of the islanded AC/DC hybrid microgrids when a SPGF occur

Figure 11. The positive-sequence equivalent model of islanded AC/DC hybrid microgrids when **Figure 11.** The positive-sequence equivalent model of islanded AC/DC hybrid microgrids when SPGF occurs. The red arrows in the figure indicate where SPGF occurs*,* and the green arrows indicate that P_{G1} and P_{G2} on the right are obtained from P_G equivalent.

Similarly, the zero-sequence equivalent model of the islanded AC/DC hybrid microgrid when a SPGF occurs is derived. The equivalent resistance in the faulty branch is *Rg*, the equivalent inductance in the faulty branch is L_g , the capacitance in the faulty branch is C_g , and the voltage is V_g^0 . Based on (13) and the equivalent model in Figure [9,](#page-6-0) the zero-sequence equivalent model of the islanded hybrid AC/DC microgrids is shown in Figure [12.](#page-8-0)

Figure 12. The zero-sequence equivalent model of islanded AC/DC hybrid microgrid when $SSPG$ SPGF occurs.

3. Large-Signal Stability Analysis of an Islanded AC/DC Hybrid Microgrid When SPGF Occurs

The mixed potential theory is utilized to analyze the stability of islanded hybrid \sim 5.600 m Then, the large-signal model and stability criterion for the positive-sequence equivalent moder in Figure II are derived. Foxt, a large signal moder and stability enteriormed [th](#page-8-0)e analyze the stability of its analyze the stability of the combined stability criteria for the islanded hybrid AC/DC microgrids when a SPGF occurs are obtained. AC/DC microgrids when a SPGF occurs. Firstly, this theory is simply demonstrated. model in Figure [11](#page-7-1) are derived. Next, a large-signal model and stability criterion for the

3.1. The Mixed Potential Function Theory

in Figure 11 are derived. Next, a large-signal model and stability of nonlinear systems [\[36\]](#page-21-12). The mixed and is widely used to analyze large-signal stability of nonlinear systems [36]. The mixed potential function is a special form of Lyapunov functions [\[37–](#page-21-13)[39\]](#page-21-14). For nonlinear circuits
points different structures, the unified form of mixed notatial function is as follows: The mixed potential theory was proposed by R.K. Rayton and J.K. Mercer in 1964 with different structures, the unified form of mixed potential function is as follows:

$$
P(i, v) = -A(i) + B(v) + (i, \gamma v - \alpha)
$$
\n(14)

In (14), *A*(*i*) is the current potential function, *B*(*v*) is the voltage potential function, and
 $(i, \gamma v - \alpha)$ denends on the system topology $(i, \gamma v - \alpha)$ depends on the system topology.

Verify the correctness of (14) by (15) .

$$
\begin{cases}\nL\frac{di_{\rho}}{dt} = \frac{\partial P(i,v)}{\partial i_{\rho}} \\
C\frac{dv_{\sigma}}{dt} = -\frac{\partial P(i,v)}{\partial v_{\sigma}}\n\end{cases}
$$
\n(15)

In (15), P is the mixed potential function, and L is the inductances in the circuit. C are the *Pive interial stability theorem of mixed potential theory is often utilized to derive large-
signal criteria. Further,* μ_1 *is the minimum eigenvalue of* $L^{-1/2}A_{ii}(i) L^{-1/2}$ *, and* μ_2 *is the* capacitances in the circuit, v_{σ} are the capacitance voltages, and i_{σ} are the inductance currents. The third stability theorem of mixed potential theory is often utilized to derive large-

minimum eigenvalue of $C^{-1/2}B_{uu}(u) C^{-1/2}$, $A_{ii}(i) = \partial^2 A(i)/\partial i^2$, $B_{vv}(v) = \partial^2 B(v)/\partial v^2$, $P_i = \partial P(i, v) / \partial i$, and $P_v = \partial P(i, v) / \partial v$.

If (16) is satisfied, then there exists a convergence region where the system trajectory converges to the steady-state equilibrium operating point.

$$
\mu_1 + \mu_2 \ge \delta, \delta > 0 \tag{16}
$$

3.2. Large Signal Model and Stability Criterion for Positive-Sequence Equivalent Model of Islanded Hybrid AC/DC Microgrids

Define currents and voltage variables according to the positive-sequence equivalent model in Figure [11.](#page-7-1)

$$
i = \begin{bmatrix} i_1^P & i_2^P & i_f^P \end{bmatrix}, v = \begin{bmatrix} v_s^P & v_{dc}^P & v_f^P \end{bmatrix}
$$
 (17)

The current function P_i^P associated with the current variables is as follows:

$$
P_i^P = -\frac{1}{2} \left(i_2^P \right)^2 R_1 - \frac{1}{2} \left(i_1^P \right)^2 R_s - v_d^P i_1^P + \int_0^{i_f^P} \frac{P_G}{i} di \tag{18}
$$

The voltage function P_v^P associated with the voltage variables is as follows:

$$
P_v^P = i_0^P v_{dc}^P - \int_0^{v_{dc}^P} i_0^P dv + P_b - \int_0^{v_{dc}^P} \frac{P_b}{v} dv - P_0 +
$$

$$
\int_0^{v_{dc}^P} \frac{P_0}{v} dv - \frac{1}{2} \frac{(v_s^P)^2}{R_L} - P^P + \int_0^{v_f^P} \frac{P^P}{v} dv
$$
 (19)

The powers of capacitors *Cs* , *Cdc*, and *C^f* are denoted as *PCs*, *PCdc*, and *PCf*, respectively, and shown as follows:

$$
\begin{cases}\nP_{C_S}^P = i_2^P v_s^P - (v_s^P)^2 / R_L - i_1^P v_s^P \\
P_{C_{dc}}^P = i_0^P v_{dc}^P - P_0 + P_b \\
P_{C_f}^P = i_f^P v_f^P - P_P - i_2^P v_f^P\n\end{cases}
$$
\n(20)

Combining (18)–(20), the mixed potential function $P(i^P, v^P)$ based on the positivesequence equivalent model in Figure [11](#page-7-1) is achieved and shown as follows:

$$
P(i^P, v^P) = -\frac{1}{2} (i_2^P)^2 R_1 - \frac{1}{2} (i_1^P)^2 R_s - v_d^P i_1^P - \int_0^{v_{dc}^P} \frac{P_b}{v} dv + \int_0^{v_{dc}^P} \frac{P_0}{v} dv + \int_0^{v_{dc}^P} \frac{P_0}{v} dv + \int_0^{v_f^P} \frac{P_C}{i} di + \frac{1}{2} \frac{(v_s^P)^2}{R_L} + \int_0^{v_f^P} \frac{P^P}{v} dv - \int_0^{v_{dc}^P} i_0^P dv - i_2^P v_s^P + i_1^P v_s^P - i_f^P v_f^P + i_2^P v_f^P
$$
\n
$$
(21)
$$

The current potential function $A(i^P)$ is as follows:

$$
A\left(i^{P}\right) = \begin{bmatrix} \frac{1}{2} \left(i_{1}^{P}\right)^{2} R_{s} + v_{d}^{P} i_{1}^{P} & 0 & 0\\ 0 & \frac{1}{2} \left(i_{2}^{P}\right)^{2} R_{1} & 0\\ 0 & 0 & -\int_{0}^{i_{f}^{P}} \frac{P_{G}}{i} di \end{bmatrix}
$$
(22)

The voltage potential function $B(v^P)$ is as follows:

$$
B(v^{P}) = \begin{bmatrix} \frac{1}{2} \frac{(v_{s}^{P})^{2}}{R_{L}} & 0 & 0\\ 0 & -\int_{0}^{v_{dc}^{P}} \frac{P_{b}}{v} dv + \int_{0}^{v_{dc}^{P}} \frac{P_{0}}{v} dv - \int_{0}^{v_{dc}^{P}} i_{0}^{P} dv & 0\\ 0 & 0 & \int_{0}^{v_{f}^{P}} \frac{P^{P}}{v} dv \end{bmatrix}
$$
(23)

In order to verify the correctness of (21), a derivation based on Figure [11](#page-7-1) and Equation (15) is carried out as follows.

$$
\begin{cases}\n\frac{dP(i,v)}{di_1^P} = -i_1^P R_s - v_d^P + v_s^P = L_s \frac{di_1^P}{dt} \\
\frac{dP(i,v)}{di_2^P} = -i_2^P R_1 - v_s^P + v_f^P = L_1 \frac{di_2^P}{dt} \\
\frac{dP(i,v)}{di_f^P} = \frac{P_G}{i_f^P} - v_f^P = L_f \frac{di_f^P}{dt} \\
\frac{dP(i,v)}{dv_f^P} = \frac{v_s^P}{R_L} - i_2^P + i_1^P = -C_s \frac{dv_s^P}{dt} \\
\frac{dP(i,v)}{dv_f^P} = \frac{p}{v_f^P} - i_f^P + i_2^P = -C_f \frac{dv_f^P}{dt} \\
\frac{dP(i,v)}{dv_{dc}^P} = -i_0^P - \frac{P_b}{v_{dc}^P} + \frac{P_0}{v_{dc}^P} = -C_{dc} \frac{dv_{dc}^P}{dt}\n\end{cases}
$$
\n(24)

Formula (24) satisfies (15), and the mixed potential function in (21) is correct.

According to (22) and (23), $A_{ii}(i^P)$ and $B_{vv}(v^P)$ are obtained, respectively, and are shown as follows: \mathbf{r} . *∂v^d* $\overline{1}$

$$
A_{ii}\left(i^P\right) = \begin{bmatrix} R_s + \frac{\sigma v_d}{\partial i_1} & 0 & 0\\ 0 & R_1 & 0\\ 0 & 0 & P_G/\left(i_f^P\right)^2 \end{bmatrix}
$$
(25)

$$
B_{vv}(v^P) = \begin{bmatrix} \frac{1}{R_L} & 0 & 0\\ 0 & \frac{P_b - P_0}{(v_{dc}^P)^2} & 0\\ 0 & 0 & -\frac{P^P}{(v_f^P)^2} \end{bmatrix}
$$
(26)

According to the closed-loop control strategy in (1), the partial derivative of v_d with respect to i_d is calculated and is as follows:

$$
\frac{\partial v_d}{\partial i_1} = \frac{\partial v_d}{\partial i_d} = k_{ip} + k_{ii}t
$$
\n(27)

In (27), *kip* and *kii* are proportional and integral parameters of the current inner loop for a bidirectional DC/AC converter. Furthermore, *kii* has unpredictable dynamic characteristics during dynamic procedures due to its own delay characteristics, and for simplification, (27) is transformed as follows:

$$
\frac{\partial v_d}{\partial i_1} = \frac{\partial v_d}{\partial i_d} = k_{ip} \tag{28}
$$

Actually, i_d in Figure [3](#page-3-1) and i_1 in Figure [11](#page-7-1) are the same. Based on (28), (25) is transformed into

$$
A_{ii}(i^P) = \begin{bmatrix} R_s + k_{ip} & 0 & 0 \\ 0 & R_1 & 0 \\ 0 & 0 & P_G / (i_f^P)^2 \end{bmatrix}
$$
 (29)

According to Figure [11,](#page-7-1) (26) and (29), the following are obtained and shown as follows:

$$
L^{-1/2}A(ii)L^{-1/2} = \begin{bmatrix} \frac{R_s + k_{ip}}{L_s} & 0 & 0\\ 0 & \frac{R_1}{L_1} & 0\\ 0 & 0 & \frac{P_G}{(i_f^P)^2 L_f} \end{bmatrix}
$$
(30)

$$
C^{-1/2}B(vv)C^{-1/2} = \begin{bmatrix} \frac{1}{R_L C_s} & 0 & 0\\ 0 & \frac{P_b - P_0}{(v_{dc}^P)^2 C_{dc}} & 0\\ 0 & 0 & -\frac{P^P}{(v_f^P)^2 C_f} \end{bmatrix}
$$
(31)

Based on mixed potential theory, μ_1 is the smallest eigenvalue of $L^{-1/2}A(ii) L^{-1/2}$, and μ_2 is the smallest eigenvalue of $C^{-1/2}B$ (*vv*) $C^{-1/2}$. μ_1 and μ_2 are as follows:

$$
\begin{cases}\n\mu_1 = \min \left[\frac{R_s + k_{ip}}{L_s} - \frac{R_1}{L_1} - \frac{P_G}{(i_f^p)^2 L_f} \right] \\
\mu_2 = \min \left[\frac{1}{R_L C_s} - \frac{P_b - P_0}{(v_{dc}^p)^2 C_{dc}} - \frac{P^p}{(v_f^p)^2 C_f} \right]\n\end{cases}
$$
\n(32)

According to (16), the large-signal stability criterion for a positive-sequence equivalent model of the islanded hybrid AC/DC microgrids is as follows:

$$
\min\left\{\frac{R_s + k_{ip}}{L_s}, \frac{R_1}{L_1}, \frac{P_G}{(i_f^p)^2} \right\} + \min\left\{\frac{1}{R_L C_s}, \frac{P_b - P_0}{(v_{dc}^p)^2 C_{dc}}, -\frac{P^P}{(v_f^p)^2 C_f}\right\} > 0 \tag{33}
$$

Based on (33), the simplified stability criterion is shown as follows:

$$
\min\left\{\frac{R_s + k_{ip}}{L_s}, \frac{P_G}{(i_f^P)^2 L_f}\right\} + \min\left\{\frac{P_b - P_0}{(v_{dc}^P)^2 C_{dc}}, -\frac{P^P}{(v_f^P)^2 C_f}\right\} > 0 \tag{34}
$$

To guarantee stable operations of the islanded hybrid AC/DC microgrids when a SPGF occurs, the positive-sequence stability criterion in (34) gives quantitative constraints on the positive-sequence power P_G of the PV unit, DC/AC converter current inner-loop proportional parameter *kip*, inductors *L^s* and *L^f* , inductor equivalent resistance *R^s* , energy storage unit power *P^b* , CPLs power *P*0, capacitors *C^d* and *C^f* , DC bus voltage *vdc*, and positive-sequence equivalent impulse power P^P of the SPGF.

3.3. Large Signal Model and Stability Criterion for Zero-Sequence Equivalent Model of Islanded Hybrid AC/DC Microgrids

The current and voltage variables are defined according to the zero-sequence equivalent model in Figure [12](#page-8-0) and are shown as follows:

$$
i = \begin{bmatrix} i_1^0 & i_3^0 \end{bmatrix}, v = \begin{bmatrix} v_s^0 & v_f^0 \end{bmatrix} \tag{35}
$$

The power function P_i^0 associated with the current variables is as follows:

$$
P_i^0 = -\frac{1}{2} \left(i_1^0 \right)^2 R_1 - \frac{1}{2} \left(i_2^0 \right)^2 R_2 - \frac{1}{2} \left(i_3^0 \right)^2 R_g \tag{36}
$$

The power function P_v^0 associated with the voltage variables is as follows:

$$
P_v^0 = -P^0 + \int_0^{v_f^0} \frac{P^0}{v} dv \tag{37}
$$

The power $P^0{}_{Cs}$, $P^0{}_{Cd}$, $P^0{}_{Cf}$, and $P^0{}_{Cg}$ of capacitors C_s , C_{dc} , C_f , and C_g are as follows:

$$
\begin{cases}\nP_{v_c}^0 = i_1^0 v_c^0 \\
P_{v_g}^0 = i_3^0 v_s^0 \\
P_{v_s}^0 = i_2^0 v_s^0 - i_1^0 v_s^0 \\
P_{v_f}^0 = P_0^0 - i_2^0 v_f^0 - i_3^0 v_f^0\n\end{cases} \tag{38}
$$

Combining (36)–(38), the mixed potential function $P(t^0, v^0)$ based on the zero-sequence equivalent model in Figure [12](#page-8-0) is achieved and shown as follows:

$$
P(i^{0}, v^{0}) = -\frac{1}{2} (i_{1}^{0})^{2} R_{1} - \frac{1}{2} (i_{2}^{0})^{2} R_{2} - \frac{1}{2} (i_{g}^{0})^{2} R_{g} + \int_{0}^{v_{f}^{0}} \frac{p^{0}}{v} dv + i_{1}^{0} (v_{c}^{0} - v_{s}^{0}) + i_{2}^{0} (v_{s}^{0} - v_{f}^{0}) + i_{g}^{0} (v_{g}^{0} - v_{f}^{0})
$$
\n
$$
(39)
$$

Similarly, the large-signal stability criterion for a zero-sequence equivalent model of the islanded hybrid AC/DC microgrids is derived and shown as follows:

$$
\min\left\{\frac{R_1}{L_1}, \frac{R_g}{L_g}\right\} - \frac{P^0}{\left(v_f^0\right)^2 C_f} > 0\tag{40}
$$

The constraints in (40) are related to the line equivalent resistance R_1 , line equivalent inductance *L*1, capacitance *C^f* , the equivalent inductance *L^g* in the faulty branch, the equivalent resistance R_g in the faulty branch, and zero-sequence equivalent impulse power $\mathrm{P}^0.$

3.4. Combined Stability Criteria for Islanded Hybrid AC/DC Microgrids When SPGF Occurs

When a SPGF occurs, based on (34) and (40), the large-signal stability criteria of the islanded hybrid AC/DC microgrids are obtained and shown as follows:

$$
\begin{cases} \min\left\{\frac{R_s+k_{ip}}{L_s},\frac{P_G}{(i_f^p)^2L_f}\right\} + \min\left\{\frac{P_b-P_0}{(v_{dc}^p)^2C_{dc}},-\frac{P^p}{(v_f^p)^2C_f}\right\} > 0\\ \min\left\{\frac{R_1}{L_1},\frac{R_s}{L_s}\right\} - \frac{P^0}{(v_f^0)^2C_f} > 0 \end{cases}
$$
(41)

When a SPGF occurs, the maximum CPL power P_0 that the islanded hybrid AC/DC microgrids could stably support is derived from (41) and shown in (42).

$$
P_0 < \left[\min \left(\frac{R_s + k_{ip}}{L_s}, \frac{P_G}{\left(i_f^P \right)^2 L_f} \right) \right] \left(v_{dc}^P \right)^2 C_{dc} + P_b \tag{42}
$$

According to (42) , if the variable CPLs power P_0 is always lower than the maximum power, islanded hybrid AC/DC microgrids stably operate as a SPGF occurs. In addition, with an increase in the PV unit power P_G , energy storage unit discharging power P_b , the current inner-loop proportional parameter *kip* of the bidirectional DC/AC converter and DC capacitor *Cdc*, the maximum CPL power that the islanded hybrid AC/DC microgrids could support increases. On the contrary, when the filter inductors *L^s* and *L^f* increase, the maximum CPL power that islanded hybrid AC/DC microgrids could support decreases. When a SPGF occurs, if the CPL power increases, in order to guarantee stable operation, the discharging power of the energy storage unit is increased. Unfortunately, if the discharging power of the energy storage unit cannot increase, load shedding appears. Inequality (42) provides important stable operation constants of the islanded hybrid AC/DC microgrids when a SPGF occurs.

Based on (41), to guarantee the stable operation of islanded hybrid AC/DC microgrids, the maximum positive-sequence impulse power and the maximum zero-sequence impulse power of a SPGF are also deduced and shown as follows:

$$
\left\{\n\begin{array}{l}\nP^P < \left[\min\left\{\frac{R_s + k_{ip}}{L_s}, \frac{P_G}{\left(\frac{P}{f_f}\right)^2 L_f}\right\}\right] \left(v_f^P\right)^2 C_f \\
P^0 < \left[\min\left\{\frac{R_1}{L_1}, \frac{R_s}{L_g}\right\}\right] \left(v_f^0\right)^2 C_f\n\end{array}\n\right.\n\tag{43}
$$

According to (43), if the positive-sequence impulse power P^P and zero-sequence impulse power *P* ⁰ are always lower than the maximum values, islanded hybrid AC/DC microgrids operate stably. In addition, with an increase in the current inner-loop proportional parameter *kip* of the bidirectional DC/AC converter, inductor equivalent resistance R_s , and the positive-sequence power P_G of PV unit, the maximum positive-sequence impulse power that islanded hybrid AC/DC microgrids could stably suffer increases. On the contrary, when filter inductors *L^s* and *L^f* increase, the maximum positive-sequence impulse power that islanded hybrid AC/DC microgrids could stably suffer decreases. Similarly, with an increase in filter capacitor C_f , line equivalent resistance R_1 , and inductor equivalent internal resistance R_g , the maximum zero-sequence impulse power that islanded hybrid AC/DC microgrids could stably suffer increases. On the contrary, when line equivalent inductance L_1 increases, the zero-sequence impulse power that islanded hybrid AC/DC microgrids could stably suffer decreases.

According to (13), the fault impulse power $S_p = P^P + P^0$. Consequently, the maximum impulse power of a SPGF that islanded hybrid AC/DC microgrids could stably suffer is derived from (43). It is concluded that inequality (43) offers extremely important guidelines for protection equipment.

4. Simulation Validation

To verify the correctness of the proposed large-signal stability criteria in (41), according to Figure [1,](#page-2-0) a simulation model of the islanded hybrid AC/DC microgrids is constructed using the MATLAB R2020a/Simulink software. *Q/V* and *P/f* droop controls are used for the bidirectional DC/AC converter. PI control strategies are utilized in the outer voltage controller and inner current controllers of the batteries' cascaded DC/DC converter. The CPLs are modeled by a closed loop-controlled buck converter and a resistor. DC/DC converters cascaded with AC/DC converters are utilized in PV units to achieve the power output. When a SPGF occurs in a PV unit, the fault impulse power is injected at the fault point. This procedure is simulated by sudden load power steps.

The simulation parameters of the islanded hybrid AC/DC microgrids are shown in Table [1.](#page-13-0) The DC bus voltage is 650 V, the AC bus voltage is 311 V, the initial power of the CPLs is 30 kW, and the power of the PV power generation unit is 5 kW.

Table 1. Simulation parameters of islanded hybrid AC/DC microgrids.

According to (41) and Table [1,](#page-13-0) the large-signal stability criteria of the islanded hybrid AC/DC microgrids when a SPGF occurs are as follows: AC/DC microgrids when a SPGF occurs are as follows: AC/DC microgrids when a SPGF occurs are as follows:

$$
\frac{R_s + k_{ip}}{L_s} - \frac{P^P}{(v_f^P)^2 C_f} + \frac{R_1}{L_1} - \frac{P^0}{(v_f^0)^2 C_f} > 0
$$
\n(44)

Then, the maximum fault impulse power of a SPGF that the islanded hybrid AC/DC microgrids could stably suffer is 38.37 kW and is shown as microgrids could stably suffer is 38.37 kW and is shown as microgrids could stably suffer is 38.37 kW and is shown as

$$
P^P + P^0 < 38.37 \, \text{kW} \tag{45}
$$

In order to verify the correctness of (45), the parameters of the two groups are designed and shown in Table [2.](#page-14-0) Group A satisfies (45), while Group B does not.

Table 2. The parameters of simulations A and B. **Table 2.** The parameters of simulations A and B. **Table 2.** The parameters of simulations A and B.

At 0.5 s, a SPGF appears in the AC side of the islanded hybrid AC/DC microgrids. To simulate this procedure, the power steps of Group A are from 3 kW to 35 kW, and the power steps of Group B are from 3 kW to 40 kW.
When parameters in Group A are utilized, the waveforms of power steps, energy steps, energy steps, energy step \overline{A} s, a SPGF appears in the islanded hybrid \overline{A} At 0.5 s, a SPGF appears in the AC side of the islanded hybrid AC/DC microgrids.

When parameters in Group A are utilized, the waveforms of power steps, energy storage unit power, DC bus voltage, and AC bus voltage are shown in Figures [13](#page-14-1)[–16.](#page-15-0) As the fault impulse power of a SPGF increases from 3 kW to 35 kW , tend to stabilize after power step, and the charging state of the energy storage unit is changed into a discharging state. After a drop, the DC bus voltage recovers to 650 V at 0.9 s, and the AC bus voltage state. After a drop, the DC bus voltage recovers to 650 V at 0.9 s, and the AC bus voltage also returns to 311 V at 0.6 s behind a small fluctuation. Figures $13-16$ $13-16$ illustrate that the parameters of Group A could guarantee islanded hybrid AC/DC microgrid stability when parameters of Group A could guarantee islanded hybrid AC/DC microgrid stability when a SPGF occurs. a SPGF occurs. also returns to 311 V at 0.6 s behind a small fluctuation. Figures 13–16 illustrate that the also returns to 311 V at 0.6 s behind a small fluctuation. Figures 13–16 illustrate that the parameters of G

Figure 13. The waveforms of power steps when Group A is utilized. **Figure 13.** The waveforms of power steps when Group A is utilized. **Figure 13.** The waveforms of power steps when Group A is utilized.

Figure 14. The power of energy storage unit when Group A is utilized.

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Figure 15. The waveform of DC bus voltage when Group A is utilized. **Figure 15.** The waveform of DC bus voltage when Group A is utilized. **Figure 15.** The waveform of DC bus voltage when Group A is utilized.

Figure 16. The waveform of AC bus voltage when Group A is utilized. **Figure 16.** The waveform of AC bus voltage when Group A is utilized.

Then, when the parameters in Group B are adopted, the waveforms of power steps, energy storage unit power, DC bus voltage, and ΔC bus voltage are shown in Figures 17–20 Δ s the fault impulse power of the SPCF increases from $3 \mu W$ to 40 kW, violent oscillations appear in power steps and energy storage unit power, and the DC bus voltage oscillates significantly between 2 kV and $\overline{0}V$ At $\overline{0.5}$ s, slight oscillations present in the AC voltage \overline{A} 0.6 s, the AC voltage is seriously distorted and cannot restore stability. Figures 17–20 demonstrate that the parameters of Group B could not guarantee islanded hybrid AC/DC microgrid stability when a SPGF occurs. Then, when the parameters in Group B are adopted, the waveforms of power steps, Then, when the parameters in Group B are adopted, the waveforms of power steps, energy storage unit power, DC bus voltage, and AC bus voltage are shown in Figures [17–](#page-15-1)[20.](#page-16-0) As the fault impulse power of the SPGF increases from 3 kW to 40 kW , violent oscillations appear in power steps and energy storage unit power, and the DC bus voltage oscillates significantly between 2 kV and 0 V . At 0.5 s , slight oscillations present in the AC voltage. At 0.6 s, the AC voltage is seriously distorted and cannot restore stability. Figures $17-20$ $17-20$ demonstrate that the parameters of Group B could not guarantee islanded hybrid AC/DC
misrooxid etability when a SPCE acquee microgrid stability when a SPGF occurs.
 \Box

The simulation result comparisons of Figures [13–](#page-14-1)[20](#page-16-0) indicate that the parameters satisfying (45) could guarantee islanded hybrid AC/DC microgrid stability when a SPGF occurs, while parameters not satisfying (45) could not ensure islanded hybrid AC/DC microgrid stability when a SPGF occurs. The simulation results verify the validity of the derived large-signal stability criteria in (41).

Figure 17. The waveform of power steps when Group B is utilized. **Figure 17.** The waveform of power steps when Group B is utilized.

Figure 18. The power of energy storage unit when Group B is utilized.

Figure 19. The waveform of DC bus voltage when Group B is utilized. **Figure 19.** The waveform of DC bus voltage when Group B is utilized. **Figure 19.** The waveform of DC bus voltage when Group B is utilized.

Figure 20. The waveform of AC bus voltage when Group B is utilized.

isfying (45) could guarantee islanded hybrid AC/DC microgrid stability when a SPGF oc-**5. Experimental Validation**

To verify the proposed large-signal stability criteria in (41), according to Figure 1, a prototype of the islanded hybrid AC/DC microgrids is built using the PTS-5000 experimental platform, Good Will Instrument (Suzhou) Co., Ltd., Suzhou, China, as shown in Figure 21.
The augustine antal platforms aggride of the east pollog TMS220E2822E, DC /DC expressions. **ISOR EXPERIMENTAL PROCESS OF THE COMMONSTRACT LOCCUPY OF THE CONFERENCE**, DC/AC converters, constant power loads (CPLs), AC resistive load, the power source, and are osembeope. Droop controls are used for the bidirectional DC/TC conventers, outer voltage controllers and inner current controllers are utilized in battery-cascaded DC/DC prototype of the internal details in the internal details in the internal detailed in the power series are also utilized to perform the impulse power of a SPGF. The parameters of the experimental prototype are shown in Table 3. the oscilloscope. Droop controls are used for the bidirectional DC/AC converters. Outer The experimental platform consists of the controller TMS320F28335, DC/DC converters, DC/AC converters, constant power loads (CPLs), AC resistive load, the power source, and for the PV unit is represented by the power source. Load power steps are also

Figure 21. Experimental platform of AC/DC hybrid microgrids with CPLs. **Figure 21.** Experimental platform of AC/DC hybrid microgrids with CPLs.

Table 3. Experimental parameters of islanded hybrid AC/DC hybrid microgrids. **Table 3.** Experimental parameters of islanded hybrid AC/DC hybrid microgrids.

the islanded hybrid AC/DC microgrids could stably suffer is 91.37 W. The parameters of the two groups are also designed and shown in Table 4. The powers of Group C are less than 91.37 W, while the powers of Group D are more than 91.37 W. Similarly, based on (41) and Table [3,](#page-17-1) the maximum fault impulse power of a SPGF that

 $\frac{1}{3}$ which $\frac{1}{3}$ while the powers of Group D are more than 91.37 W. $\frac{1}{3}$ **Table 4.** The experimental parameters of groups C and D.

and current are shown in Figure 22, and the DC bus voltage and the energy storage current are sho[wn i](#page-18-1)n Figure 23. As the fault impulse power of the SPGF increases from 30 W to 80 W, the AC bus voltage maintains stable operation after a small fluctuation, and the DC bus voltage also returns to 60 V after a transient drop, while the output current of the energy storage unit increases and [the](#page-18-1)n operates steadily. The results in Figures 22 and 23 coincide with the simulation result[s in](#page-18-1) Figures $13-16$. Figures 22 and 23 illustrate that the parameters of Group C could guarantee that islanded hybrid AC/DC microgrids are stable when a SPGF occurs. When the parameters in Group C are utilized, the waveforms of the AC bus voltage

Figure 22. Waveforms of AC bus voltage and current when Group C is utilized. **Figure 22.** Waveforms of AC bus voltage and current when Group C is utilized. **Figure 22.** Waveforms of AC bus voltage and current when Group C is utilized. **The current when Group C** is utilized. **The current when Group C** is utilized. **The current when Group C** is utilized. **The current when Group**

Then, when the parameters is $\frac{1}{2}$ are adopted, the waveforms of the $\frac{1}{2}$ are $\frac{1}{2}$ are $\frac{1}{2}$ and $\frac{1}{2}$ Figure 23. Waveforms of DC bus voltage and the energy storage current when Group C is utilized.

Then, when the parameters in Group D are adopted, the waveforms of the AC bus voltage and current are shown in Figure 24 , and the DC bus voltage and the energy storage current are shown in Fi[gure](#page-19-0) 25. As the fault impulse power of the SPGF increases from 30 $\breve{\text{W}}$
to 100 W serious dist[orti](#page-19-0)ons appear in the AC bus voltage and current waveforms, while to 100 W, serious distortions appear in the AC bus voltage and current waveforms, while
the DC bus voltage drops from 60 V to 49.51 V and does not recover to 60 V. The results in
Figures 24 and 25 coincide with the simula the DC bus voltage drops from 60 V to 49.51 V and does not recover to 60 V. The results in Figures [24](#page-18-2) and [25](#page-19-0) coincide with the simulation results in Figures [17–](#page-15-1)[20.](#page-16-0) Figures 24 and 25 demonstrate that the parameters of Group D could not guarantee islanded hybrid AC/DC microgrid stability when a SPGF occurs. $\frac{24}{4}$ and $\frac{25}{4}$ demonstrate that the parameters of $\frac{25}{4}$ could not guarantee islamically set $\frac{25}{4}$.

Figure 24. Waveforms of AC bus voltage and current when Group D is utilized. **Figure 24.** Waveforms of AC bus voltage and current when Group D is utilized.

Figure 25. Waveforms of DC bus voltage and the energy storage current when Group D is utilized. **Figure 25.** Waveforms of DC bus voltage and the energy storage current when Group D is utilized.

The experimental result comparisons of Figure[s 22](#page-18-0)[–25](#page-19-0) illustrate that the parameters The experimental result comparisons of Figures 22–25 illustrate that the parameters satisfying (41) could guarantee the islanded hybrid AC/DC microgrid's stability when a satisfying (41) could guarantee the islanded hybrid AC/DC microgrid's stability when a SPGF occurs, while the parameters not satisfying (41) could not ensure the islanded hybrid AC/DC microgrid's stability when a SPGF occurs. These experimental results show the correctness of the derived large-signal stability criteria in (41).

The experimental results and simulation results all prove the validity of the proposed The experimental results and simulation results all prove the validity of the proposed large-signal stability criteria of the islanded hybrid AC/DC microgrids when a SPGF occurs, as shown in (41).

6. Conclusions 6. Conclusions

Firstly, based on the structure and control strategies, the method of symmetrical components is utilized, and the positive-sequence equivalent model and zero-sequence equivalent model of islanded AC/DC hybrid microgrids when a SPGF occurs are derived $\overline{\text{m}}$ separately. Then, the mixed potential theory is utilized, and large-signal stability crite-
in the mixed potential theory is utilized, and large-signal stability criteria of the islanded hybrid AC/DC microgrids when a SPGF occurs are obtained. The proposed stability criteria in (41) give quantitative constraints on the positive-sequence proposed stability criteria in (41) give quantitative constraints on the positive-sequence power *P_G* of the PV unit, DC/AC converter current inner-loop proportional parameter k_{ip} , inductors L_s and L_f , inductor equivalent resistance R_s , energy storage unit power P_b , CPLs power P_0 , capacitors C_d and C_f , DC bus voltage v_{dc} , the line equivalent resistance R_1 , ance equivalent inductance L_1 , capacitance C_f , the equivalent inductance L_g in the faulty branch, the positive-sequence equivalent resistance *R*g in the faulty branch, the positive-sequence equivalent impulse power *P*P of impulse power *P* ^P of SPGF, and zero-sequence equivalent impulse power *P* 0 . The stability influences of these parameters are sufficiently analyzed, and the maximum power of the parameters are sufficiently analyzed, and the maximum power of the CPLs that islanded CPLs that islanded hybrid AC/DC microgrids could stably support as the SPGF occurs is hybrid AC/DC microgrids could stably support as the SPGF occurs is obtained. Simulta-obtained. Simultaneously, to guarantee the stable operation of islanded hybrid AC/DC neously, to guarantee the stable operation of islanded hybrid AC/DC microgrids, the max-microgrids, the maximum impulse power of a SPGF is also deduced. Finally, simulation and experimental validations are performed, and prove the correctness of the derived large-signal stability criteria. line equivalent inductance *L*1, capacitance *C^f* , the equivalent inductance *L*^g in the faulty

The derived maximum impulse power of a SPGF that the islanded hybrid AC/DC microgrids could stably suffer is extremely important and provides quantitative guidelines for protection equipment to decide when to work. Furthermore, if the impulse power of a SPGF is less than the maximum power, islanded hybrid AC/DC microgrids could work under a SPGF. Then, the allowable maximum CPL power that islanded hybrid AC/DC microgrids could stably support as the SPGF occurs is deduced, and the power is usually adopted to determine the power of the energy storage unit and load shedding in advance.

The presented large-signal stability criteria of islanded hybrid AC/DC microgrids when a SPGF occurs are essential for achieving power allocation and guaranteeing sta-The presented large-signal stability criteria of islands of islands of islands and islands are controlled in the stability criteria of islands and is a stability controlled in the stability criteria of islands and in the s ble operation.

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