

*Article*



# **Experimental Evaluation of Vibrational Stability of SOPs in Aerospace Industry Using PCB Strain Effectiveness of a PCB-Strain-Based Design Methodology**

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**Abstract:** Steinberg's theory, which is based on the fatigue failure theory, has been widely used for predicting the structural safety of solder joints in aerospace electronic units under vibration during launches. However, theoretical limitations are encountered when evaluating the structural safety of highly integrated electronic packages mounted on printed circuit boards (PCBs) under various boundary conditions. Therefore, in our previous study, a PCB-strain-based methodology was proposed to overcome the technical limitations of the conventional Steinberg theory, and its effectiveness was validated by conducting fatigue life tests on various types of specimens, such as the ball grid array, column grid array, and quad flat package. In this study, the aim was to increase its completeness and reliability by targeting small outline packages (SOPs) that have not yet been considered. The finite element (FE) model of the SOP was proposed to guarantee the reliable prediction of the structural safety of the solder joints used in the PCB-strain-based methodology. The proposed modeling technique contributes to enable the rapid construction of an FE model for the electronic unit because it was greatly simplified into a zero-dimensional lumped mass and rigid link element to simulate the package mass and solder joint, respectively. The effectiveness of the methodology was validated by performing fatigue life tests on PCB specimens under various boundary conditions. Those experimental and analytical results indicated that the proposed methodology was much more effective in predicting the structural safety of a solder joint for various cases of tested specimens compared with the Steinberg's theory. The simplified FE model of SOP with the rigid link element connected to six points on the package mounting area of the PCB was effective for estimating margin of safety of solder joint. The results of this study would contribute to increase the availability of the proposed methodology for rapid mechanical design of electronic units in aerospace industries.

**Keywords:** aerospace industry; electronic unit; random vibration; structural safety; PCB strain; structural design; small outline package (SOP)

# **1. Introduction**

Similar to the electronic units employed in different engineering fields, electronic units developed in aerospace industries are exposed to various extreme environmental loads during a mission [\[1\]](#page-22-0). These environments involve launch vibration, on-orbit thermal cycling, and space radiation. Generally, the extreme level of mechanical vibration during a launch campaign is the first major environment that onboard electronics are exposed to during a mission. These loads involve quasi-static loads, sine vibration, random vibration, and shock. Among these, random vibration excitation occurs over a wide frequency band from 20 to 2000 Hz and is one of the main causes of the mechanical failure of spaceborne electronic units [\[2\]](#page-22-1). The probability of fatigue failure in the solder joints of electronic packages increases, owing to the repetitive bending behavior of printed circuit



**Citation:** Park, T.-Y.; Park, J.-H.; Oh, H.-U. Experimental Evaluation of Vibrational Stability of SOPs in Aerospace Industry Using PCB Strain Effectiveness of a PCB-Strain-Based Design Methodology. *Aerospace* **2023**, *10*, 516. [https://doi.org/10.3390/](https://doi.org/10.3390/aerospace10060516) [aerospace10060516](https://doi.org/10.3390/aerospace10060516)

Academic Editor: Ephraim Suhir

Received: 28 February 2023 Revised: 30 April 2023 Accepted: 18 May 2023 Published: 30 May 2023



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boards (PCBs) under vibration excitation. This can lead to the malfunctioning or failure of electronics, and in extreme cases, the failure of the spacecraft.

Various fatigue life prediction approaches based on fatigue failure theories have been studied to enhance the structural safety of solder joints against vibration-induced fatigue failure [\[3](#page-22-2)[–5\]](#page-22-3). However, most of these approaches are based on the detailed finite element (FE) modeling of the various types of electronic packages. Additionally, although these modeling techniques detail the actual configurations of the packages and solder joints, they are time-consuming. This is because the construction and computation of the FE models of the electronic unit grow cumbersome as the number of packages and their types increase. A more fundamental problem is the difficulty in acquiring all the required accurate information on the material properties, geometry of the solder joint, and internal configuration of the package when they are either absent or kept confidential by the manufacturer. In general, the mechanical design of electronic units requires iterative design and analysis processes based on trade-off studies to obtain a final design that satisfies different development requirements. However, the rapid evaluation of structural safety reduces the number of design trade-off studies, which contributes to the minimization of project resources such as the schedule, manpower, and cost. From this perspective, these time-consuming life prediction approaches based on detailed FE models are not appropriate for the rapid design of spaceborne electronic units with different package types.

As a design methodology for spaceborne electronics, industries and research organizations in the space sector have widely adopted Steinberg's fatigue failure theory proposed in the 1970s [\[6\]](#page-22-4). This theory guarantees more than 2  $\times$  10<sup>7</sup> fatigue cycles for solder joints exposed to random vibration when the maximum PCB dynamic displacement is limited to lower than the allowable value calculated using Steinberg's empirical formula. The concept of this theory allows the use of simplified modeling techniques for electronic packages because the board displacement is calculated with reasonable accuracy, even if the package configuration in the FE model is not sufficiently detailed [\[7](#page-22-5)[,8\]](#page-22-6). However, Steinberg's formula was created with the assumption that PCBs are rectangular-shaped and have a simple half-sine mode shape under simply supported boundary conditions. Therefore, if a PCB has a complex distorted mode shape, this assumption causes an estimation error in the margin of safety (MoS) of solder joints due to the asymmetric board shape and irregular positions of the fixation points. Another problem is that the criterion of 2  $\times$  10<sup>7</sup> fatigue cycles for random vibration creates a huge margin between the fatigue life of solder joints and the total number of cycles accumulated during the vibration tests and launch. These are the major issues related to the structural overdesigning of the electronic unit.

In response to these limitations, Park and Oh [\[9\]](#page-22-7) proposed a critical strain-based structural design methodology called "Oh–Park methodology" to evaluate the structural safety of solder joints in spaceborne electronic units based on the critical strain theory. Its key approach is to evaluate the structural safety of solder joints based on a MoS calculation with respect to the PCB strain and considering random vibrations that occur during launch. In addition, the *MoS* can be calculated in accordance with the required time to failure (*TTFreq*) for the survival of solder joints in on-ground vibration tests and during launch. These approaches mitigate the limitations of Steinberg's theory such as the unreliable structural safety evaluation and possibility of structural overdesign. Park and Oh [\[9\]](#page-22-7) proposed simplified FE modeling techniques for the ball grid array (BGA) and column grid array (CGA) packages, which only consist of a zero-dimensional (0D) lumped mass and rigid link elements. This technique effectively reduced the time and effort required to model the package while being reasonably accurate at evaluating the solder joint's structural safety. For evaluating the effectiveness of the Oh–Park methodology, plastic BGA and ceramic CGA package specimens with various PCB boundary conditions were fabricated and exposed to random vibration excitation. Using a comparison of the *MoS* of the solder joints and TTF of the tested specimens, they proved that the results of their proposed method were consistent with the test results. In contrast, Steinberg's theory generally yielded invalid results for these specimens.

In a previous study [\[9\]](#page-22-7), the Oh–Park methodology was validated only for the area array packages of the BGA and CGA. However, lead frame packages such as the small outline package (SOP) and quad flat package (QFP), wherein the lead frames and solder joints are arranged along the outer perimeter of the package body, have also been widely used in spaceborne electronic units. The gull-wing-shaped configuration of the lead frame array provides increased mechanical flexibility compared with the solder joints of area array packages. This configuration reduces the thermal deformation and vibrationinduced stress acting on the solder and lead frame itself. However, the lead frame package is still vulnerable to fatigue failure in the solder joint when exposed to high-vibrationinduced board deflection. To increase the completeness of the Oh–Park methodology in the structural design of spaceborne electronic units, further verification using lead-frame types of packages is required in addition to those considered by Park and Oh in [\[9\]](#page-22-7).

The aim of this study is to validate the effectiveness of the Oh–Park methodology in evaluating the structural safety of SOP solder joints; this has not been previously investigated. One important goal of this study is to find a feasible simplified FE modeling technique for SOPs by using the 0D lumped mass and rigid link elements. This form of modeling has not been proposed for SOPs in previous studies. The elements used in this model are so simple that it is advantageous for saving significant time and effort to model the package rapidly. The new FE modeling technique for SOP is the core of the novelty of this study. A total of 48-pin thin SOP (TSOP48) specimens are fabricated and mounted on PCBs under various boundary conditions before being exposed to random vibration excitation on a vibration shaker. The time to failure (TTF) of the tested packages is then compared with their *MoS* values, which are calculated using the Oh–Park methodology and based on various forms of simplified FE modeling techniques. The proposed method is also compared with the conventional Steinberg's theory. The verification results indicate that the application of the Oh–Park methodology using the proposed FE modeling technique is a feasible method for evaluating the structural safety of SOP solder joints.

#### **2. Structural Design Methodologies**

Figure [1](#page-3-0) shows the structural design processes using the Oh–Park methodology and Steinberg's theory for spaceborne electronic units. The major equations that constitute these methodologies are described throughout this chapter to describe their theoretical differences.

In the first step of evaluation (Step 1), the FE model of the electronic units is constructed by applying the simplified FE modeling techniques for the various types of electronic packages such as the BGA, CGA, and lead frame packages. The electronic package FE modeling techniques are detailed in Section [4.](#page-11-0) In Step 2, the modal analysis of the constructed FE model is performed to identify the fundamental PCB modes with respect to the different locations on the board where each package is mounted.

Steps 3–1 to 6–1 describe the design evaluation process using Steinberg's theory. In Step 3–1, the modal analysis results obtained in Step 2 are used to find the mode that is most relevant to the maximum dynamic response of the board at the package mounting position. This is the most important process to properly estimate an allowable PCB displacement *Zallow* for the package in Step 4–1 using Equation (1) shown below [\[6\]](#page-22-4):

$$
Z_{allow} = \frac{0.00022B}{Ctr\sqrt{L}},\tag{1}
$$

where *Zallow* is allowable PCB displacement [in.] (converted to mm after estimation). *B* is length of the PCB boundary parallel to the package [in.]. *C* is constant for the electronic packaging type. *t* is PCB thickness [in.]. *r* is relative position factor of the package on the PCB calculated by geometric factors *x*, *y*, *X*, and *Y* defined in Figure [2a](#page-3-1). Here, *X* or *Y* value becomes the value of a length of PCB parallel to the package *B* [in.]. *L* is package body length [in.].

<span id="page-3-0"></span>

Figure 1. Structural design evaluation processes based on the Steinberg theory and Oh–Park methododology for spaceborne electronic units. ology for spaceborne electronic units.

<span id="page-3-1"></span>

 $\Gamma$ tion (3). Definitions of fectors in Equation (1), (a) fector widely fectors  $\overline{R}$  tend  $\overline{R}$  $t_{\text{c}}$  for  $t_{\text{c}}$  is the value of the value of  $t_{\text{c}}$  is typically taken as vibration, the value of  $t_{\text{c}}$  which as Figure 2. Definitions of factors in Equation  $(1)$ : (a) factor  $\kappa$  (b) factors  $\kappa$ **Figure 2.** Definitions of factors in Equation (1): (**a**) factor  $r$ , (**b**) factors *B*,  $t$  and *L*.

In Step 5–1, random vibration analysis is performed on the electronic unit using the input random spectrum equal to the qualification test's full level (0 dB). In Step 6–1, the analyzed maximum PCB displacement (*Zmax*) for each package, which is the three-sigma (3σ) value of the root mean square (RMS) displacement, is obtained at the PCB location underneath the center of the package body defined as shown in Figure [2b](#page-3-1). In the final step, *MoS* of the solder joint is calculated for each package based on *Zallow* and *Zmax* using Equation (2) described as follows:

$$
MoS = \frac{Z_{allow}}{FoS \times Z_{max}} \geq 0, [-]
$$
 (2)

where *FoS* is factor of safety for *MoS* calculation. The *FoS* values such as 1.11, 1.25, and 1.4 have been used for *MoS* calculation in previous studies [\[7,](#page-22-5)[10–](#page-22-8)[12\]](#page-22-9). Applying *FoS* values to the stress yields the *FoS* in terms of the fatigue life and in accordance with the stress-life cycle curve (*S*-*N* curve) described as follows [\[13\]](#page-22-10):

$$
N = S^b \text{ or } N = Z^b,
$$
\n(3)

where *b* is the fatigue exponent of the solder or lead frame material of the package. Steinberg [\[6\]](#page-22-4) assumed that the stress or strain of the solder joint is proportional to the board displacement (*Z*). Therefore, this relationship is also valid for *Zmax*, as described in Equation (3). In general, the Sn-Pb37 solder material has been widely used in spaceborne electronic units. For high cycle fatigue such as vibration, the value of *b* is typically taken as 6.4 [\[6\]](#page-22-4). The ECSS rule [\[14\]](#page-22-11) recommends using *FoS* = 4.0 or higher for the fatigue. For example, if the Sn-Pb37 solder is used in a package, the corresponding *FoS* of *Zmax* is approximately 1.25, according to Equation (3). These aspects were reflected to determine the *FoS* for evaluating the solder joint safety of a selected SOP specimen that is described later. If the estimated  $MoS$  is greater than or equal to zero, at least 2  $\times$   $10^7$  fatigue cycles for random vibration is guaranteed for solder joint according to the Steinberg's criterion.

The theoretical limitations of Steinberg's theory are mainly owing to *B* and *r* in Equation (1). These factors can be clearly estimated only when the mode shape is an ideal half-sine wave, like that shown in Figure [2b](#page-3-1). The occurrence of an error in the estimated *Zallow* is inevitable if the mode shape strays from the assumption. This implies that Equation (1) cannot explain the complex mode shapes caused by the asymmetric mechanical design configuration of electronic units. A similar problem occurs in the case of *Zmax* owing to the aforementioned limitation. This is because the proportional relationship between the board displacement and actual stress on the solder joints, assumed by Steinberg, can be invalid if the board is distorted in a complex manner.

Another technical problem in Steinberg's theory is the production of an overly conservative design criterion that causes structural overdesigning of electronic units; this was reported in a previous study [\[7\]](#page-22-5). Steinberg's criterion of 2  $\times$  10<sup>7</sup> fatigue cycles imposes a very high margin on the fatigue life of the solder joints of spaceborne electronic units, which are typically exposed to vibrations for several tens of minutes in on-ground vibration tests and during launch. Generally, the PCB assembly is designed to achieve the fundamental resonant frequency from 100 to 1000 Hz [\[7](#page-22-5)[–10](#page-22-8)[,15\]](#page-22-12). Assuming that *MoS* is greater than zero, the criterion of 2  $\times$  10<sup>7</sup> fatigue cycles can increase the solder joint fatigue life by several hours, even if the board resonant frequency goes higher than 1000 Hz. This conservatism becomes more aggravated when *FoS* is applied in the *MoS* calculation. The root cause of this issue is the use of Steinberg's criterion even though it was not originally established for spaceborne electronic units in the first place. In other words, prior to the study conducted by Park and Oh [\[9\]](#page-22-7), no other suitable design criteria were developed for spaceborne electronic units. The recent New Space paradigm requires the development of lighter and smaller satellites. In this context, one of the core mechanical design strategies is to reduce the structural overdesigning of onboard electronic units that occupy a large mass

and volume in the satellite. Thus, a new design criterion for spaceborne electronic units is required to substitute that proposed by Steinberg. tra volume in the suteme. Thus, a new acsign enterion for space

As mentioned earlier, to overcome the theoretical limitations of Steinberg's theory, As mentioned earlier, to overcome the theoretical limitations of Steinberg's theory, Park and Oh [\[9\]](#page-22-7) proposed a new mechanical design methodology called the Oh–Park Park and Oh [9] proposed a new mechanical design methodology called the Oh–Park methodology for spaceborne electronic units. Unlike Steinberg's theory, the Oh–Park methodology for spaceborne electronic units. Unlike Steinberg's theory, the Oh–Park methodology for spaces one electronic units. Shinke stemberg's utesty, the St Funk<br>methodology evaluates the solder joint safety based on the *MoS* calculation with respect to the critical PCB strain on the electronic package. The use of the board strain enables the feasible evaluation of solder joint safety even if the PCB exhibits complex distorted mode feasible evaluation of solder joint safety even if the PCB exhibits complex distorted mode shapes. In addition, it resolves the overdesigning problem caused by Steinberg's design shapes. In addition, it resolves the overdesigning problem caused by Steinberg's design criterion because the solder joint safety is evaluated considering the  $TTF_{req}$  for survival in the vibration test and during launch processes.

The Oh–Park methodology is employed to evaluate the solder joint safety based on the The Oh–Park methodology is employed to evaluate the solder joint safety based on design process in Steps 3–2 to 7–2, as described in Figure [1.](#page-3-0) In Step 3–2, the total vibration exposure time in the vibration test and during the launch processes is estimated. In this study, the effectiveness of the Oh–Park methodology was evaluated based on an example of the test and launch processes for satellite onboard units, as described in Figure [3.](#page-5-0) In this scenario, only a single electronic unit is assumed to be manufactured and subjected to qualification test phases at the unit level and flight acceptance tests at the satellite system level and during launch to reduce the cost and schedule. Here, the qualification test phase includes random vibration tests with five steps for each axis and a gradual increase in the input levels from −12 to 0 dB (qualification full level) with +3 dB increments. The 0 dB level was excluded from the acceptance test, and the rest of the processes were the same as those of the qualification test. An entire launch campaign typically lasts between tens of minutes and several hours; however, the major random vibration occurs in the first few minutes after lift-off when extreme aerodynamic pressure is acting on the launcher. Accordingly, the launch random vibration duration was assumed to be 4 min, and simultaneously, the excitation equivalent to the acceptance test level was generated in 3 axes. the excitation equivalent to the acceptance test level was generated in 3 axes.

<span id="page-5-0"></span>

**Figure 3.** Assumed test and launch processes for spaceborne electronics. **Figure 3.** Assumed test and launch processes for spaceborne electronics.

Generally, a mechanical design evaluation of the electronic units is performed to Generally, a mechanical design evaluation of the electronic units is performed to prove the structural safety with respect to the qualification-level random vibration (0 dB). prove the structural safety with respect to the qualification-level random vibration (0 dB). Therefore, all the load levels in the test and launch were substituted to a 0 dB-equivalent Therefore, all the load levels in the test and launch were substituted to a 0 dB-equivalent exposure time for random vibration, using the Equation (4) described as follows: exposure time for random vibration, using the Equation (4) described as follows:

$$
T_{xdB} = (t_{test} \text{ or } t_{lnch}) \times (G_{ratio})^b \times n \text{ [min]},
$$
\n(4)

where *ttest* and *tlnch* are the exposure times of an individual vibration test and launch, respectively, and *b* is the fatigue exponent of the solder or lead material. Additionally, *Gratio* is the ratio of the RMS input test level to the 0 dB input, which is described as follows:

$$
G_{ratio} = 10^{\left(\frac{x}{20}\right)} \left[ - \right],\tag{5}
$$

where *n* is the number of tests for each axis. Here, it is assumed that the solder joint fatigue equal to that of the PCB out-of-plane excitation is accumulated in all the test axes of the electronic unit. Therefore,  $n = 3$  was used for estimation in the 3-axis tests. This assumption simplified the estimation and generation of conservatism on solder joint safety to some extent.

A total 0 dB-equivalent exposure time can be estimated for a single test phase from the summation of the  $T_{xdB}$  values for each test level derived using Equations (4) and (5). For the unit-level qualification test, the accumulated 0 dB-equivalent vibration exposure time,  $\sum T_{U−O}$ , can be described as follows:

$$
\sum T_{U-Q} = T_{-12\ dB} + T_{-9\ dB} + T_{-6\ dB} + T_{-3\ dB} + T_{0\ dB} \text{ [min]},
$$
 (6)

In Step 4–2, the *TTFreq* for the solder joint is estimated from the summation of the accumulated exposure time during the entire test phase and launch phase using Equation (6) described as follows:

$$
TTF_{req} = \sum_{k=1}^{n} T_k \times FoS_{tf} \text{[min]},\tag{7}
$$

where  $T_k$  is an accumulated 0 dB-equivalent vibration exposure time in the vibration test or launch phase. *FoS*<sup>*ttf*</sup> is a factor of safety for  $TTF_{\text{rea}}$ . In this study, an  $FoS_{\text{t}}/$  value of 4.0, which is the recommended value for fatigue based on the ECSS standard [\[14\]](#page-22-11), was used for the estimation. The *TTFreq* for the scenario displayed in Figure [3](#page-5-0) is estimated as follows:

$$
T T F_{\text{req}} = \left( \sum T_{U-Q} + \sum T_{S/S-A} + \sum T_L \right) \times F \sigma S_{ttf} \text{ [min]},\tag{8}
$$

where  $\sum T_{S/S-A}$  and  $\sum T_L$  are the accumulated 0 dB-equivalent vibration exposure times in the acceptance test at the spacecraft system (S/S) level and launch phases, respectively.

In Step 5–2, the design factor, *DF*, is estimated based on *f<sup>n</sup>* and *TTFreq* estimated in Steps 2 and 4–2:

$$
DF = \left(\frac{N_{org}}{N_{req}}\right)^{1/b} = \left(\frac{2 \times 10^7}{TTF_{req} \times 60 \times f_n}\right)^{1/b} [\cdot],\tag{9}
$$

where  $N_{org}$  is an original Steinberg design criteria (2  $\times$  10<sup>7</sup> cycles) for solder joint survival when exposed to random vibration, *Nreq* is the total number of fatigue cycles for solder joint survival, and the *DF* is estimated for each package.

In Steps 6–2 to 7–2, the maximum in-plane principal PCB strain *ε <sup>p</sup>max* is estimated for each package by using the RMS strain components derived from the random vibration analysis of the electronic unit using Equation (10) described as below:

$$
\varepsilon_{p_{max}} = 3 \times \left( \frac{\varepsilon_{x_{rms}} + \varepsilon_{y_{rms}}}{2} + \sqrt{\left( \frac{\varepsilon_{x_{rms}} - \varepsilon_{y_{rms}}}{2} \right)^2 + \left( \varepsilon_{x_{y_{rms}}}\right)^2} \right) \text{ [$\mu$-strain$]},\tag{10}
$$

where *εxrms* and *εyrms* are *x* and *y* normal strain components of the PCB [µ-strain], respectively. *ε*xyrms is *xy* shear strain component of the PCB [µ-strain]. The *ε <sup>p</sup>max* is then used to estimate PCB strain rate ε [μ-strain/s], allowable PCB strain *ζ* [μ-strain] and critical PCB strain *ε<sup>c</sup>* [µ-strain] using Equations (11)–(13) described as below:

$$
\dot{\varepsilon} = 2\pi \times \varepsilon_{p_{max}} \times f_n,\tag{11}
$$

$$
\zeta = \sqrt{\frac{2.35}{t}} \times \{1900 - 300 \times \log(\dot{\varepsilon})\},\tag{12}
$$

$$
\varepsilon_c = \frac{\zeta}{C\sqrt{L}},\tag{13}
$$

where the factors *t*, *C* and *L* are the same as those defined in Steinberg's theory. Finally, *MoS* is calculated by using *εc*, *ε <sup>p</sup>max* , and *DF*, using Equation (14) described as follows:

$$
MoS = \frac{\varepsilon_c \times DF}{\varepsilon_{pmax}} - 1 \ge 0 \, [-], \tag{14}
$$

where  $DF$  is estimated by Equations (4)–(9). The design criterion of Oh-Park methodology is that *TTFreq* for random vibration is guaranteed when the estimated *MoS* is greater than or equal to zero.

Those three factors of  $\varepsilon_c$ ,  $\varepsilon_{p_{max}}$ , and DF are key elements used to utilize the main advantages of the Oh-Park methodology. Substituting *Zallow* and *Zmax* into the *ε<sup>c</sup>* and *ε <sup>p</sup>max* in the *MoS* calculation overcomes the theoretical limitations of Steinberg's empirical formula, which leads to a reliable evaluation of solder joint safety. The overdesigning issue in Steinberg's design criterion can also be resolved by estimating and using a suitable *DF* that reflects the solder joint fatigue life for a specific test and launch plan.

In the space engineering field, the peak responses of acceleration, displacement, stress, and strain in random vibration are commonly considered as 3σ values of RMS response under the assumption that the peak responses follow the Gaussian probability distribution. In accordance with Gaussian distribution, most of the fatigue damage is generated by peak responses up to the 3σ level [\[6\]](#page-22-4). This is because the response will be within a  $-3\sigma \sim +3\sigma$ range for 99.73% of random vibration duration. The use of the 3σ value for evaluating structural safety is one of the common standards in the space industry. Those are the reasons why we applied the maximum peak value of  $\varepsilon_{\mathrm{p}_{\mathrm{max}}}$  as the 3 $\sigma$  value of RMS in-plane principal strain. However, the peak response for Gaussian stationary random vibration is almost always greater than  $3\sigma$  in most of the cases. Nevertheless, even if the peak responses at 4, 5, and 6σ levels are involved in the estimation, the total consumed fatigue life is increased by only 28.13% compared with that calculated by involving up to the 3σ level. This can be observed by calculation using a Miner's cumulative damage cycle ratio equation with a principle of a three-band technique  $[6]$ . This percentage does not change even if *fn*, *T*, and stress level are changed. This means that the above calculation is valid in different boundary conditions of structure and enforced vibration conditions. Based on these assessments, maximum peak responses exceeding 3σ level were ignored in this study because their portion in the fatigue life is relatively much smaller than  $F \circ S_{tt} f = 4.0$  (400%) margin) included in the *TTFreq* for *MoS* calcualation. These assessments give a justification to use the 3σ level of RMS in-plane principal strain as  $ε_{p_{max}}$  involved in MoS estimation.

## **3. Fatigue Life Tests on the PCB Specimens**

As mentioned earlier, to validate the Oh–Park methodology for the SOP, a 48-pin plastic SOP (TSOP48) was selected and assembled on PCB specimens to perform fatigue life tests under random vibration. The representative configuration of the PCB specimen in Case 1 is depicted in Figure [4,](#page-8-0) and Table [1](#page-8-1) shows the specifications of TSOP48. The PCB consists of a two-layer FR-4 laminate with an area of 135 mm  $\times$  135 mm and a thickness of 1.6 mm. Additionally, the weight of the PCB assembly is  $54.5$  g, and the PCB is constrained using four M3 screw holes. Figure [5](#page-8-2) shows an example of a daisy-chain circuit for TSOP assembled on the PCB. This form of electrical connection, where all the lead frames are connected in a series, makes it easier to detect crack initiation in the solder joint or lead frame during the vibration test, simply by monitoring the two-wire resistance of that circuit.

<span id="page-8-0"></span>

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**Figure 4.** PCB specimen configuration for Case 1. **Figure 4.** PCB specimen configuration for Case 1. **Figure 4.** PCB specimen configuration for Case 1.

<span id="page-8-1"></span>**Table 1.** Specifications of TSOP48. **Table 1.** Specifications of TSOP48. **Table 1.** Specifications of TSOP48. **Figure 4.** PCB specimen configuration for Case 1.



<span id="page-8-2"></span>

**Figure 5.** Daisy-chain circuit concept for TSOP48.

 $\mathbf{S}^*$  type according to various boundary conditions, as shown in Figure 6. These specimens, as shown in Figure 6. These specimens, as shown in Figure 6. The specimens of  $\mathbf{S}^*$ Five PCB specimen cases were fabricated to validate the Oh-Park methodology for the  $\overline{CD}$ SOP type according to various boundary conditions, as shown in Figu[re](#page-9-0) 6. These specimen cases involve various mounting locations of packages on the board (Cases 1, 1–1, 1–2)

and various board sizes (Cases 2, 3). In Case 1–2, the package is located adjacent to the screw joint at a distance of 10 mm, wherein the board strain caused by the screw constraint influences the package. The main intention of establishing five cases of PCB specimens, as those shown above, is to verify the effectiveness of the proposed methodology for use in various design cases. This is because the PCB size and package mounting location are major factors that are frequently changed in the design trade-off of electronic units. In this study, two sets of test specimens were prepared and tested to ensure the reliability of the test results.

<span id="page-9-0"></span>



Figure 6. PCB specimen configuration cases: (a) various package mounting positions and (b) various PCB sizes. PCB sizes.

Figure [7](#page-10-0) shows the representative fatigue life test setup, where Case 1 of the PCB specimens is mounted on an electrodynamic shaker. The *TTF* of each package was measured by applying 2-wire resistance logging and monitoring, which was performed by using a DAQ6510 (Keithley Co., Ltd., Solon, OH, USA) data acquisition (DAQ) device. As per the IPC-9701A standard [\[16\]](#page-22-13), the solder joint fails when a daisy-chain resistance value that is 20% higher than the initial value is consecutively detected five times. Table [2](#page-10-1) shows the random vibration input specifications of the fatigue life test, wherein 20 Grms of random vibration were continuously applied to the test specimens until the failure criterion was reached. We recontinuously applied to the test specific the failure criterion was reached.

<span id="page-10-0"></span>

**Figure 7.** Representative fatigue life test setup for Case 1 of the PCB specimen. **Figure 7.** Representative fatigue life test setup for Case 1 of the PCB specimen.



<span id="page-10-1"></span>**Table 2.** Random vibration input specifications for the fatigue life test. **Table 2.** Random vibration input specifications for the fatigue life test.

Figure [8](#page-11-1) shows the time histories of the daisy-chain resistance values of the first set of test specimens. Here, the first failure of the solder joint is detected after 9.22 min of random excitation in the Case 1 specimen. Subsequently, Case 3 fails after 13.0 min of excitation, and Cases 2 and 1–1 fail after approximately 20 min. Case 1–2 survives the longest among the specimen cases and finally fails after  $113.45$  min of exposure.

Table 3 summarizes the measured time to failure,  $T\overline{T}_{\text{test}}$ , of all tested specimens. The overall test results show that the time to failure of the solder joints tends to increase higher than that of Case 1 with an increase in the position of the package from the center of the PCB and with a decrease in the board size. It was observed that the change in package mounting position is more effective to increase the time to failure as compared with the increase in fundamental resonant frequency  $(f_n)$  of PCB by reducing the board size. The  $TTF_{\text{test}}$  of the PCB specimens in each case show a similar tendency in Sets 1 and 2, although there are some differences. These test results were used to validate the Oh–Park methodology for the SOP.

<span id="page-11-1"></span>

**Figure 8.** Time histories of the measured daisy-chain resistances for Set 1 of the PCB specimens **Figure 8.** Time histories of the measured daisy-chain resistances for Set 1 of the PCB specimens during random vibration excitation. during random vibration excitation.

Case	$TTF_{test}$ (min)		Difference between	Average TTF <sub>test</sub> of Set 1 and 2 (min)	
	Set 1 Set 2		Sets $1 & 2$ (%)		
	9.22	10.38	12.6	9.8	
$1 - 1$	21.37	31.35	46.7	26.4	
$1 - 2$	113.45	89.65	21.0	101.6	
$\mathcal{D}$	19.53	19.45	0.4	19.5	
3	13	50.15	385.8	31.6	

<span id="page-11-2"></span>**Table 3.** Measured time to failure of tested PCB specimens (). **Table 3.** Measured time to failure of tested PCB specimens (*TTFtest*).

#### <span id="page-11-0"></span>**4. Verification of the Structural Design Methodology**

Figure 9 shows the evaluation process used to determine which structural design methodology is more relative in estimating the structural strety of the SOT solder joint.<br>The FE modeling guideline for the SOP was set up by employing a trial-and-error approach through repetitive structural analyses based on the PCB modeling technique proposed by Park and Oh [9]. Consequently, three types of simplified mod[el](#page-22-7)ing techniques were proposed for the SOP. Each analysis case had a different modeling technique, and solder foint safety evaluation was performed using the On–I ark methodology and stemberg's<br>theory according to the analysis process described in Figure [1.](#page-3-0) The evaluation criterion were proposed for the Sophistical analysis process are the social technique, and the methodology is whether the estimated *MoS* values accurately represent the average *TTF<sub>test</sub>* of the tested specimens. In this study, the *TTF* is predicted based on two methodologies to investigate whether the calculated *MoS* values accurately represent the  $TTF_{test}$  of the tested packages. The predicted  $TTF$  ( $TTF_{pred}$ ) is methodology is more reliable in estimating the structural safety of the SOP solder joint. joint safety evaluation was performed using the Oh–Park methodology and Steinberg's

estimated based on the PCB displacement using the power-law-based equation, described as follows:

$$
TTF_{pred} = N_C \times \left(\frac{Z_{allow}}{Z_{max}}\right)^b \times \left(\frac{1}{f_n \times 60}\right) \text{[min]},\tag{15}
$$

where  $N_C$  is 2  $\times$  10<sup>7</sup>, being the number of cycles for random vibration. Based on the PCB strain,  $TTF_{pred}$  is calculated as follows:

$$
TTF_{pred} = N_C \times \left(\frac{\varepsilon_c}{\varepsilon_{p_{max}}}\right)^b \times \left(\frac{1}{f_n \times 60}\right) \text{[min]}.
$$
 (16)

Figure [10](#page-13-0) shows a representative FE model for the Case 1 specimen with Type 2 modeling of the SOP. The PCB was modeled using the first-order shell element of QUAD4 Figure 10 shows a representative FE model for the Case 1 specimen with Type 2 modeling because it is more efficient in predicting the in-plane strain of a plate structure like PCB compared with the solid element in terms of the minimization of the mesh density. The area of the PCB occupied by the package body is uniformly meshed and the mesh size is the same as the lead frame pitch  $(0.5 \text{ mm})$  of the package. The rest of the board area has a 1.5 mm mesh size. The fixations by using bolt screw joints were simulated by using rigid  $\,$  link elements with mechanical constraints in three translational degrees-of-freedom ( $\,$ DoFs),  $\,$ where the independent node was located at the center of the screw hole, and six DoFs were constrained. These PCB modeling techniques are similar to those proposed by Park and Oh [\[9\]](#page-22-7).  $\mathcal{D}h$  [9]. The similar to those proposed by Park and Oh  $\mathcal{D}h$ 

<span id="page-12-0"></span>

**Figure 9.** Evaluation process for the structural design methodologies. **Figure 9.** Evaluation process for the structural design methodologies.

Figure 11 shows the close-up views of the proposed FE modeling techniques for SOP. Figure [11](#page-14-0) shows the close-up views of the proposed FE modeling techniques for SOP. In this study, the package was modeled with a 0D lumped mass to simulate the package In this study, the package was modeled with a 0D lumped mass to simulate the package mass and rigid link elements in the solder joint simulation. The main feature of the SOP mass and rigid link elements in the solder joint simulation. The main feature of the SOP configuration was implemented in the FE modeling. The SOP-type leaded package has configuration was implemented in the FE modeling. The SOP-type leaded package has two lines of gull-wing-shaped lead frames at two opposing sides of the package body, as two lines of gull-wing-shaped lead frames at two opposing sides of the package body, as shown in Table 1, unlike the BGA and CGA packages, which have solder joints in the area shown in Table [1,](#page-8-1) unlike the BGA and CGA packages, which have solder joints in the area arrays. To reflect these features in the modeling, the rigid link element was connected only to the nodes at the two opposing side edges of the package body area to simulate package–board connections using lead frame solder joints. Types 1, 2, and 3 modeling the package–board connections using lead frame solder joints. Types 1, 2, and 3 modeling correspond to the case where the connection points on the rigid link elements are four, correspond to the case where the connection points on the rigid link elements are four, six, and ten nodes, respectively, as shown in Figure 11a, b, and c, respectively. This form six, and ten nodes, respectively, as shown in Figure [11a](#page-14-0), b, and c, respectively. This form of modeling has not been proposed for SOPs in previous studies despite being extremely of modeling has not been proposed for SOPs in previous studies despite being extremely simple and consumer that the consumer than model multiple packages. The consumer that  $\frac{1}{n}$  ${\rm simple}$  and consuming significantly less time and effort than model multiple packages.

<span id="page-13-0"></span>

This contributes to the rapid model construction of electronic units using various types of packages, which is one of the important advantages of the Oh–Park methodology.

- 3 translational DoF constraints

Figure 10. Representative example of the FE model of the PCB specimen constructed using the Type 1 modeling technique for SOPs. repetitive and property of candidate models to find a property of rigid link connections for rigid links connections for  $\sim$ 



**Figure 11.** *Cont*.

<span id="page-14-0"></span>

Figure 11. Various simplified modeling techniques for electronic packages: (a) Type 1 (4 nodes nection), (**b**) Type 2 (6 nodes connection), and (**c**) Type 3 (10 nodes connection). connection), (**b**) Type 2 (6 nodes connection), and (**c**) Type 3 (10 nodes connection).

Figure 12 shows the estimation method for from the random vibration analy-As the number of rigid link connection points is increased, the dynamic strain response<br>
As the number of rigid link connection points is increased, the dynamic strain response of PCB at the corner-most points in the package mounting area is reduced by strain dispersion to the adjacent connection points. Therefore, the analyzed  $ε_{p_{max}}$  value could be excessively lower to estimate the *MoS*, and vice versa. Our approach to finding a valid FE model was the repetitive analysis of candidate models to find a proper number of rigid link connections for the package that produces a minimum error in estimating *MoS* of the solder joint.

Figure [12](#page-14-1) shows the estimation method for  $\varepsilon_{p_{max}}$  from the random vibration analysis results.  $\varepsilon_{p_{max}}$  is calculated using Equation (10), and the RMS nodal strains  $\varepsilon_{x_{rms}}, \varepsilon_{y_{rms}}$ , and  $\varepsilon_{xy_{rms}}$  are extrapolated from the element centroids. The RMS nodal strain is the average value of the strains at the four nodes in the corner-most QUAD4 element of the PCB package mounting area. All the modeling and analysis approaches described in this paper are part of the Oh–Park methodology.

<span id="page-14-1"></span>

**Figure 12.** Estimation approach for *ε p*<sub>*max*</sub> using the constructed FE model of the SOP.

After the FE models of the PCB test specimens were constructed by using the three different modeling techniques, modal analysis was performed for each case. Figure [13](#page-15-0) shows the representative results of the first three major mode shapes of Case 1 of the PCB specimen constructed via Type 2 modeling. Table [4](#page-16-0) summarizes the analyzed *f<sup>n</sup>* for the five cases of tested PCB specimens modeled using the Type 1, 2, and 3 techniques.

<span id="page-15-0"></span>

**Figure 13.** Mode shapes for Case 1 of the PCB specimen modeled using the Type 2 package modeling **Figure 13.** Mode shapes for Case 1 of the PCB specimen modeled using the Type 2 package modeling technique. (a) First mode: 196.2 Hz, (b) second mode: 379.3 Hz, and (c) third mode: 379.7 Hz. (Scale bar: eigenvectors, translational). bar: eigenvectors, translational).



<span id="page-16-0"></span>**Table 4.** Representative modal analysis results for the five cases of PCB specimens using various types of package modeling techniques.

Table [5](#page-16-1) summarizes the *TTFreq* estimation results for solder joint survival in the assumed development scenario shown in Figure [3.](#page-5-0) The estimation was performed using Equations (4)–(8). The estimated *TTFreq* value is 35.2 min, which was used as the design criterion for the electronic unit. For the *MoS* calculation, *DF* was estimated using Equation (9) by applying the analyzed  $f_n$  and  $TTF_{req}$ .

<span id="page-16-1"></span>**Table 5.** Estimation results of *TTFreq* for a given development scenario (Figure [3\)](#page-5-0).

Item	Factor	Value	Unit	<b>Remarks</b>
No. of vibration tests in each test level	N	3		Test is performed in 3-axes
Fatigue exponent for the solder joint	$\boldsymbol{b}$	6.4		For Sn-Pb37 solder & copper lead frame
Duration of a single test	$t_{\text{test}}$	2.00	min	
Duration of random vibration during launch	$t_{\text{lnch}}$	4.00	min	
	$T_{-12}$ dB	0.00029	min	
0 dB-equivalent vibration	$T_{-9}$ dB	0.0026	min	
exposure time for the vibration	$T_{-6}$ dB	0.024	min	Equations $(4)$ and $(5)$
tests at each test level	$T_{-3}$ dB	0.219	min	
	$T_{0, \text{dB}}$	2.00	min	
0 dB-equivalent vibration exposure time for the qualification test (unit level)	$\Sigma T_{C-O}$	6.74	min	For the 3-axis tests
0 dB-equivalent vibration exposure time for the acceptance test (S/S level)	$\sum T_{S/S-A}$	0.74	min	
0 dB-equivalent vibration exposure time for the launch	$\Sigma T_L$	1.32	min	Equivalent to the acceptance level $(T_{-3dB})$ ; the simultaneous 3-axis excitations
Factor of safety with respect to the required time to failure (min)	$FoS_{\text{ttf}}$	4.0		ECSS-E-ST-32C
Required time to failure for solder joint safety (min)	$TTF_{\text{req}}$	35.2	min	Equations $(6)$ – $(8)$

Table [6](#page-17-0) indicates the estimated *MoS* for the tested specimens using the Oh–Park methodology and Steinberg's theory when the Type 1 FE model is applied. Figure [14](#page-18-0) shows a comparison of *TTF*test and *TTF*pred values estimated by using both methodologies. For the evaluation, *FoS* = 1.25 was selected and applied in the *MoS* calculation for TSOP48 comprising an Sn-Pb37 solder and copper lead frame considering *b*= 6.4 according to the relation described in Equation (3). The blue dotted line plotted in the figure is *TTF*req = 35.2 min, derived in the assumed test and launch scenario summarized in Table [5.](#page-16-1) This line becomes a criterion for judging the reliability of predictions based on the design methodologies. It helps to understand whether  $TTF_{pred}$  well agrees with  $TTF_{test}$  in each specimen case. The effectiveness of the design methodologies was evaluated based on the estimated *MoS* and *TTF* values plotted in the figure. If both *TTF*<sub>pred</sub> and *TTF*<sub>test</sub> values are larger or smaller than the *TTF*req (above or below the blue dotted line), it can be seen that the prediction well agrees with the test results. In addition, this means that *MoS* well represents *TTF*<sub>test</sub>.

(a) Oh-Park Methodology					
<b>Factors for MoS Estimation</b>			Conventional Approach	Proposed Approach	
ε $(\mu\text{-strain/s})$	$\varepsilon_c$ $(\mu\text{-strain})$	$\varepsilon_{\rm p_{max}}$ (µ-strain)	MoS $(FoS = 1.25)$	DF	MoS $(TTF_{req} = 35.2 \text{ min})$
763,943	152.7	630.5	$-0.81$	1.84	$-0.55$
588,998	190.9	484.8	$-0.68$	1.84	$-0.28$
375,215	257.3	307.6	$-0.33$	1.84	0.54
980,573	115.9	505.3	$-0.82$	1.71	$-0.61$
1,140,945	93.6	470.3	$-0.84$	1.65	$-0.67$
<b>Factors for MoS estimation</b>			Conventional Approach	Proposed Approach	
r $(-)$	$Z_{\text{allow}}$ (mm)	$Z_{\rm max}$ (mm)	MoS $(FoS = 1.25)$	DF	MoS $(TTF_{req} = 35.2 \text{ min})$
1.000	0.437	0.567	$-0.38$	1.84	2.25
0.751	0.582	0.417	0.12	1.84	2.31
0.200	2.187	0.087	19.11	1.84	3.23
1.000	0.356	0.177	0.61	1.71	8.66
1.000	0.324	0.093	1.79	1.65	16.74
	(b) Steinberg's Theory				

<span id="page-17-0"></span>**Table 6.** Estimated *MoS* values of the tested specimens using the two methods (Type 1 FE model).

The analysis results shown in Table [6](#page-17-0) and Figure [14](#page-18-0) indicate that the *MoS* and *TTFpred* estimated by using the two methodologies show clearly opposite trends for most of the specimens. The *MoS* calculated by using Steinberg's theory shows invalid results because the Case 1 specimen is the only one with a negative value although the *TTFtest* values of Cases 1, 1–1, 2, and 3 were less than *TTF*req = 35.2 min. Furthermore, the *MoS* of Case 1 is inaccurate because the *MoS*, which was calculated based on *DF* instead of *FoS*, shows a positive margin, as described in Table [6.](#page-17-0) In addition,  $TTF_{pred}$  values of all the specimen cases were unreasonably higher than the specified error range for *TTF*. Meanwhile, the results obtained using Oh–Park methodology show excessively negative *MoS* and *TTFpred* values in all the specimen cases when *FoS* = 1.25. However, the *MoS* values of all cases become reasonable when *DF* is applied instead of *FoS*, although their *TTFpred* values are higher than the acceptable error range. The inaccurate *TTFpred* resulted in an excessive value of the analyzed  $\varepsilon_{p_{max}}$  due to the strain concentration at the corner-most area of the package.

Table [7](#page-18-1) summarizes the results of the *MoS* calculation for the Type 2 FE model, and the relevant *TTFpred* values are plotted in Figure [15.](#page-19-0) Both the *MoS* and *TTFpred* calculated by using the Oh–Park methodology represent the *TTFtest* within a specified error range. All results indicate that the structural safety for the *TTFreq* = 35.2 min is evaluated with considerably high accuracy compared with that in the case of the Type 1 model. This is because the strain concentration phenomenon observed in the latter case, as shown in Table [7](#page-18-1) and Figure [15,](#page-19-0) is mitigated due to the increased connection points of rigid link elements for the SOP. In contrast, compared with the test data, the *MoS* calculated by

<span id="page-18-0"></span>

using Steinberg's theory still shows excessively positive margins in all cases. The analyzed *TTFpred* values are also significantly higher than those of the test data.

**Figure 14.** Comparison of the  $TTF_{test}$  and  $TTF_{pred}$  estimated using both structural design methodoloodologies and Type 1 FE modeling. gies and Type 1 FE modeling.

<span id="page-18-1"></span> $T_{\rm eff}$  summarizes the results of the results of the Type  $\sim$ **Table 7.** Estimated MoS values for the tested specimens using two methodologies (Type 2 FE model).

(a) Oh-Park Methodology								
Case	<b>Factors for MoS Estimation</b>			Conventional Approach	Proposed Approach			
	ż. $(\mu\text{-strain/s})$	$\varepsilon_c$ $(\mu\text{-strain})$	$\varepsilon_{\rm p_{max}}$ $(\mu\text{-strain})$	MoS $(FoS = 1.25)$	DF	MoS $(TTF_{req} = 35.2 \text{ min})$		
1	640,407	178.6	519.5	$-0.72$	1.83	$-0.37$		
$1 - 1$	521,808	208.8	422.9	$-0.61$	1.83	$-0.10$		
$1 - 2$	388,489	252.2	314.3	$-0.36$	1.83	0.47		
$\overline{2}$	725,669	160.2	352.3	$-0.64$	1.69	$-0.23$		
3	744,224	156.5	299.6	$-0.58$	1.64	$-0.14$		
	(b) Steinberg's Theory							
Case	<b>Factors for MoS estimation</b>			Conventional Approach		Proposed Approach		
	r $(-)$	$Z_{\text{allow}}$ (mm)	$Z_{\text{max}}$ (mm)	MoS $(FoS = 1.25)$	DF	MoS $(TTF_{req} = 35.2 \text{ min})$		
1	1.000	0.437	0.531	$-0.34$	1.83	2.45		
$1 - 1$	0.751	0.582	0.390	0.19	1.83	2.52		
$1 - 2$	0.200	2.187	0.078	21.43	1.83	3.69		
2	1.000	0.356	0.123	1.32	1.69	12.74		
3	1.000	0.324	0.069	2.75	1.64	22.77		

<span id="page-19-0"></span>

2 1.000 0.356 0.123 1.32 1.69 12.74

**Figure 15.** Comparison of the  $TTF_{test}$  and  $TTF_{pred}$  estimated using both structural design methodologies and Type 2 FE modeling.

 $T_{\rm eff}$  and  $T_{\rm eff}$  and the evaluation results using both design methodology methodolog Table [8](#page-20-0) and Figure [16](#page-20-1) presents the evaluation results using both design methodologies and based on the Type 3 FE model. The Oh–Park methodology exhibits similar *MoS* calculation results. However, the *TTF<sub>pred</sub>* values are excessively positive beyond the acceptable error range in all specimens except Case 1 because of the significant reduction in the aforementioned strain concentration. This implies that the Type 3 model is less suitable compared with the Type 2 model. Steinberg's theory provided invalid results of the *MoS* and *TTF<sub>pred</sub>,* similar to those obtained in Type 1 and 2 models. The calculated MoS values misrepresented the test results and exhibited overly positive margins when the condition *TTFreq* = 35.2 min was applied. The overall evaluation results indicate that the limitations of Steinberg's theory cannot be resolved by using an FE modeling technique.

The main reason for the trend of time to failure between specimen cases observed in experimental and analytical results is mainly caused by the presence of factor  $ε$  (strain rate of PCB) of Equation (11) as a function of  $\varepsilon_{p_{\text{max}}}$  and  $f_n$ . The increase in  $f_n$  of PCB by reducing the board size causes an increase in  $\varepsilon$ , as observed in the estimation results of Tables [6](#page-17-0)[–8.](#page-20-0) On the other hand, the change in package mounting locations in Cases 1–1 and 1–2 led to a much lower *ε* value compared with Cases 1, 2, and 3. The key point of those evaluation results is that the difference in fatigue life of solder joint between specimen cases can be explained by factor ε. Meanwhile, Steinberg's theory also predicted this phenomenon to some extent because the estimated *Z*allow values in Tables [6](#page-17-0)[–8](#page-20-0) indicated that *Z*allow is reduced by an order of Cases  $1 \rightarrow 2 \rightarrow 3$ . However, MoS values estimated by Steinberg's theory could not well represent the test results because  $Z_{\text{allow}}$  and  $Z_{\text{max}}$  values for most of the test cases were estimated in an excessively positive manner due to the abovementioned theoretical limitations.

(a) Oh-Park Methodology							
Case	<b>Factors for MoS Estimation</b>			Conventional Approach	Proposed Approach		
	ε $(\mu\text{-strain/s})$	$\varepsilon_c$ (µ-strain)	$\varepsilon_{\rm p_{max}}$ $(\mu\text{-strain})$	MoS $(FoS = 1.25)$	DF	MoS $TTF_{req} = 35.2 \text{ min}$	
	508.298	212.6	412.3	$-0.59$	1.83	$-0.06$	
$1 - 1$	365.491	261.2	296.2	$-0.29$	1.83	0.61	
$1 - 2$	355,560	265.3	289.6	$-0.26$	1.83	0.69	
2	572,053	195.2	277.7	$-0.44$	1.69	0.19	
3	565,659	196.9	227.7	$-0.31$	1.64	0.42	
	(b) Steinberg's Theory						
<b>Factors for MoS estimation</b>				Conventional Annroach		Proposed Approach	

<span id="page-20-0"></span>**Table 8.** Estimated MoS values for the tested specimens using two methodologies (Type 3 FE model).



<span id="page-20-1"></span>

Figure 16. Comparison of the TTF<sub>test</sub> and TTF<sub>pred</sub> estimated using both structural design methodoloologies and Type 3 FE modeling. gies and Type 3 FE modeling.**Figure 16.** Comparison of the  $TTF_{test}$  and  $TTF_{pred}$  estimated using both structural design methodologies and Type 3 FE modeling.

The evaluation results for Type 1, 2, and 3 models indicate that, compared with Steinberg's theory, the Oh–Park methodology is more reliable in evaluating the structural safety of SOP solder joints. In addition, the Type 2 model with six connection points for the rigid link elements is the most suitable modeling technique among the considered candidates as it has a proper number of rigid link connection points to analyze *ε <sup>p</sup>max* , which leads to the reliable *MoS* estimation.

The fatigue life data of metallic materials like solder joints generally have a large amount of scatter. This scattering is inevitable as long as there are various factors involved in the development process of electronic units, such as prediction error, manufacturing and assembly tolerances, test and measurement tolerances, and FE model correlation error. This means that the fatigue life is extremely difficult to be accurately defined by both prediction and test. One reference [\[6\]](#page-22-4) also mentioned such difficulty. Therefore, some level of scattering can be acceptable if there is a standardized tolerance range. In this study, *FoS*<sub>ttf</sub> = 4.0 was used for *MoS* of solder joint estimated based on *TTF*<sub>req</sub>, in accordance with the ECSS standard (reference [\[14\]](#page-22-11)). Therefore, we can say that a maximum of four instances of scattering is considered to be acceptable for  $TTF_{pred}$  and  $TTF_{test}$ . From this perspective, the accuracy of the current test results becomes sufficient if the *TTF*<sub>test</sub> values of specimens are within a certain error range, although there is some extent of dispersion in the failure data between tested specimens observed in Table [3.](#page-11-2)

The most important criterion for evaluating the effectiveness of the proposed methodology is whether the estimated *MoS* value is a positive value (*MoS* > 0  $\rightarrow$  *TTF*<sub>req</sub> > 35.2 min) or a negative value ( $MoS < 0 \rightarrow TTF_{\text{req}} < 35.2 \text{ min}$ ) for the tested specimen cases. A total of 10 package specimens was used for this test, although the number was divided into five cases in accordance with the configuration of the PCB layout. Although this number might be insufficient to observe some level of consistency in the test results, the overall trend of the test results corresponded to the estimated *MoS* for each case. From this perspective, the extent of dispersion observed in the test results in Table [3](#page-11-2) is acceptable for evaluating the *MoS* value and corresponding *TTF*pred because they are in the specified error tolerance range. In a future study, we will improve the reliability of the proposed methodology by performing additional fatigue tests.

## **5. Conclusions**

In this study, the effectiveness of the Oh–Park methodology, which is a PCB-strainbased structural design methodology for spaceborne electronic units, was investigated to validate its reliability in evaluating the structural safety of SOP solder joints under random vibration. The main goal of this study was to find a feasible simplified FE modeling technique for SOPs that has not yet been considered. The candidate modeling techniques were proposed based on the rigid link elements and 0D lumped mass to simulate the solder joints and package bodies, respectively. The main features of SOP configuration were implemented in the modeling technique to realize the reliable and rapid evaluation of solder joint safety. To validate the effectiveness of the Oh–Park methodology using the proposed modeling technique, TSOP48 specimens were fabricated and mounted on PCBs with various boundary conditions before being exposed to random vibration excitation. The time to failure of the tested specimens was then compared with their margin of safety and the predicted time to failure that was estimated based on the Oh–Park methodology and conventional Steinberg's theory. These verification results indicate that a combination of the Oh–Park methodology and the proposed modeling technique is effective and reliable for the rapid evaluation of the solder joint safety of spaceborne electronic units. It was concluded that the simplified FE model of Type 2 was the most proper one among the candidate FE models because the dynamic strain response was properly estimated and led to the accurate estimation of *MoS* for solder joints of all the test cases. In addition, the Oh-Park methodology was proven to be a reliable solution for solder joint safety evaluation as compared with Steinberg's theory, which showed significant error in the *MoS* estimation. The experimental and analytical results of this study will improve the completeness of the Oh-Park methodology for evaluating the structural safety of spaceborne electronic units.

**Author Contributions:** Conceptualization, T.-Y.P. and H.-U.O.; methodology, T.-Y.P.; validation, T.-Y.P., J.-H.P. and H.-U.O.; formal analysis, T.-Y.P.; investigation, T.-Y.P. and J.-H.P.; data curation, J.-H.P.; writing—original draft preparation, T.-Y.P.; writing—review and editing, T.-Y.P.; visualization, T.-Y.P. and J.-H.P.; supervision, H.-U.O.; project administration, H.-U.O.; funding acquisition, H.-U.O. All authors have read and agreed to the published version of the manuscript.

**Funding:** This research was funded by the Ministry of Education (MoE), Republic of Korea.

**Data Availability Statement:** The data presented in this study are available on request from the corresponding author.

**Acknowledgments:** This research was supported by the National Research Foundation of the Republic of Korea (NRF, NRF-2018R1D1A1B05047385).

**Conflicts of Interest:** The authors declare no conflict of interest.

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