



Article

Modular Multi-Input DC/DC Converter for EV Fast Charging

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Abstract: In this paper, a modular multi-input, single output DC/DC converter is proposed to enhance the energy management of a fast-charging station for electric vehicles (EVs). The proposed bidirectional converter can work in different modes of operation with fewer components and a modular design to extend the input power sources and increase the charging power rate. The converter has several merits compared to the conventional converters, such as centralizing the control, reducing power devices, and reducing power conversion stages. By using MATLAB/Simulink, the converter was tested in many operation modes and was used to charge a Nissan Leaf EV's battery (350 V, 60 Ah) from hybrid sources simultaneously and individually in power up to (17 kW). In addition, it was tested on a hardware scale at a low power rate (100 W) for the validation of the simulation work and the topology concept. In addition, its different losses and efficiency were calculated during the different operation modes.

Keywords: efficiency; fast charging stations; hybrid energy system; modular DC/DC converter



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1. Introduction

Fast-charging stations (FCSs) rely heavily on power converters, especially DC/DC ones, to connect an energy storage device and an input power source, such as a battery and a PV, to a DC link. This, in turn, is connected to an electric vehicle's (EV) battery to charge it. A hybrid energy system is preferred in fast-charging stations to combine the high-energy density of a device such as a battery with the high-power density of a device such as a supercapacitor [1,2]. This hybrid system can also be utilized to mitigate the load from the grid, as charging one EV is equivalent to the load of one typical household. The energy storage devices can be charged during off-peak hours and discharged during peak hours, which could also reduce the cost of charging. To control and transfer power between the DC link and the energy storage devices, bidirectional DC/DC converters are used. In addition, energy storage devices can use them to transfer power between each other. This could be used to increase the battery's lifetime as its state of charge (SOC) should not be more than 80% or less than 20% [3]. To improve the efficiency of the system and increase the power rate of charging, it is important to design DC/DC converters with high efficiency and low power losses [4].

Using multi-input converters has several merits compared to single-input DC/DC converters, such as minimizing the number of components, reducing the number of power conversion stages, and centralizing the control. This makes them a good choice for numerous applications such as plug-in and hybrid electric vehicles, as the load can be supplied from different input voltages to various output levels. Many studies in multi-input converters have been conducted to obtain different topologies that combine one or more of these merits in a modular design which makes integrating additional power sources into the converter much easier. However, combining all the features and advantages in one topology is impossible. The structure of multi-input converters is divided into isolated and non-isolated converters, such as conventional, single-input converters. Isolated converters

are larger and heavier compared to non-isolated ones. Using multi-input DC/DC converters in fast-charging stations can reduce the number of parts, charging costs, and power losses. In addition, the centralized control of the multi-input DC/DC converter will reduce the maintenance costs and make troubleshooting easier. There are various topologies used to design multi-input DC/DC converters. Topologies might vary based on features and characteristics such as the number of operation modes, input sources, and components, as well as the power flow capability and modularity.

With a low number of components and continuous input current, the multi-input converter in [5] is used in renewable energy applications but has a unidirectional power flow. The load can be supplied with different voltages from two sources without circulating current using the multi-input converter in [6], but it lacks modularity. The proposed converter in [7] has lower conduction losses and can supply the load individually or simultaneously from two different sources with different voltage–current characteristics with three power switches alone. Therefore, it is used in DC microgrid applications, but the sources cannot transfer the power between them. The multi-input converter in [8] has many advantages such as high-power density and low cost, so it is a good choice for small unmanned aerial vehicle applications. The integrated power electronics interface (IPEI) in [9] can supply the grid from the vehicle, but its switching losses are high as it uses a lot of power switches. The DC/DC converter in [10] has the advantage of being capable of working in different operation modes with minimum components, and the power can be transferred between the output and the input sources in bidirectional power flow capability. However, this topology lacks the modularity to extend the power sources or energy storage devices. The proposed converter in [11] has several advantages: it has a simple switching sequence, the input sources have low current ripples, there is low-voltage stress on the switches, and it has the feature of extendibility to extend more PVs to the DC bus of the system. However, it is a unidirectional converter, so the power cannot be transferred to the input sources. A bidirectional converter with fewer components is presented in [12], and the DC sources can discharge in and charge from the load, but the power cannot be transferred between the DC sources. Table 1 summarizes the different characteristics of the multi-input DC/DC converters in the literature review.

This paper discusses a modular, multi-input, single-output, non-isolated, bidirectional DC/DC converter to connect two energy storage devices to a fast-charging station. The energy sources in the proposed converter can exchange the power between them and between the DC link and vice versa by using a low number of power switches. In addition, the DC link can be supplied from different power sources with different voltage levels. With a simple design and modularity feature, the power sources can be extended by adding two power switches and one inductor. The five operating modes of the proposed converter were simulated using MATLAB/Simulink, and it was also used to calculate the converter's different losses. The converter was tested on a hardware scale at a low power rate of 100 W to validate the simulation work. The converter is expected successfully work in different operation modes to supply the DC link with different voltage sources simultaneously and individually in bidirectional power flow capability, and to exchange power between the sources. The simulation results are also expected to match the experimental results. The remaining paper is organized in the following manner: Section 2 describes the schematic of the proposed converter and its different operation modes. The losses and the efficiency of the converter are analyzed in Section 3. Section 4 presents the proposed fast-charging station. Section 5 presents the simulation and experimental results of the operation modes. Section 6 presents the conclusion of the work.

Table 1. Different characteristics of the multi-input DC/DC converters in the literature review.

Reference	Title	Number of the Components	Number of Operation Modes	Power Flow Capability	Modularity
[5]	Design and implementation of a multi-input single-output DC-DC converter.	7	2	Unidirectional	No
[6]	Development and performance analysis of dual-input DC-DC converters for DC microgrid application.	4	2	Unidirectional	No
[7]	A Novel multi-input and single-output DC/DC converter for small unmanned aerial vehicle	7	2	Bidirectional	No
[8]	Soft-switched single inductor single stage multiport bidirectional power converter for hybrid energy systems.	14	6	Bidirectional	No
[9]	An Advanced Power Electronics Interface for Electric Vehicles Applications.	10	2	Bidirectional	Yes
[10]	Single Inductor Bidirectional Multi-Input Converter with Continuous Battery Current Based on Integration of Buck and Three Port Boost Topologies.	9	7	Bidirectional	No
[11]	Expandable Non-Isolated Multi-Input Single-Output DC-DC Converter with High Voltage Gain and Zero-Ripple Input Currents.	14	2	Unidirectional	Yes
[12–14]	Dual-Input Non-isolated DC-DC Converter with Vehicle to Grid Feature.	5	6	Bidirectional	Yes

2. Proposed Topology

2.1. Design of the Multi-Input Converter

The proposed fast-charging station in Figure 1 uses a multi-input converter that can work in five operation modes to simultaneously or individually charge and discharge the energy storage devices as well as to exchange power between them. The battery and supercapacitor are used to form a hybrid energy storage system to combine the high-energy density from the battery and the high-power density from the supercapacitor. The converter consists of six MOSFETs, two inductors, and one capacitor, as shown in Figure 2. The design can be modular by adding two power switches and one inductor to combine the switching leg. Table 2 shows the equations used to calculate the minimum values of inductors L_1 and L_2 , where D is the duty cycle, T_s is the total period, d_1 is the ratio of the on-time of switch S_3 to total switching period T_s and, similarly, d_2 corresponds to switch S_2 , V_{Bt} is the battery voltage, V_{SC} is the supercapacitor voltage, and V_{DC} is the DC link voltage, and Table 3 shows the design specifications of the proposed converter with rated values.

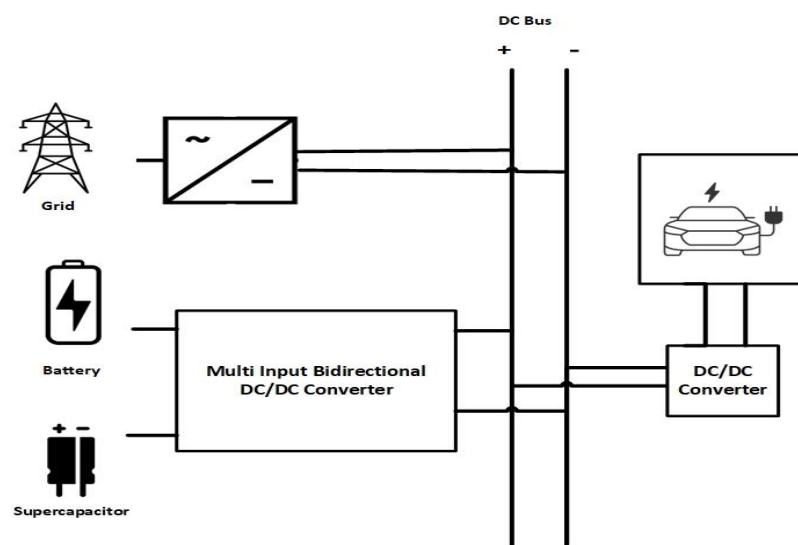
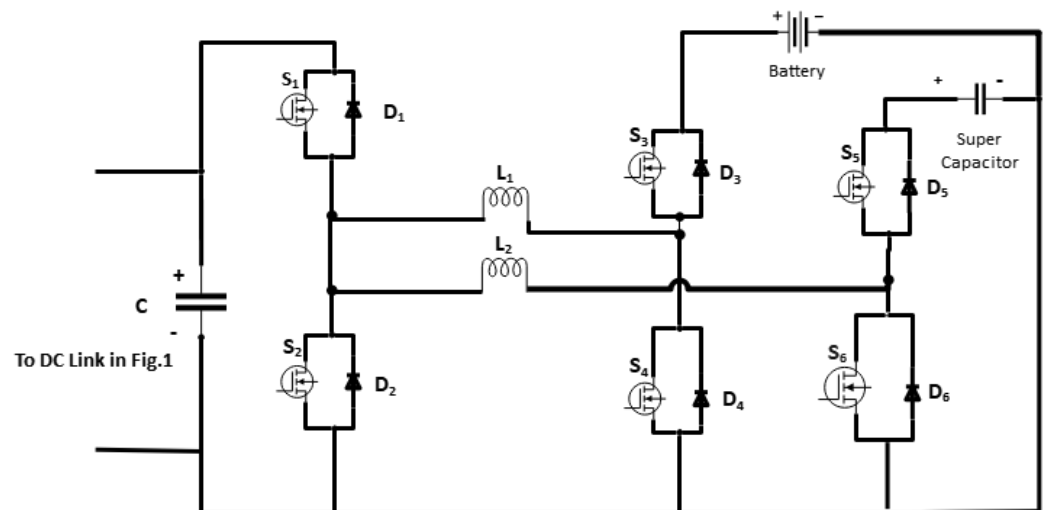
**Figure 1.** The configuration of the fast-charging station of electric vehicles.

Table 2. Inductance equations for different operation modes.

Mode	1	2	3	4	5
$L_{1, min}$	$\frac{(1-D) \cdot T_s}{\Delta I_{L1}} \cdot V_{DC}$	-	$\frac{D \cdot T_s}{2\Delta I_L} \cdot V_{Bt}$	$\frac{V_{DC} \cdot (1-d_2) \cdot T_s}{\Delta I_L}$	$\frac{(V_{DC} - V_{Bt})d_1 \cdot T_s}{\Delta I_{L1}}$
$L_{2, min}$	-	$\frac{(1-D) \cdot T_s}{\Delta I_{L2}} \cdot V_{DC}$	$\frac{D \cdot T_s}{2\Delta I_L} \cdot V_{Bt}$	$\frac{V_{DC} \cdot (1-d_2) \cdot T_s}{\Delta I_L}$	$\frac{(V_{DC} - V_{SC})d_1 \cdot T_s}{\Delta I_{L2}}$

**Figure 2.** The proposed converter configuration.**Table 3.** Design specifications of the proposed converter.

Specification	Battery Voltage (V_{BT})	Supercapacitor Voltage (V_{SC})	DC Link Voltage (V_{DC})	Switching Frequency (f_s)	Inductors (L_1 and L_2)	Capacitor (C)	Power
Values	200 V	160 V	500 V	20 kHz	0.75 mH and 0.75 mH	500 μ F	17 kW

To avoid the worst-case scenario, protection elements will be added to the station, such as circuit breakers and fuses, to increase the safety of the station and the converter.

2.2. Control of the Converter

A PI controller is one of the most common control algorithms used in power electronics, especially in DC/DC converters. It has many advantages such as simplicity, reliability, and ease of implementation in linear systems. It is part of the PID controller family with three integrated constants, as shown in Figure 3, which have a control loop mechanism, employing feedback that is frequently utilized in power electronics circuits. Typically, the proportional and the integral (PI) constants are only used in DC/DC converters to control the steady-state and transient errors and to enhance the response time of the system.

The system performance and response can be optimized by tuning the constants K_p , K_i , and K_d . However, as the constants K_p and K_i are typically only used in DC/DC converters, the output currents and voltages can be adjusted by tuning them and acquiring the desired process requirements. The error value is the difference between the feedback signal and the desired setpoint. The controller attempts to reduce it by modifying a controlled variable (e), the tracking error, as shown in Figure 4.

The error signal will be delivered to the PID controller, and the signal immediately after the controller will match the proportional gain times the error magnitude plus the integral gain times the integral of the error plus the derivative gain times the derivative of the error, where this signal will be sent to the plant to obtain the desired output.

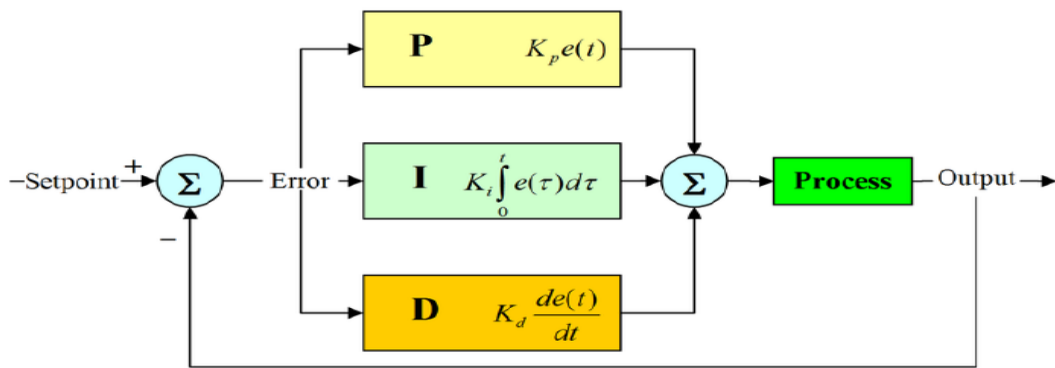


Figure 3. General PID controller.

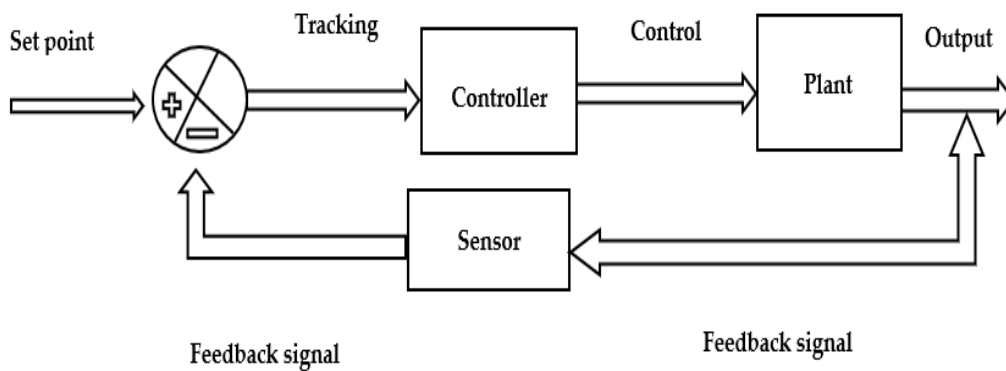


Figure 4. System block diagram with feedback control.

The PI controller is used for the voltage control for the buck and boost stages of the multi-input DC/DC converter. The closed-loop transfer function of the PI controller’s first-order system is:

$$G(s) = \frac{\left(K_p + \frac{K_i}{s}\right) \cdot k}{\tau \cdot s + 1 + \left(K_p + \frac{K_i}{s}\right) \cdot k} \tag{1}$$

where τ is the time constant and K is the DC Gain. Equations (2)–(4) were used to implement the controller algorithm in Figure 5.

$$\frac{V_{out}(s)}{V_{set\ point}(s)} = \frac{G(s)}{1 + G(s)} \tag{2}$$

$$G(s) = G_p(s) \cdot G_{PWM}(s) \cdot G_{DC_DC}(s) \tag{3}$$

$$G_{converter}(s) = G_{PWM}(s) \cdot G_{DC_DC}(s) \tag{4}$$

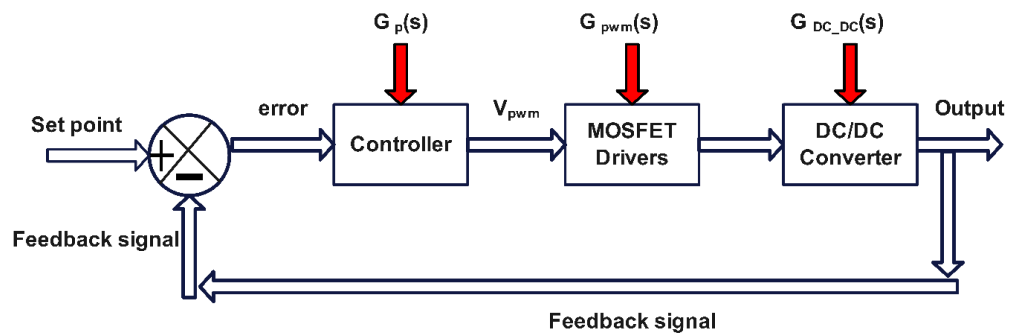


Figure 5. Multi-input DC/DC converter process with closed-loop controller.

As shown in Figure 5, the output signals of the PI controller determine the suitable duty ratio for switching the MOSFETs gates to obtain the desired operation mode. The same algorithm is used in the downscale DC/DC converter to control the charging current of the EV. The system is underdamped by regulating the value of the proportional coefficient $K_p = 3$ and the integral coefficient $K_I = 5$ to minimize the steady-state error. To make the whole system stable, the lead–lag compensator was designed using Equation (5), with $K_p = 3$ and $T_i = 7.5758 \times 10^{-4}$.

$$C_{iL} = K_p \left(1 + \frac{1}{T_{iS}} \right) \quad (5)$$

2.3. The Operation Modes of the Converter

The proposed converter works in five different operation modes, as we will discuss later, to transfer the power between the DC link and energy storage devices simultaneously or individually and to exchange power between them. The five operating modes of the converter are:

2.3.1. Mode 1: Battery to DC Link

The inductor L_1 is charged and discharged during this operation mode in three-time intervals, T_1 , T_2 , and T_3 , to discharge the battery only in the DC link to charge the electric vehicle's (EV's) battery. The switches S_2 and S_3 are turned on in the first-time interval to charge the L_1 , as shown in Figure 6a. In the second interval, the L_1 discharges through diodes D_1 and D_4 while switches S_2 and S_3 are turned off to supply the DC link in Figure 6b. To maintain the continuous discharging of energy from L_1 in the DC link, the switches S_1 and S_4 are turned on during the third time interval, as opposed to the diodes in T_2 . The switches S_1 and S_4 operate as synchronous rectifiers to reduce the voltage drop to a level of about 0.2 V; hence, the system efficiency is improved, as shown in Figure 6c. Under the steady-state condition, the relation between DC link voltage as an output and battery voltage as input is expressed using Equation (6):

$$V_{DC} = \frac{T_1}{T_2 + T_3} \cdot V_{BT} = \frac{D}{1 - D} \cdot V_{BT} \quad (6)$$

where D is the duty cycle ratio defined by $\frac{T_1}{T_S}$, and T_S is the total period of the switching cycle. The battery voltage is boosted to the DC link when working in a duty cycle equal to $D > 0.5$.

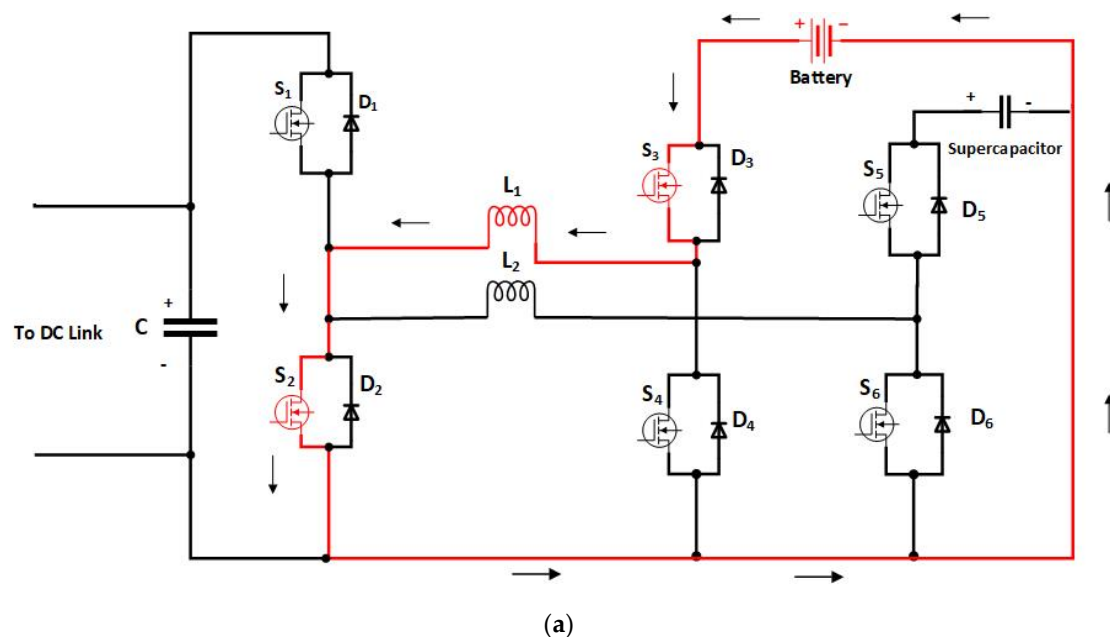


Figure 6. Cont.

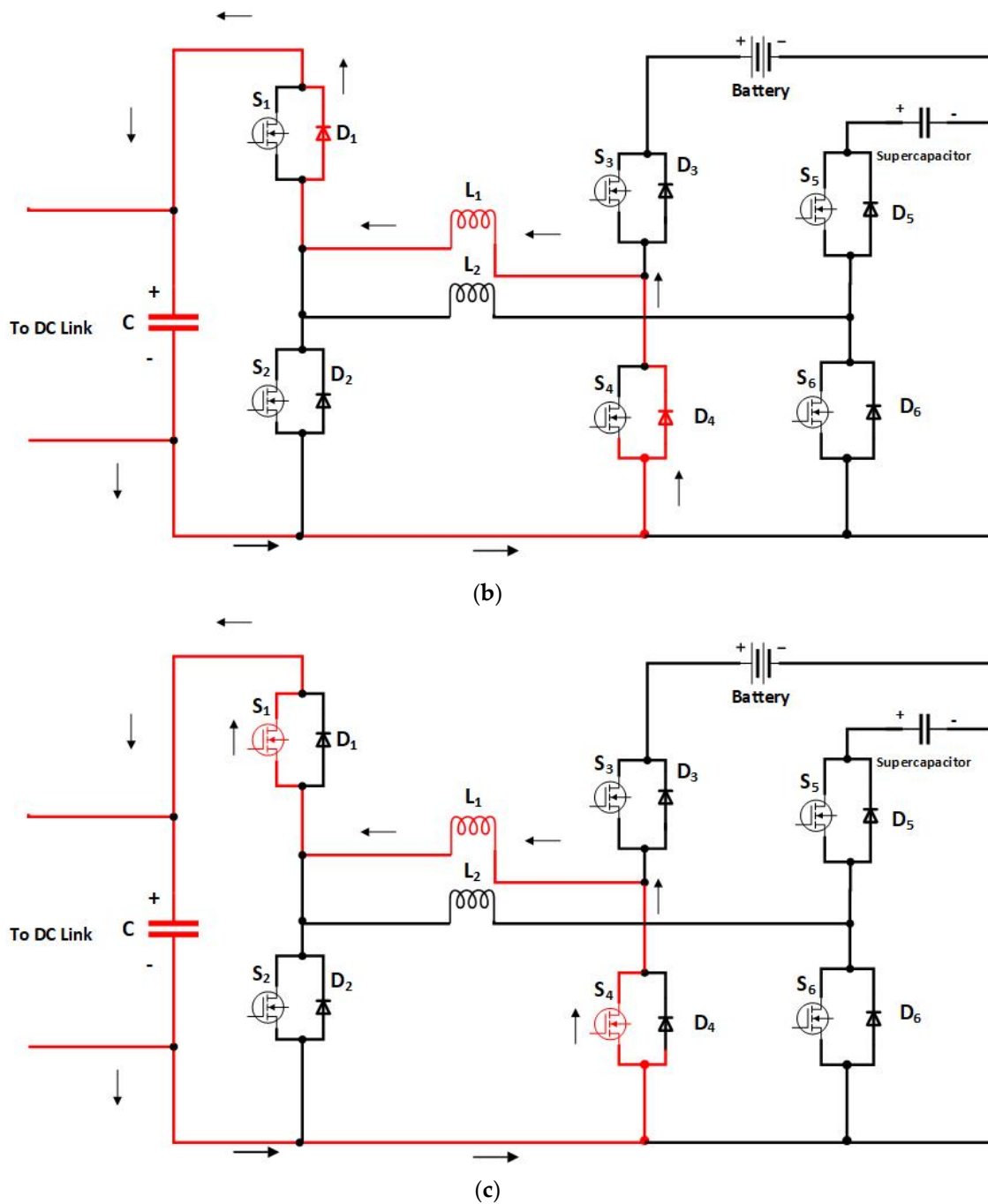


Figure 6. (a). Mode 1(a): T1 charging L1 from battery. (b) Discharging from L1 in DC link through the diodes. (c) Discharging from L1 in DC link through the switches.

The DC link charges the battery by reversing the current in L_1 . Under the steady-state condition, and taking into consideration D to be T_1/T_S , the relation between the DC link voltage and battery voltage can be expressed using Equation (7).

$$V_{BT} = \frac{T_1}{T_2 + T_3} \cdot V_{DC} = \frac{D}{1 - D} \cdot V_{DC} \quad (7)$$

2.3.2. Mode 2: Supercapacitor to DC Link

This operating mode is used to utilize the merit of the high-power density of the supercapacitor to charge the EV in less time. The inductor L_2 is charged and discharged in the DC link in three-time intervals, T_1 , T_2 , and T_3 , similar to the previous operation mode.

The switches S_2 and S_5 are turned on in the first-time interval to charge the L_2 . In the second interval, the L_2 discharges through diodes D_1 and D_6 while switches S_2 and S_5 are turned off to supply the DC link. In the third interval, the switches S_6, S_1 are turned on for the continued discharging of L_2 in the DC link and to reduce the voltage drop of the diodes. Equation (8) shows the voltage of the DC link V_{DC} as an output voltage as a function of the supercapacitor V_{SC} input voltage.

$$V_{DC} = \frac{T_1}{T_2 + T_3} \cdot V_{SC} = \frac{D}{1 - D} \cdot V_{SC} \quad (8)$$

where D is the duty cycle ratio equal $\frac{T_1}{T_s}$. To charge the supercapacitor from the DC link, the current flow reverses in L_2 and operates at $D < 0.5$. Equation (9) expresses the relation between the input and output voltage considering the duty ratio D to be T_1/T_s .

$$V_{SC} = \frac{T_1}{T_2 + T_3} \cdot V_{DC} = \frac{D}{1 - D} \cdot V_{DC} \quad (9)$$

2.3.3. Mode 3: Battery and Supercapacitor

In mode 3(a), the supercapacitor can be charged from the battery. The converter boosts the battery voltage V_{BT} to charge the supercapacitor, as shown in Equation (10), using the switches S_3, S_5 , and S_6 .

$$V_{SC} = \frac{T_s}{T_2 + T_3} \cdot V_{BT} = \frac{1}{1 - D} \cdot V_{BT} \quad (10)$$

The lifetime of the battery can be increased by charging it from the supercapacitor using mode 3(b). The switches S_3, S_5 , and S_6 are turned on to reverse the current through the inductors, as the battery can be charged in the buck operation mode using Equation (11).

$$V_{BT} = \frac{T_1}{T_s} \cdot V_{SC} = D \cdot V_{SC} \quad (11)$$

The switching sequence of the power switches in the three-time intervals of the previous operation modes is summarized in Table 4.

Table 4. Modes (1), (2), and (3) switching states.

	Mode 1(a)	Mode 1(b)	Mode 2(a)	Mode 2(b)	Mode 3(a)	Mode 3(b)
T_1	S_2, S_3	S_1, S_4	S_2, S_5	S_1, S_6	S_3, S_6	S_3, S_5
T_2	D_4, D_1	D_2, D_3	D_6, D_1	D_2, D_5	S_3, D_5	D_6, S_3
T_3	S_1, S_4	S_2, S_3	S_6, S_1	S_2, S_5	S_3, S_5	S_6, S_3

2.3.4. Mode 4: Battery and Supercapacitor to DC Link

In this operation mode, the load from the grid can be mitigated during the peak hours by charging the EV from the battery and the supercapacitor simultaneously, as the EVs consume high power and increase the load on the grid. As well, this mode can be utilized to reduce the charging cost by charging the battery and the supercapacitor during the off-peak hours and discharging them during the peak hours. Inductors L_1 and L_2 are charged and discharged in five-time intervals, represented in Table 5, to supply the DC link simultaneously from the battery and the supercapacitor using Equations (12) and (13).

$$V_{BT} = \frac{T_4 + T_5}{T_1} \cdot V_{DC} = \frac{T_s - d_2 T_s}{d_1 T_s} \cdot V_{DC} = \frac{1 - d_2}{d_1} \cdot V_{DC} \quad (12)$$

$$V_{SC} = \frac{T_4 + T_5}{T_1 + T_2 + T_3} \cdot V_{DC} = \frac{T_s - d_2 T_s}{d_2 T_s} \cdot V_{DC} = \frac{1 - d_2}{d_2} \cdot V_{DC} \quad (13)$$

where d_1 is the ratio of the on time of switch S_3 to the total switching period T_s and, similarly, d_2 corresponds to switch S_2 .

Table 5. Switching states in modes (4) and (5).

	T_1	T_2	T_3	T_4	T_5
mode 4	S_2, S_3, S_5	S_2, D_4, S_5	S_2, S_4, S_5	D_1, S_4, D_6	S_1, S_4, S_6
mode 5	S_1, S_3, S_5	D_2, S_3, S_5	S_2, S_3, S_5	S_2, S_4, S_5	S_1, D_3, S_5

2.3.5. Mode 5: DC Link to Battery and Supercapacitor

In contrast to operation mode 4, during the off-peak hours, the DC link can charge the energy storage devices simultaneously to save the cost of energy. By using Equations (14) and (15), and in the same way as in mode 4, inductors L_1 and L_2 will be charged and discharged in five-time intervals to charge the supercapacitor and battery.

$$V_{BT} = \frac{T_1 + T_5}{T_1 + T_2 + T_3 + T_5} \cdot V_{DC} = \frac{T_1 + T_5}{T_s - T_4} \cdot V_{DC} = \frac{d_1}{d_2} \cdot V_{DC} \quad (14)$$

$$V_{SC} = \frac{T_1 + T_5}{T_s} \cdot V_{DC} = d_1 \cdot V_{DC} \quad (15)$$

3. Losses and Efficiency of the Proposed Converter

The switching losses and conduction losses (as shown in Tables 6 and 7) are the main losses of the proposed converter. As the MOSFETs of the converter are exposed to high voltage and current during a transition between the on and off states, this creates switching losses in the converter. By using Equation (16), the switching losses in the different operation modes can be calculated.

$$P_s = \int V_D I_D dt \quad (16)$$

where V_D and I_D are the voltage and current of the MOSFET. The resistive elements of the MOSFETs dissipate power and create conduction losses as current is conducted through the device. The resistive parameter is described as on-resistance, or $R_{DS(ON)}$, and it equals 0.1 Ω in the proposed converter. The conduction losses can be calculated using Equation (17).

$$P_c = I^2 \cdot R_{DS(ON)} \quad (17)$$

Therefore, the total losses will be:

$$P_T = P_s + P_c \quad (18)$$

Table 6. Switching losses of the five operation modes.

	S_1	S_2	S_3	S_4	S_5	S_6	Total
mode 1(a)	2.8 W	6.6 W	55.9 W	23.7 W	-	-	89 W
mode 1(b)	6.7 W	16.3 W	9.5 W	3.8 W	-	-	36.3 W
mode 2(a)	3 W	13.11 W	-	-	14.2 W	4 W	33 W
mode 2(b)	63 W	183 W	-	-	176 W	57 W	479 W
mode 3(a)	-	-	48 W	-	32 W	13 W	96 W
mode 3(b)	-	-	45 W	-	32 W	13 W	90 W
mode 4	3 W	12 W	3.2 W	3.7 W	1.2 W	0.38 W	23 W
mode 5	5.6 W	12.4 W	4.3 W	0.9 W	3.8 W	0 W	27 W

Table 7. Conduction losses of the five operation modes.

	S_1	S_2	S_3	S_4	S_5	S_6	Total
mode 1(a)	0.25 W	6.6 W	52 W	2.12 W	-	-	63.97 W
mode 1(b)	6.7 W	1.5 W	0.87 W	3.8 W	-	-	12.87 W
mode 2(a)	0.27 W	13.2 W	-	-	14.24 W	0.37 W	28.08 W
mode 2(b)	62.7 W	16.5 W	-	-	15.8 W	57 W	152 W
mode 3(a)	-	-	855 W	-	40 W	400 W	1295 W
mode 3(b)	-	-	4 W	-	31.7 W	1.6 W	37.3 W
mode 4	0.28 W	12 W	3.25 W	0.33 W	1.26 W	0.035 W	17.16 W
mode 5	5.6 W	1.12 W	0.39 W	0.94 W	0.35 W	0 W	8.4 W

The switching and conduction losses in the different operation modes are summarized in Tables 5–7 based on Equations (17) and (18).

The efficiency of the proposed converter in the different operation modes is summarized in Table 8 based on Equation (19).

$$\text{efficiency} = \frac{P_{op}}{P_{in}} \quad (19)$$

Table 8. The efficiency of the converter of the five operation modes.

	Mode 1(a)	Mode 1(b)	Mode 2(a)	Mode 2(b)	Mode 3(a)	Mode 3(b)	Mode 4	Mode 5
P_{in}	4000 W	2100 W	1520 W	2100 W	9000 W	2200 W	1200 W	2100 W
P_{op}	3847 W	2051 W	1471 W	1469 W	7609 W	2073 W	1160 W	2065 W
Efficiency	96.18%	97.60%	96.70%	70%	85%	98.70%	96.70%	98.30%

4. The Proposed Fast-Charging Station

As mentioned before, the proposed converter is used to charge EVs individually or simultaneously from energy storage devices. The energy storage devices are connected with the grid utility to the DC link, as shown in Figure 1. This DC link is connected to a step-down converter to reduce the DC link voltage and increase the charging current of the EV as well as control the charging rate.

4.1. The Design of the Buck Converter

The step-down DC/DC converter shown in Figure 7 consists of MOSFET switch S to charge and discharge the inductor L and diode D to prevent the current from passing through it during the on-state of the switch as its reversed biased by voltage. The capacitor C is used as a filter for voltage ripples. During the on-state, the switch S is turned on, the current through inductor L is increased, and the voltage across the inductor will be

$$V_L = V_{in} - V_O \quad (20)$$

as in this case, the V_{in} will be the DC link voltage, and the V_O will be the output voltage to charge the EV's battery. In the off-state, the switch S is turned off, the diode D will be forward biased, and the voltage across the inductor will be

$$V_L = -V_O \quad (21)$$

and the current through the inductor will decrease and flow through the freewheeling diode D and the capacitor C .

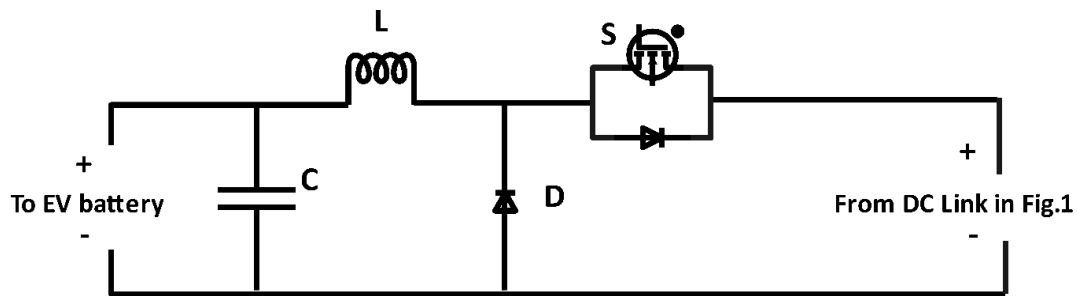


Figure 7. Circuit diagram of buck converter.

The inductor is selected to make the converter work in the continuous operation mode and reduce the current ripples and is calculated using Equation (22)

$$L = \frac{(1 - D)R}{2f} \quad (22)$$

where R is the resistive load and D is the duty cycle. Similarly, the capacitor is selected and calculated using Equation (23) to reduce the output voltage ripples and handle the required ripple current stress.

$$\Delta V_C = \frac{V_S D(1 - D)R}{8LCf^2} \quad (23)$$

The buck converter works at a switching frequency f 20 kHz to reduce the size of the inductor and the capacitor. Table 9 shows the design specification of the proposed buck converter [15].

Table 9. Design specification of the buck converter.

Specifications	Input Voltage	Output Voltage	Switching Frequency (f_s)	Inductor (L)	Capacitor (C)	Power
Values	500 V	350 V	20 kHz	5 mH	50 μ F	17 kW

4.2. The Control of the Buck Converter

The multi-input converter was based on voltage control to sustain the DC link voltage at 500 V. In contrast, the buck converter is based on current control, as shown in Figure 8, to control the charging current of the EV. The proportional integral (PI) controller was chosen to also to control the charging current. The PI controller minimizes the error between the output current and the reference charging current to determine the desired duty ratio for generating the pulses of the switch. The system is underdamped by regulating the value of the integral coefficient $K_I = 5$ and proportional coefficient $K_p = 3$ to minimize the overshoot and the steady-state error. These values were obtained using the same Equations (1)–(5).

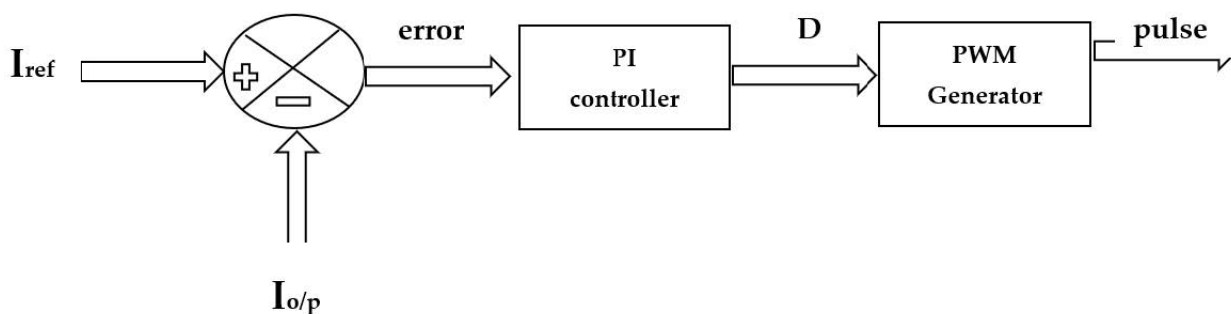


Figure 8. Buck converter controller.

5. Results

5.1. Simulation Results

MATLAB/Simulink was used to obtain the different results of the proposed converter and verify its analysis in the different operation modes. Figure 9a shows the battery was boosted from 200 V to 500 V to discharge in the DC link through L_1 in mode 1(a). Based on the switching sequence in this mode, the switch S_2 works complementarily with switch S_4 , as the voltage across S_2 presents the DC link voltage, and the voltage across S_4 presents the battery's voltage. In mode 1(b), the battery is charged from the DC link by reversing the current in the inductor L_1 in the average current of 15 A, as shown in Figure 9b, and similarly, the voltage across S_2 presents the DC link's voltage, and the voltage across S_4 presents the battery's voltage. In mode 2(a), the energy transferred from the supercapacitor to the DC link as voltages across switches S_2 and S_6 show the DC link and supercapacitor voltage, respectively, along with their states. As shown in Figure 10a, the supercapacitor's voltage is boosted from 160 V to the DC link's voltage of 500 V by charging the inductor L_2 . By using the same switches and by reversing the current in the inductor L_2 , the supercapacitor charges from the DC link by reducing the voltage from 500 V to 160 V, as shown in Figure 10b. Power can be exchanged between the battery and supercapacitor, as shown in Figure 11a,b, in mode 3(a) and mode 3(b), respectively, using switches S_3 , S_5 , and S_6 . The SOC of the supercapacitor increases with a low-voltage ripple of charging, and the charging current is negative, which means the current enters the supercapacitor, as shown in Figure 11a. Similarly, the voltage and current ripple of charging the battery from the supercapacitor was less than 0.2 V, as shown in Figure 11b. In mode four, the DC link is boosted to 500 V from the battery's voltage of 200 V and the supercapacitor's voltage of 160 V simultaneously with low-voltage ripples through inductors L_1 and L_2 , as shown in Figure 12. Simultaneously, the supercapacitor and the battery can be charged from the DC link to their voltages by reversing the currents through inductors L_1 and L_2 as well, as shown in Figure 13. Their SOC increases, and the charging currents are negative, which means the currents enter the energy storage devices.

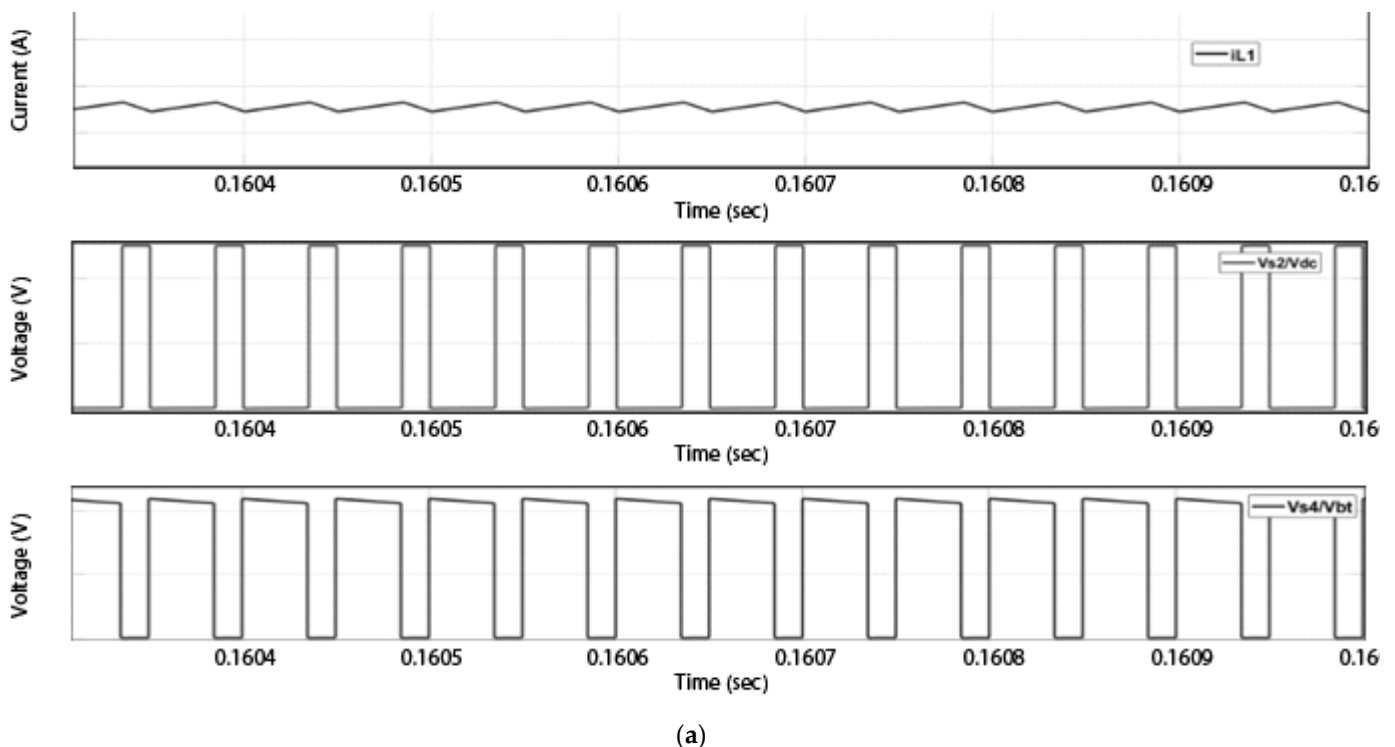


Figure 9. Cont.

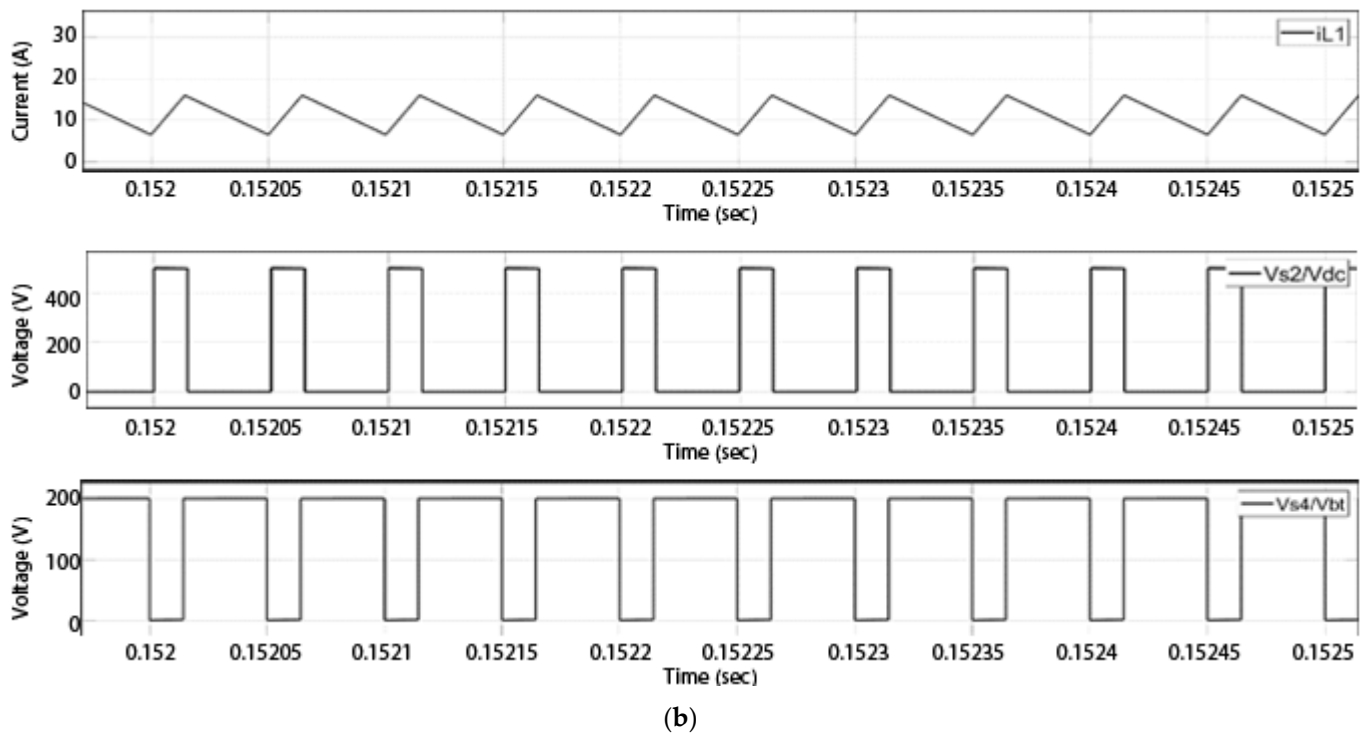


Figure 9. (a) Voltages of switches S_4 and S_2 and current of L_1 in mode 1(a). (b) Voltages of switches S_4 and S_2 and current of L_1 in mode 1(b).

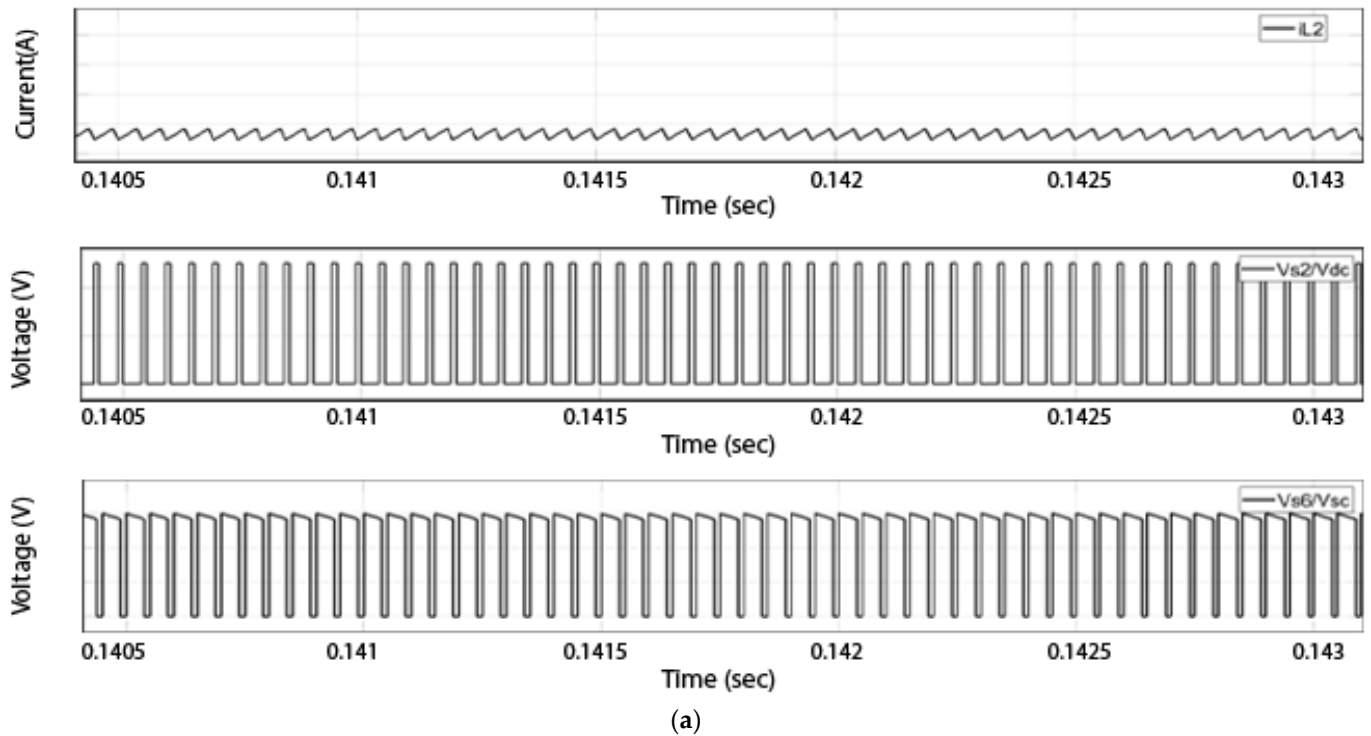


Figure 10. Cont.

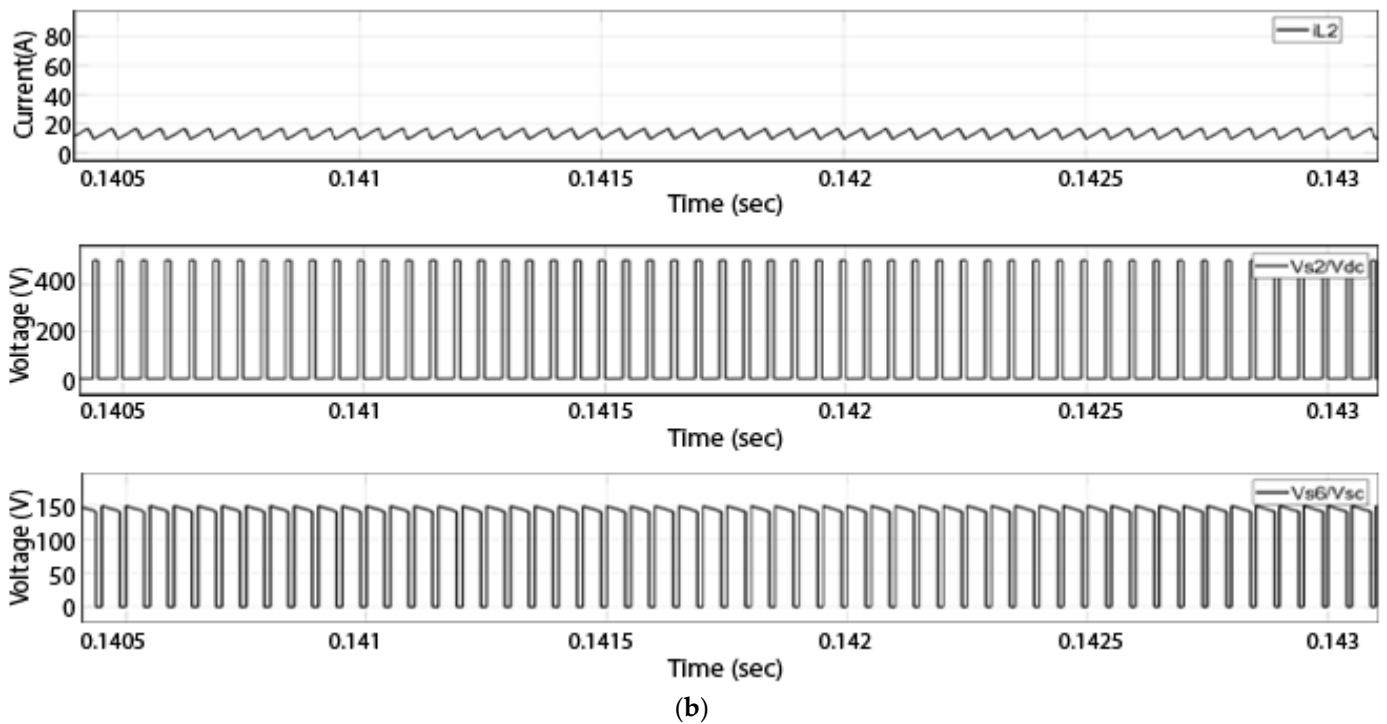


Figure 10. (a) Voltages of switches S_6 and S_2 and current of L_2 in mode 2(a). (b) Voltages of switches S_6 and S_2 and current of L_2 in mode 2(b).

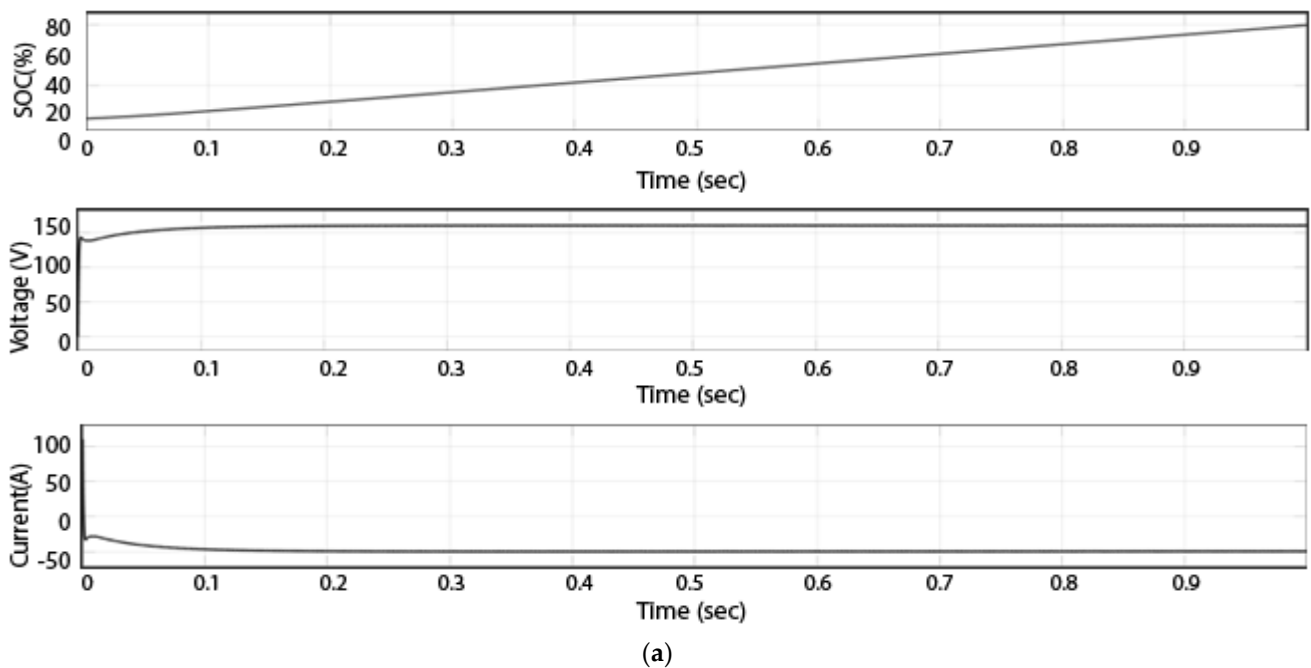


Figure 11. Cont.

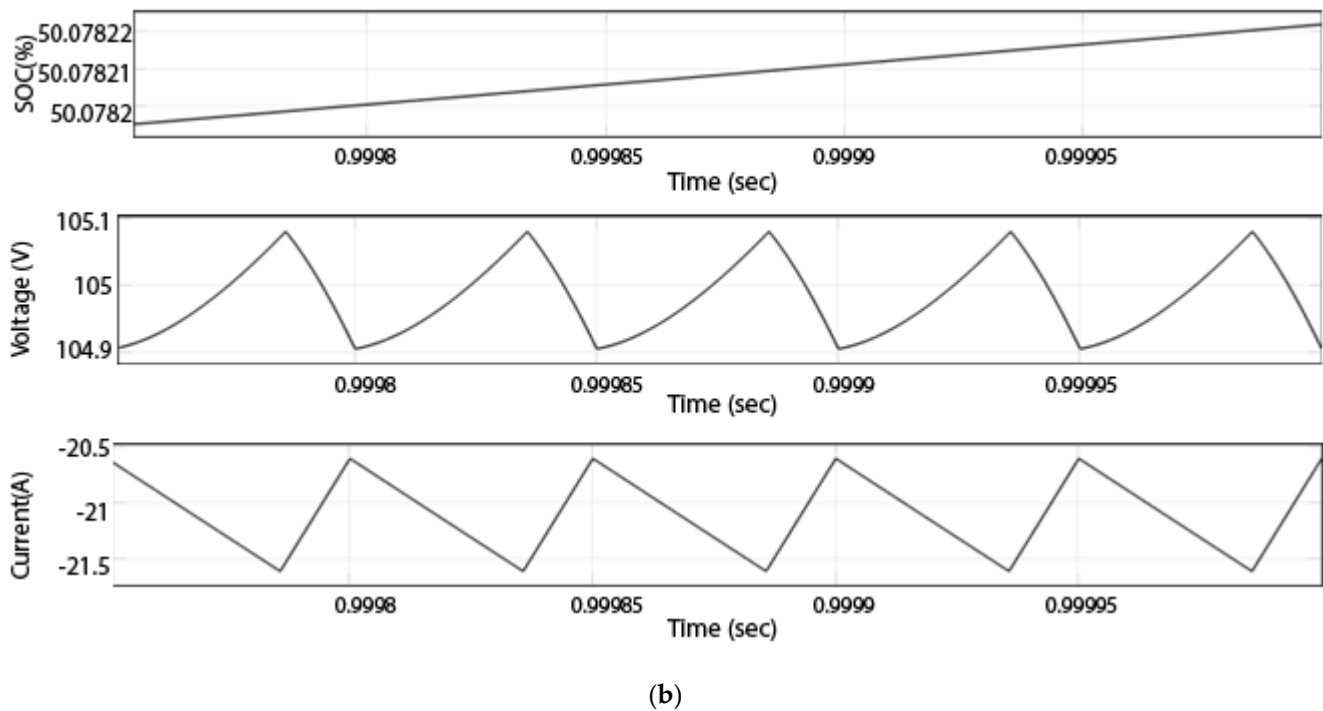


Figure 11. (a) SOC, voltage, and current of supercapacitor in mode 3(a). (b) SOC, voltage, and current of battery in mode 3(b).

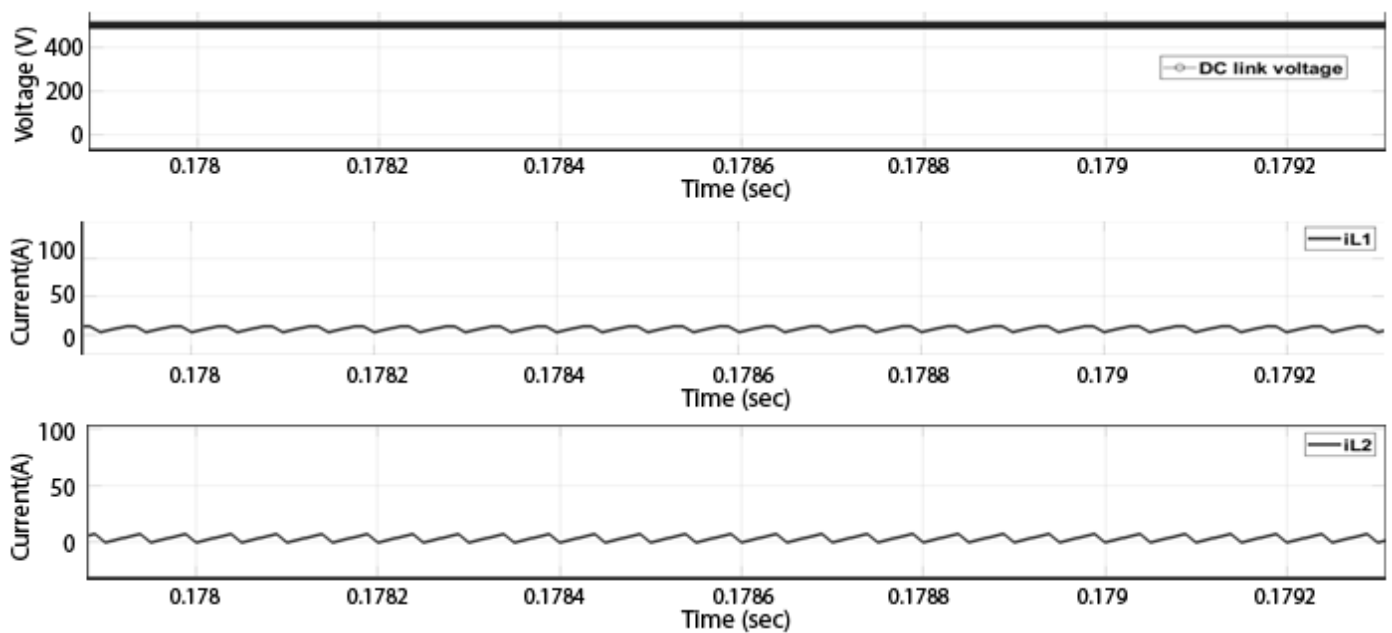


Figure 12. Currents of L_2 and L_1 and DC link voltage in mode 4.

To test the proposed multi-input converter charging capability to charge an EV's battery from the energy storage devices individually or simultaneously without the grid utility, a battery was connected to the buck converter with specifications similar to the Nissan leaf battery (350 V, 60 Ah). It was shown that the battery successfully charges the EV's battery with a charging current of more than 25 A and an output voltage of 350 V, as shown in Figure 14. Using the supercapacitor, the SOC of the EV's battery increased with a charging current of 25 A, as shown in Figure 15. To increase the current charging rate and to reduce the charging time, the EV's battery is charged from the battery and supercapacitor simultaneously, as shown in Figure 16. The charging current was 50 A, and the SOC for

one second of charging was higher than the SOC in the same charging time by charging from the battery or supercapacitor alone.

The simulation results show that the converter can work and charge the EVs at a power rate up to 17 kW, and this is considered level 2 in the SAE J1772 standard [16].

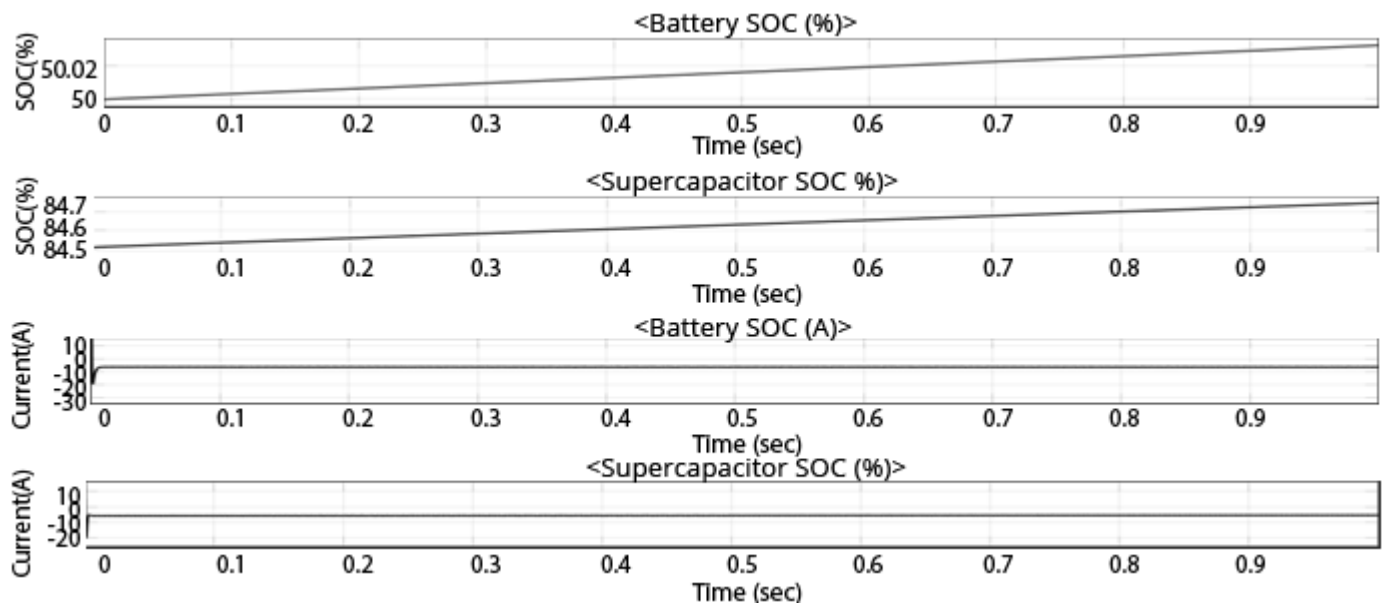


Figure 13. SOC and charging currents of battery and supercapacitor in mode 5.

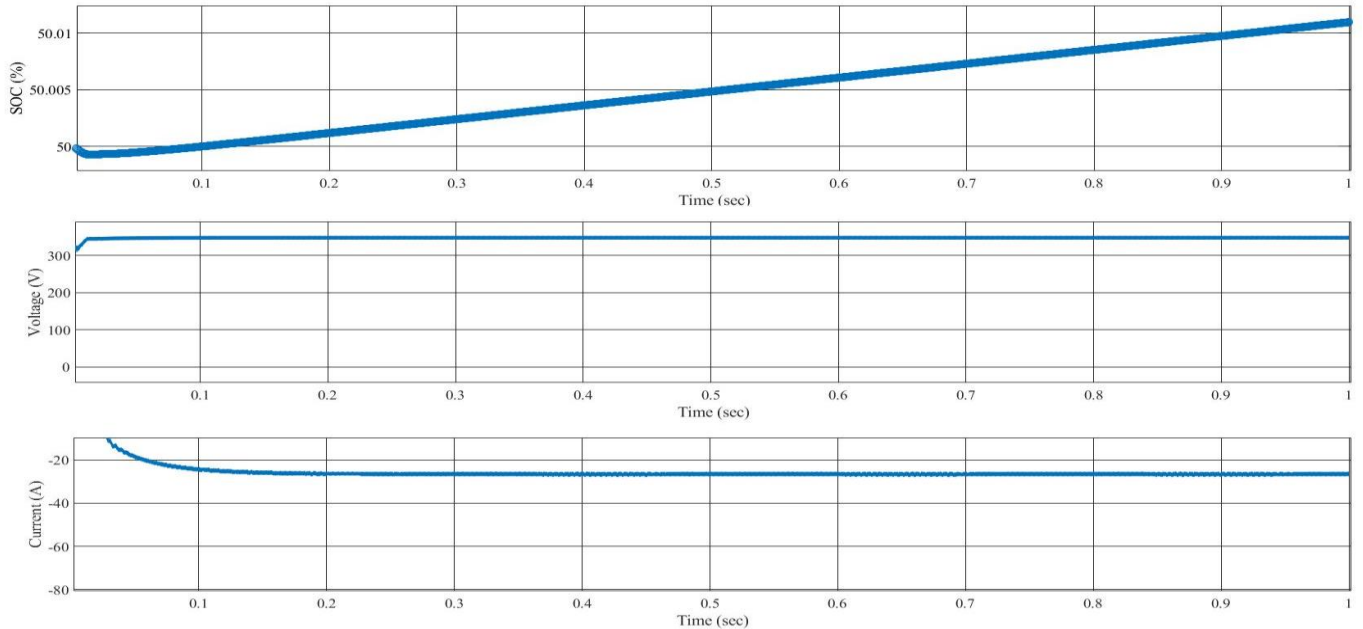


Figure 14. Charging the EV's battery from the battery alone.

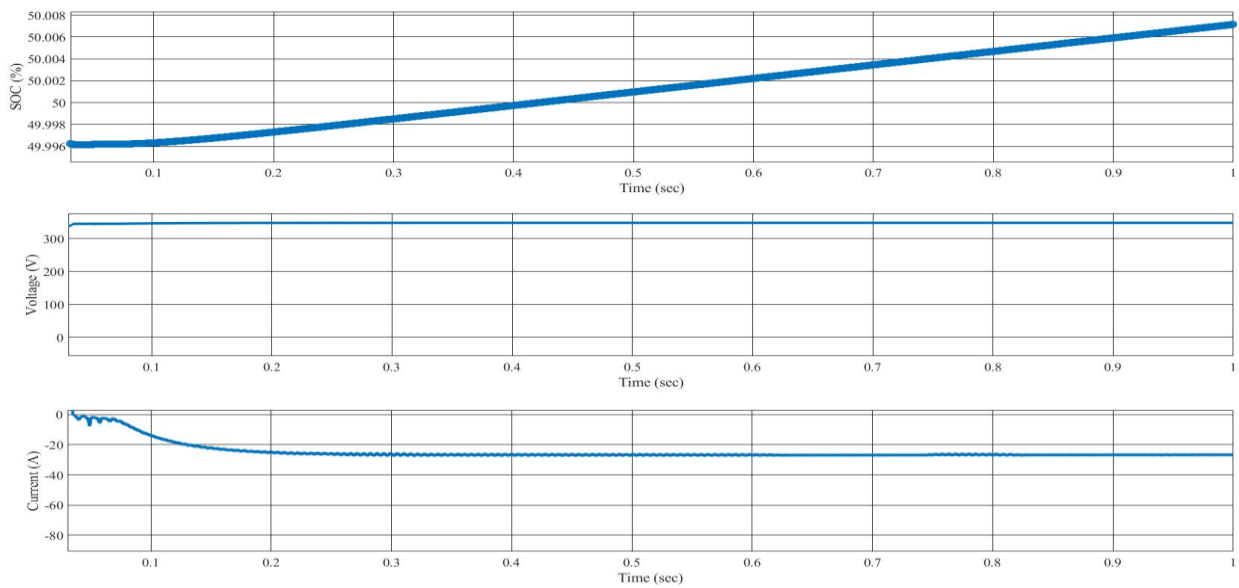


Figure 15. Charging the EV's battery from the supercapacitor alone.

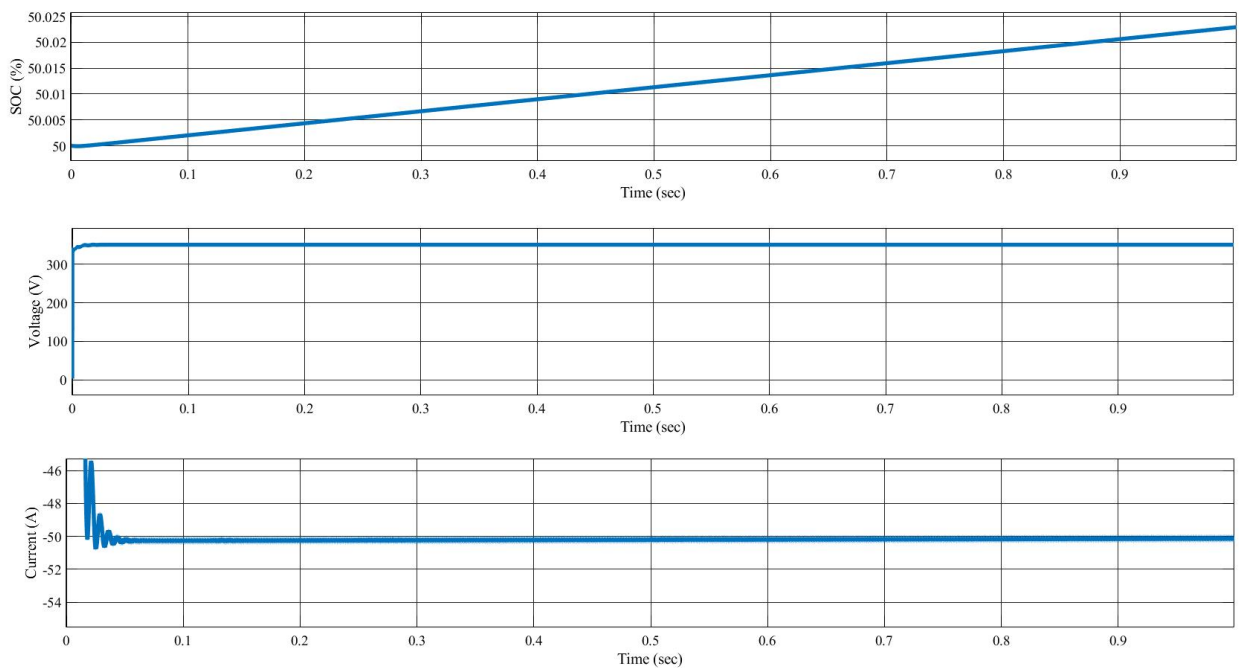


Figure 16. Charging the EVs from both sources simultaneously.

5.2. Experimental Results

Battery, supercapacitor bank, and resistive loads in different values were used in the prototype to validate the switching sequence of the converter and the topology concept. In addition, Arduino UNO was used as a controller with an IRS 2186 MOSFET gate driver to switch the MOSFETs of the multi-input converter prototype and maintain the voltage across the gate and source of the high-side MOSFETs. The prototype of the multi-input DC/DC converter in Figure 17 was tested at a low power rate (up to 100 W) to test the functionality of the converter in different operation modes in different switching sequences and to prove the concept of the topology. The prototype in Figure 17 was used to test the capability of the converter at different power rates with different loads. Table 10 shows the design specifications of the multi-input converter prototype.

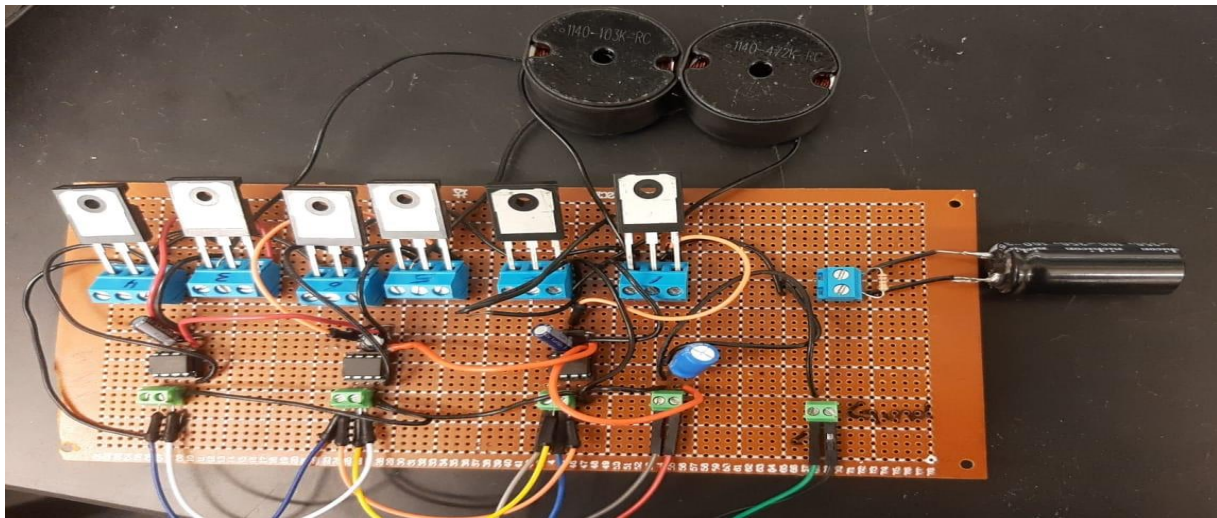


Figure 17. Multi input converter prototype.

Table 10. Design specification of the multi-input converter prototype.

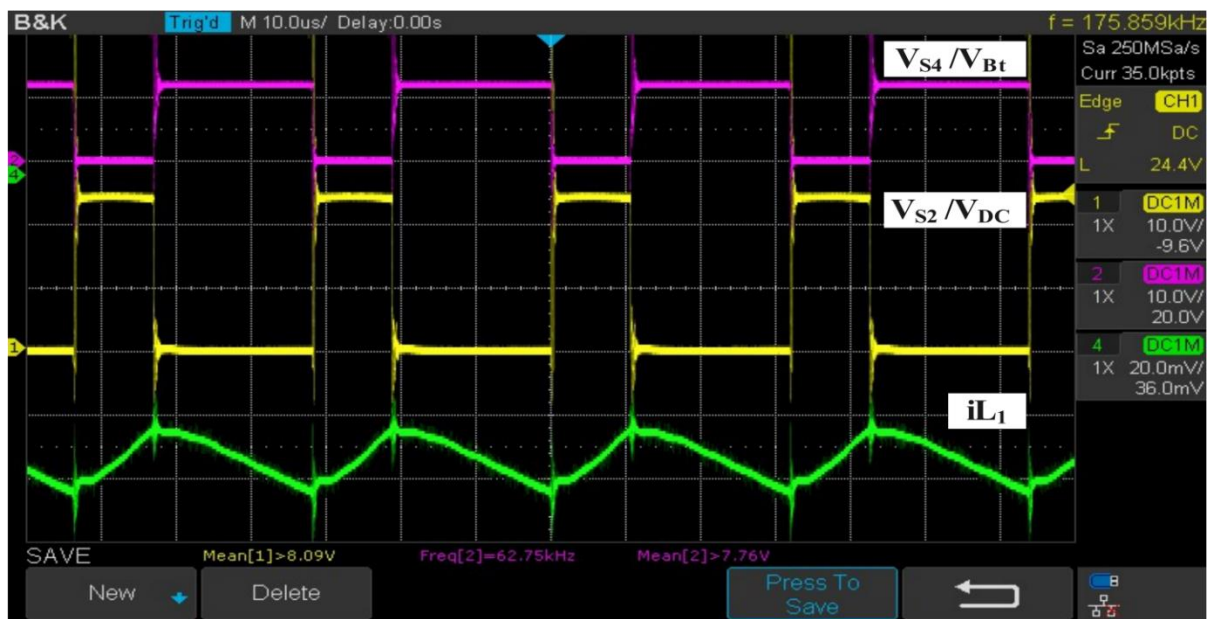
Specifications	Battery	Supercapacitor	DC Link	Inductor (L_1)	Inductor (L_2)	Capacitor (C)	Switching Frequency (f_s)	Load	Power
Values	12 V	15 V	24 V	2 mH	2 mH	100 μ F	30 kHz	10 k Ω	100 W

5.2.1. Supplying the DC Link from the Battery Alone

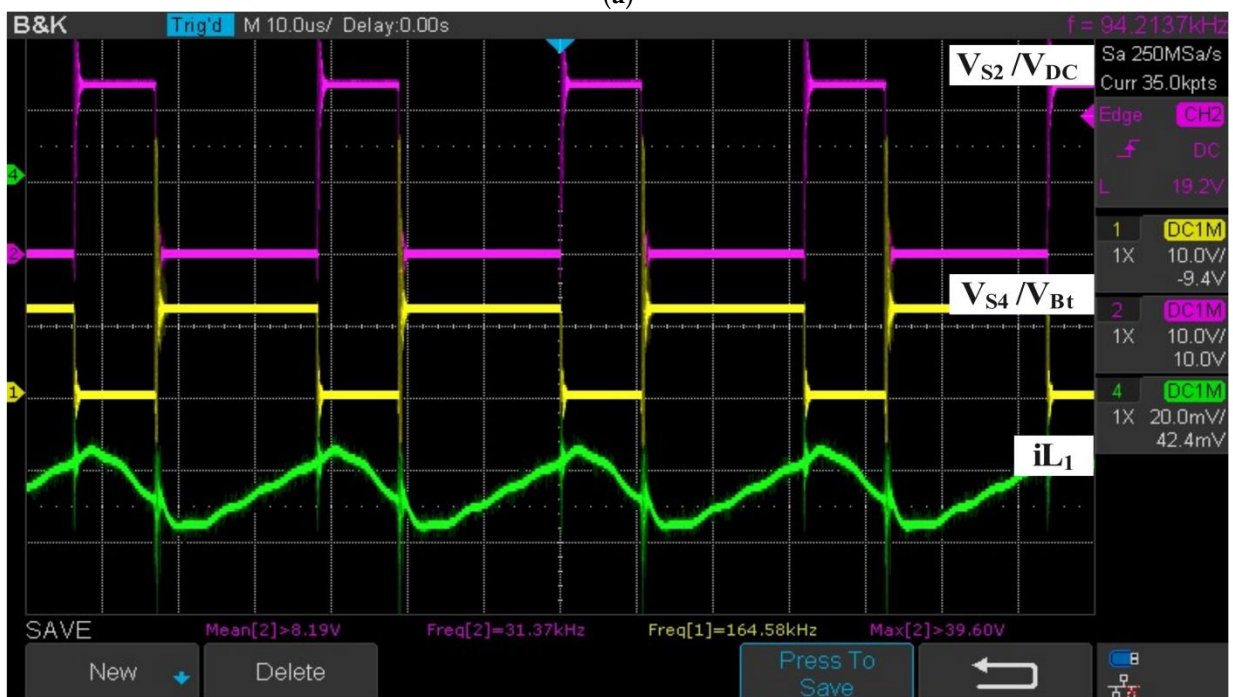
The battery's voltage was boosted from 12 V to 24 V with complementary signals, as shown in Figure 18a, by switching the MOSFETs S_3 and S_2 complementarily with S_1 and S_4 . Inductor L_1 is charged when switches S_3 and S_2 are turned on from the battery and discharged in the DC link when the S_1 and S_4 are turned on, as the voltage across S_2 presents the DC link's voltage, and the voltage across S_4 presents the battery's voltage. Vice versa, the DC link voltage buck from approximately 24 V to 12, as shown in Figure 18b, by operating switches S_1 and S_4 complementarily with S_3 and S_2 . The charging current is reversed in L_1 to charge the battery from the DC link, as shown in Figure 18b, which also indicates the reversal of the direction of power to approve the bidirectional power flow capability of the proposed converter.

5.2.2. Supplying the DC Link from the Supercapacitor Only

Identical to the previous mode, the supercapacitor's voltage was boosted from 15 V to 24 V with complementary signals, as shown in Figure 19a, but by switching the MOSFETs S_5 and S_2 complementarily with S_1 and S_6 . At this operation mode, the inductor L_2 is charged and discharged to transfer the power from the supercapacitor to the DC link. Inductor L_2 is charged when the switches S_5 and S_2 are turned on and discharged in the DC link when the switches S_1 and S_6 are turned on, as shown in Figure 19a, in which the slope of the inductor current returns towards the zero value. Vice versa. the DC link voltage bucks from approximately 24 V to 15, as shown in Figure 19b, by operating switches S_1 and S_6 complementarily with S_5 and S_2 . As presented in Figure 19b, the current of inductor L_2 reverses its current in mode 2(b), so the i_{L_2} slope returns to zero, which means the inductor L_2 is discharging when the switches S_5 and S_2 are turned on.

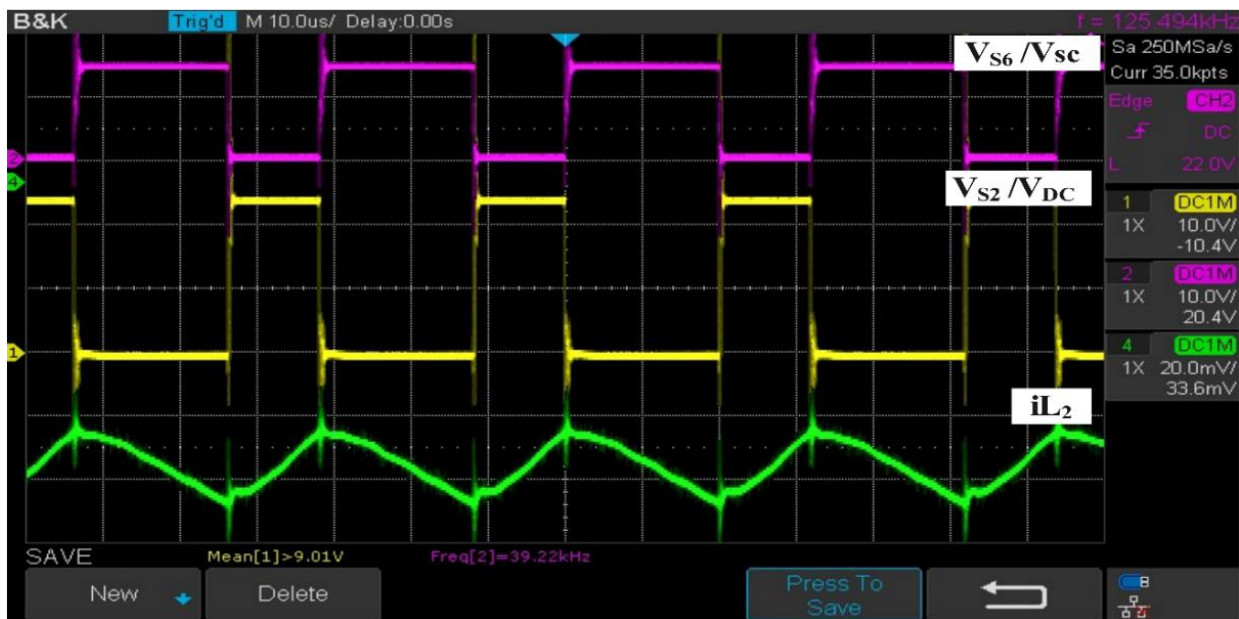


(a)

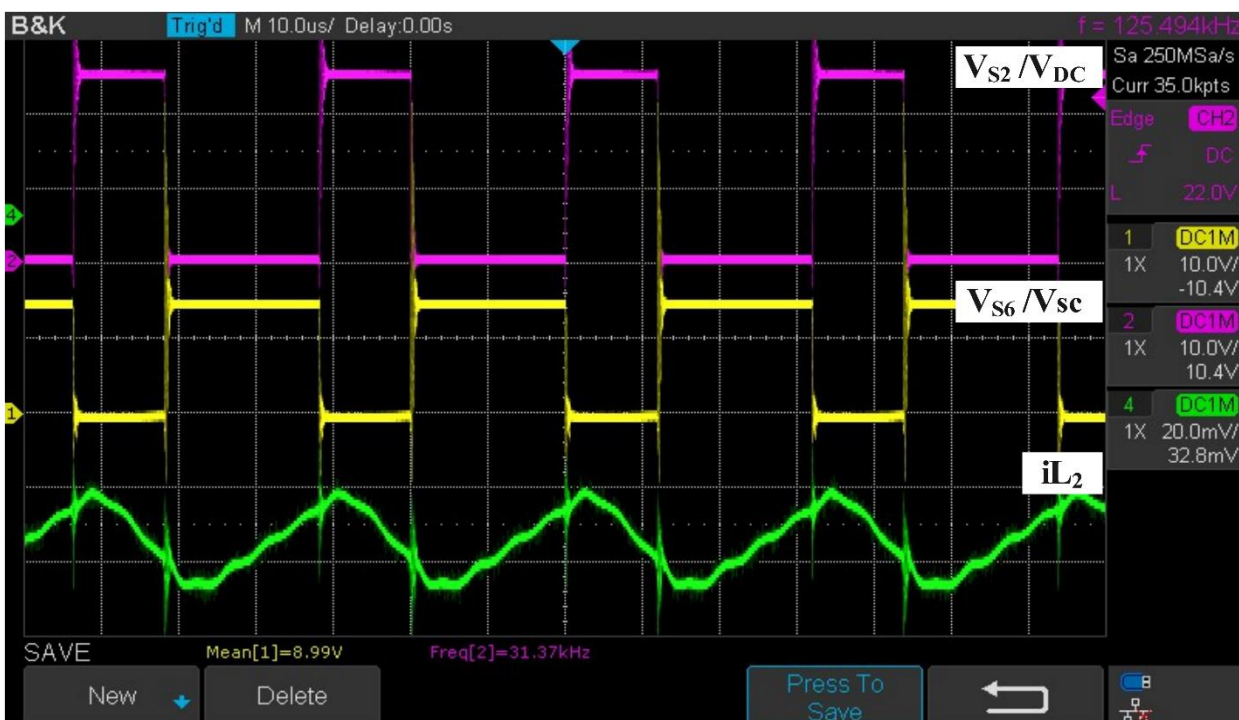


(b)

Figure 18. (a) Voltage across switch S_2 , V_{S2} (scale: 20 V/div), and voltage across switch S_4 , V_{S4} (scale: 10 V/div). (b) Mode 1(b): voltage across switch S_2 , V_{S2} (scale: 20 V/div), and voltage across switch S_4 , V_{S4} (scale: 10 V/div).



(a)



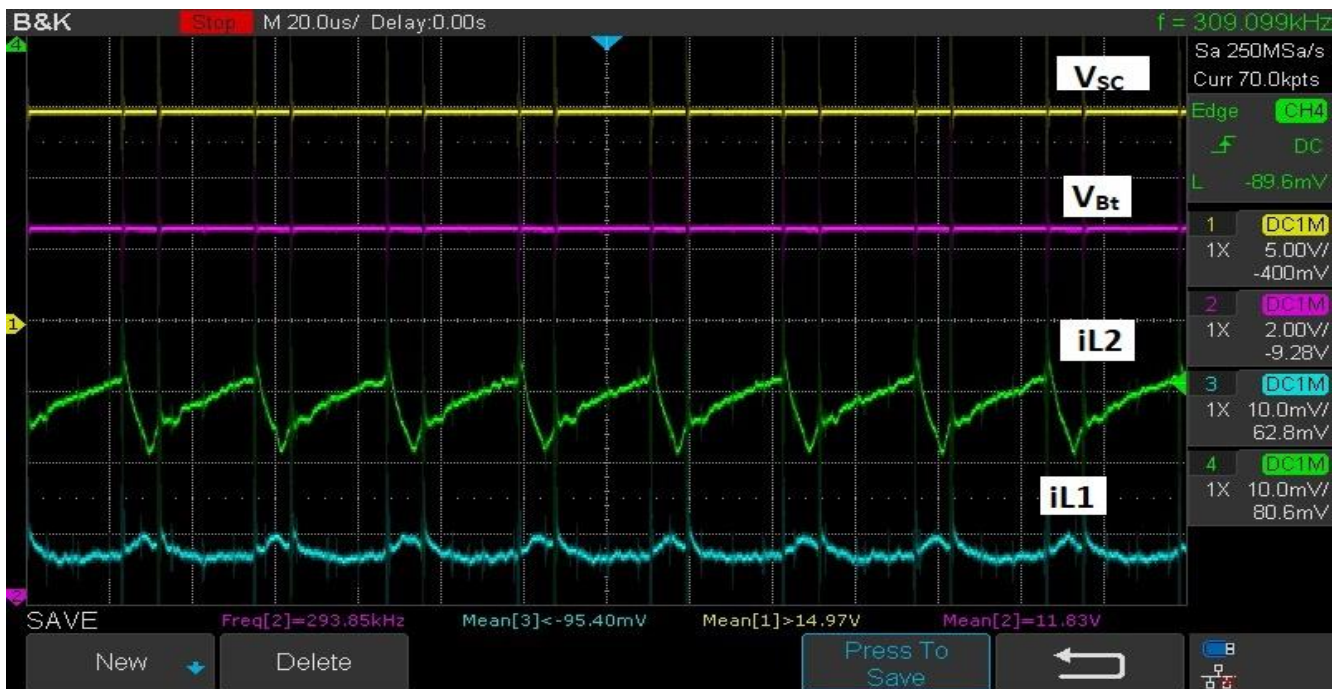
(b)

Figure 19. (a) Mode 2(a): charging and discharging of inductor L_2 . (b) Mode 2(b): charging and discharging of inductor L_2 .

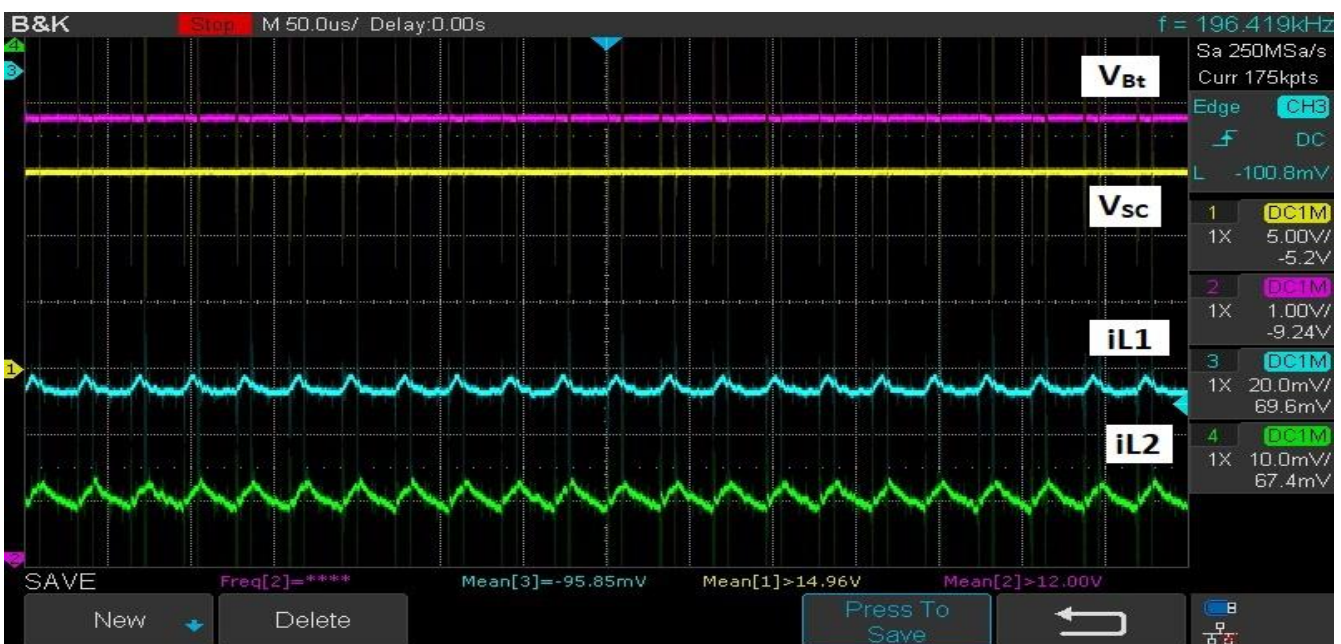
5.2.3. Supplying the Battery from the Supercapacitor

To extend the lifetime of the battery, this operation mode was used and tested as the supercapacitor voltage bucked from 15 V to 12 V using switches S_3 , S_5 , and S_6 . As shown in Figure 20a, the inductors L_1 and L_2 are charged at the same time from the supercapacitor to discharge in the battery, and vice versa, the battery voltage is boosted from approximately 12 V to 15, as shown in Figure 20b, by reversing the currents in inductors L_1 and L_2 .

Additionally, the figures below show the current ripples reduce; hence, the two inductors are series.



(a)



(b)

Figure 20. (a) Mode 3(a): charging battery from the supercapacitor using inductors L1 and L2. (b) Mode 3(b): Charging supercapacitor from the battery by reversing the currents in inductors L1 and L2.

5.2.4. Supplying the DC Link from the Battery and the Supercapacitor

The inductors L_1 and L_2 are charged simultaneously in this operation mode from the battery, and the supercapacitor uses switches S_2 , S_3 , and S_5 , as shown in Figure 21. Then, the inductors discharge their energy at the DC link using switches S_1 , S_4 , and S_6 to boost

the DC link voltage to 24 V, as shown in Figure 21. The voltage across switch S_2 represents the DC link's voltage, and the voltage across switch S_6 shows the supercapacitor's voltage.

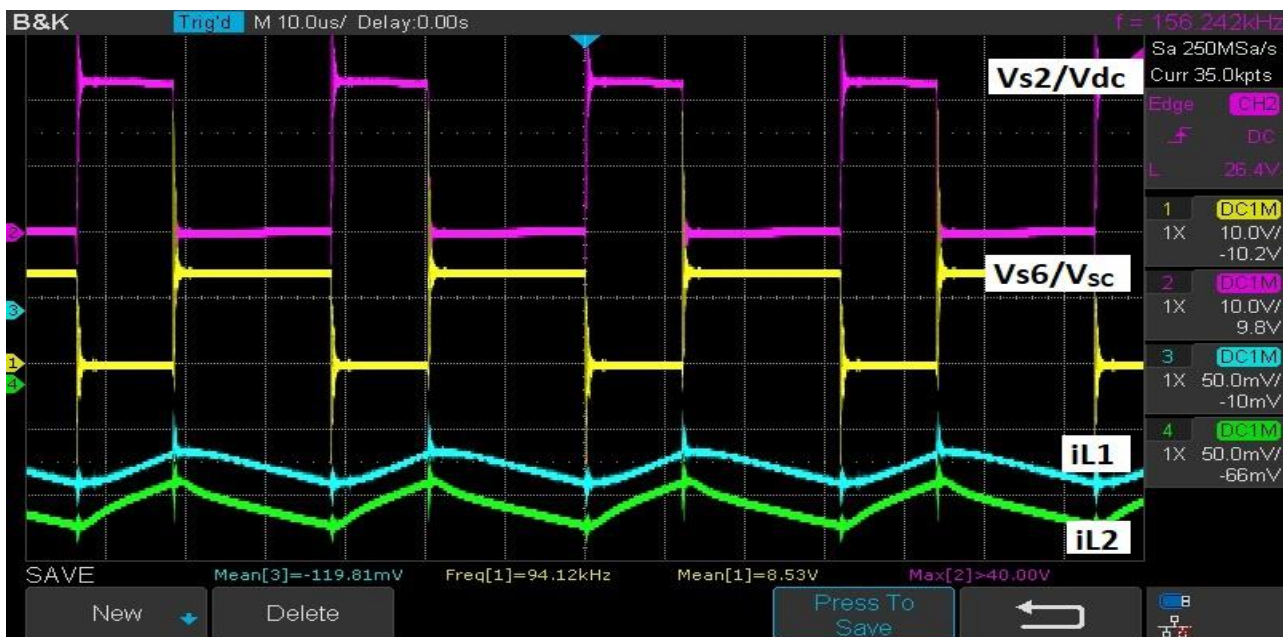


Figure 21. Simultaneously charging the DC link from the battery and supercapacitor.

5.2.5. Charging the Battery and the Supercapacitor from the DC Link

In this operation mode, the battery and supercapacitor are charged simultaneously from the DC link by reversing the L_1 and L_2 inductors' currents compared to the previous mode. Identical to the last operation mode, the inductors L_1 and L_2 are charged simultaneously from the DC link using switches S_1 , S_4 , and S_6 , as shown in Figure 22. Then, the inductors discharge their energy at the battery and the supercapacitor using S_2 , S_3 , and S_5 , as shown in Figure 22. The voltage across switch S_2 represents the DC link's voltage, and the voltage across switch S_6 shows the supercapacitor's voltage.

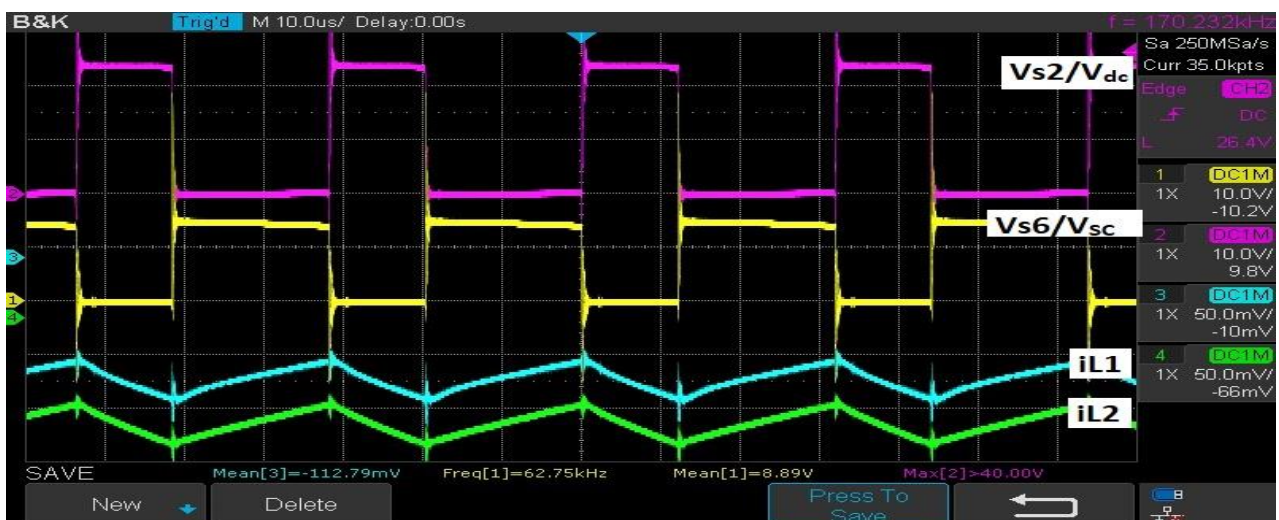


Figure 22. Simultaneously charging the battery and supercapacitor from the DC link in mode 5.

6. Conclusions

This paper discusses and analyzes a multi-input DC/DC converter to integrate a hybrid energy system for a proposed fast-charging station of electrical vehicles. The five operating modes of the converter were simulated successfully, and the losses and the efficiency of the converter were calculated in the different operation modes. The voltage ripples during most of the operation modes were less than 5%, and the efficiency was higher than 90%. The proposed converter was tested to charge a Nissan Leaf battery (350 V, 60 Ah), individually and simultaneously from the energy storage devices. The output results show the converter successfully charges the EV's battery individually on average with 25 A, and simultaneously on average with 50 A. The experimental results validate the concept topology of the converter, specifically the switching sequence to charge and discharge the energy storage devices in different scenarios at a low power rate of (100 W). It also shows the bidirectional power flow capability of the converter. As shown in Figure 9a, they have similar waveforms to Figure 18a, but in different values, which proves the functionality of the converter, especially the switching sequence. Similarly, the output results in Figure 10b are identical to Figure 19b, which validates the simulation results and the bidirectional power flow capability. Figure 20 validates the functionality of the converter to exchange power between two energy storage devices, as presented before in the simulation results. Figures 21 and 22 show the capability of the converter to charge and discharge two sources simultaneously, as shown before in the simulation results. Therefore, to operate the converter at a high-power rate, we need to adjust the values of the specification design to match the high-power rate applications. By utilizing the feature of the modularity of the converter, the input power can be enlarged by adding more power sources, hence increasing the charging power rate, and reducing the charging time. The future scope of this work is to integrate more power sources, test the prototype at a high-power rate, and optimize the control system.

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