

Article **An Improved Fault Diagnosis Algorithm for Highly Scalable Data Center Networks †**

Wanling Lin ¹ [,](https://orcid.org/0000-0001-5007-1913) Xiao-Yan Li 1,[∗] **[,](https://orcid.org/0000-0001-6295-2072) Jou-Ming Chang 2,**[∗](https://orcid.org/0000-0002-9542-7968) **and Xiangke Wang ¹**

- ¹ College of Computer and Data Science, Fuzhou University, Fuzhou 350108, China; 221010007@fzu.edu.cn (W.L.); n190320060@fzu.edu.cn (X.W.)
- 2 Institute of Information and Decision Sciences, National Taipei University of Business, Taipei 10051, Taiwan
- ***** Correspondence: xyli@fzu.edu.cn (X.-Y.L.); spade@ntub.edu.tw (J.-M.C.)
- † This paper is an extended version of the paper published in Security and Privacy in Social Networks and Big Data, 7th International Symposium, SocialSec 2021, Fuzhou, China, 19–21 November 2021; pp. 42–57.

Abstract: Fault detection and localization are vital for ensuring the stability of data center networks (DCNs). Specifically, adaptive fault diagnosis is deemed a fundamental technology in achieving the fault tolerance of systems. The highly scalable data center network (HSDC) is a promising structure of server-centric DCNs, as it exhibits the capacity for incremental scalability, coupled with the assurance of low cost and energy consumption, low diameter, and high bisection width. In this paper, we first determine that both the connectivity and diagnosability of the *m*-dimensional complete HSDC, denoted by *HSDCm*(*m*), are *m*. Further, we propose an efficient adaptive fault diagnosis algorithm to diagnose an $HSDC_m(m)$ within three test rounds, and at most $N + 4m(m - 2)$ tests with $m \geq 3$ (resp. at most nine tests with $m = 2$), where $N = m \cdot 2^m$ is the total number of nodes in $HSDC_m(m)$. Our experimental outcomes demonstrate that this diagnosis scheme of HSDC can achieve complete diagnosis and significantly reduce the number of required tests.

Keywords: data center networks; diagnosability; adaptive diagnosis; hamiltonian; cycle decomposition

MSC: 05C38; 05C45; 68R10

1. Introduction

Cloud computing has emerged as a promising paradigm across diverse domains within the information and communication technology (ICT) field. Notably, it has witnessed a growing adoption in various research areas, including e-commerce, nuclear science, agriculture, healthcare, and smart grids [\[1\]](#page-14-0). *Data center networks* (DCNs) play a pivotal role in cloud computing infrastructure, facilitating essential cloud services like GFS, Bigtable, and Dryad. As critical components supporting both 5G and cloud computing, DCNs have evolved into hubs for diverse resources and business service centers. An ideal DCN should possess exceptional scalability, optimize the utilization of switches and servers, and exhibit robust fault tolerance. The existing data center network structures can be divided into two categories: *switch-centric DCNs* and *server-centric DCNs*. For switch-centric DCNs, such as Fat-tree [\[2\]](#page-14-1) and PortLand [\[3\]](#page-14-2), switches deal with routing and addressing, and servers act as endpoint hosts that send and receive data. In a server-centric DCN, servers equipped with multiple network ports (NICs) connect to several layers of mini-switches, which function exclusively as crossbars. These servers are additionally tasked with carrying out computationally intensive operations. The well-known server-centric DCNs include DCell [\[4\]](#page-14-3), BCCC [\[5\]](#page-14-4), RCube [\[6\]](#page-14-5), and HSDC [\[7\]](#page-14-6).

This paper is an extended research of [\[8\]](#page-14-7), which discusses a *high scalability data center network* (HSDC for short), a server-centric DCN known for its desirable features, such as incremental scalability, cost-efficiency, low energy consumption, short diameter, and high bisection width. For a server-centric DCN, each switch can be considered a transparent

Citation: Lin, W.; Li, X.-Y.; Chang, J.-M.; Wang, X. An Improved Fault Diagnosis Algorithm for Highly Scalable Data Center Networks. *Mathematics* **2024**, *12*, 597. [https://](https://doi.org/10.3390/math12040597) doi.org/10.3390/math12040597

Academic Editor: Guy De Tré

Received: 18 December 2023 Revised: 18 January 2024 Accepted: 15 February 2024 Published: 17 February 2024

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device so that we can define the *logic graph* of the HSDC as L-HSDC concerning the connection among servers. Since an HSDC has not been proposed for too long, there is not much related literature available. Let us briefly introduce it below. Qin et al. [\[9\]](#page-14-8) treated an L-HSDC as a class of compound graphs and provided an algorithm to construct multiple completely independent spanning trees (CISTs). Dong et al. [\[10\]](#page-14-9) determined the connectivity, tight super connectivity, and diameter of an L-HSDC and proposed an algorithm to obtain the shortest path between any two distinct nodes in the HSDC. For an *n*-dimensional HSDC, Yang et al. [\[11\]](#page-14-10) showed that the L-HSDC is vertex-transitive and designed a construction scheme for building *n* independent spanning trees (ISTs). As the design scheme relies upon only the node address and tree index, such a construction can be easily implemented in parallel. Dong et al. [\[12\]](#page-14-11) studied the Hamiltonian properties in an L-HSDC and proved that the *n*-dimensional L-HSDC is (*n* − 3)-fault-tolerant Hamiltonianconnected and (*n* − 2)-fault-tolerant Hamiltonian for *n* ≥ 3. He et al. [\[13\]](#page-14-12) constructed a 2-disjoint path cover with prescribed end nodes in an HSDC.

However, with the ongoing expansion of DCNs in terms of scale and traffic load, it becomes evident that failures occur inevitably [\[14\]](#page-14-13). To ensure the network's reliability, it is imperative that, whenever a server is identified as faulty, it should be substituted with a fault-free server. The method for detecting faulty processors within the system, achieved through the testing and interpretation of test results, is called *system-level faulty diagnosis* [\[15\]](#page-14-14). Likewise, when each server in a server-centric DCN is regarded as a processor within a multi-processor system, the diagnosis of servers in a server-centric DCN aligns with the diagnosis of processors in a multi-processor system. Preparata, Metze, and Chien [\[16\]](#page-14-15) initially introduced the *PMC model*, a system-level diagnosis model for multiprocessor systems. Specifically, this model assumes that a processor, acting as a *tester u*, sends a test message to its neighbor, acting as a *testee v*, where $r(u, v)$ represents the test. When *u* is fault-free, the outcome of $r(u, v)$ can be used to deduce the state of *v*. That is, if the outcome of $r(u, v)$ is 1 (resp. 0), v is faulty (resp. fault-free). However, if u is faulty, then the outcome of $r(u, v)$ becomes unreliable, rendering the state of v unreliable as well. For the sake of easy description, $r(u, v) = 1$ (resp. $r(u, v) = 0$) is called 1-*arrow* (resp. 0-*arrow*). Moreover, the *diagnosability* of a system is the maximum number of faulty processors that the system can self-identify. A system is said to be *t-diagnosable* when all faulty processors can be accurately detected, provided that the number of faulty processors does not exceed *t*. For a *t*-diagnosable *N*-processor system, every processor should be tested by at least *t* other processors in non-adaptive diagnosis if as many as *t* processors may be faulty. In this case, it is obvious that *Nt* tests are necessary under the PMC model, which will lead to inefficient diagnostics.

To overcome this shortage, an *adaptive diagnosis* scheme was proposed by Nakejima [\[17\]](#page-15-0), in which tests can be scheduled dynamically during the diagnosis process according to the previous test outcomes. The adaptive diagnostic process is conducted in several rounds, and each processor is allowed at most one test participation per round. Diagnosis time is gauged by the number of test rounds, and the diagnosis cost is determined by the quantity of tests administered. This flexibility is a key factor in significantly enhancing diagnostic efficiency [\[18–](#page-15-1)[20\]](#page-15-2). For example, in a completely connected system, it has been demonstrated that the number of tests required and deemed adequate to identify at most *t* faulty processors decreases from *Nt* to *N* + *t* − 1 under the PMC model [\[21\]](#page-15-3). In addition, several well-known adaptive diagnosis algorithms for interconnected network topologies have been investigated, such as hypercube networks [\[22,](#page-15-4)[23\]](#page-15-5), hierarchical multiprocessor systems [\[24\]](#page-15-6), butterfly networks [\[25\]](#page-15-7), and Hamiltonian networks [\[26\]](#page-15-8). However, the existing fault diagnosis schemes for DCNs generally lack adaptability and exhibit low diagnostic efficiency [\[27–](#page-15-9)[29\]](#page-15-10). Simultaneously, there is a scarcity of research focused on fault diagnosis in HSDC networks. Thus, the development of adaptive diagnostic schemes for DCNs has emerged as a pressing research topic.

In this paper, we develop an adaptive fault diagnostic scheme to deal with the large number of faulty servers existing in HSDC networks under the PMC model, which can diagnose all states of servers within three test rounds. The key contributions of this work are listed as follows:

- We determine the connectivity and diagnosability of HSDC networks, which are fundamental properties of the DCNs.
- Based on the Hamiltonian cycle and the technique of cycle decomposition, we design an adaptive diagnosis algorithm to identify the exact status of all servers in HSDC networks. This is the first study to explore adaptive diagnosis in DCNs.
- We conduct experiments to assess the performance of the proposed algorithm and demonstrate that our scheme can significantly reduce the number of tests for improving the efficiency of fault diagnosability.

The rest of the paper is organized as follows: Section [2](#page-2-0) gives some basic knowledge and the formal definition of HSDC networks. In Section [3,](#page-4-0) we explore the connectivity and diagnosability of HSDC networks. We propose an adaptive diagnosis algorithm and analyze the maximum number of tests of our algorithm in Section [4.](#page-6-0) We verify the performance of the proposed algorithm in Section [5.](#page-10-0) Finally, we conclude this paper in Section [6.](#page-13-0)

2. Preliminaries

In this section, we first provide some necessary notations used in this paper, most of which are referenced from Ref. [\[30\]](#page-15-11). A data center network (resp. a system) is usually modeled as an undirected simple graph $G = (V(G), E(G))$, where the *node set* $V(G)$ and the *edge set* $E(G)$ represent the set of servers (resp. processors) and the set of communication channels between servers (resp. processors), respectively. Let $|V(G)|$ denote the total number of nodes in *G*. Two distinct nodes *u* and *v* are *adjacent* if $(u, v) \in E(G)$, where *u* is called a *neighbor* of *v*, and vice versa. The graphs *G* and *R* are *isomorphic*, denoted by $G \cong R$, if there is a bijection $\varphi: V(G) \to V(R)$ such that $(u, v) \in E(G)$ if and only if $(\varphi(u), \varphi(v)) \in E(R)$. The *connectivity* of *G*, denoted by $\kappa(G)$, is the minimum number of nodes whose removal makes *G* disconnected. A *path P* in a graph *G*, denoted by $P = \langle v_0, v_1, \ldots, v_\ell \rangle$, is a subgraph of *G* with the node set $V(P) = \{v_0, v_1, \ldots, v_\ell\}$ and edge set $E(P) = \{(v_i, v_{i+1}) | v_i, v_{i+1} \in V(P) \text{ for } 0 \le i \le \ell - 1\}$, where v_0 and v_ℓ are two *end-nodes* of *P*. A path whose two end-nodes are identical is called a *closed path*. A *cycle* C of length $\ell + 1$ for $\ell \geq 2$ is a closed path $\langle v_0, v_1, \ldots, v_\ell, v_0 \rangle$. A *Hamiltonian path* (resp. *Hamiltonian cycle*) is a path (resp. cycle) that contains every node of *G* exactly once. A *complete graph* with *m* nodes, denoted as *Km*, is a graph in which arbitrarily two distinct nodes are adjacent. An *m*-dimensional *hypercube* network, denoted by *Qm*, has the node set $V(Q_m) = \{x_m x_{m-1} \cdots x_1 | x_i \in \{0,1\} \text{ for } 1 \le i \le m\}$ and edge set $E(Q_m) = \{ (x_m x_{m-1} \cdots x_i \cdots x_1, x_m x_{m-1} \cdots \bar{x}_i \cdots x_1) | x_i \in \{0,1\}, 1 \le i \le m, \bar{x}_i = 1 - x_i \}.$ For integers m, n with $n < m$, let $[n, m] = \{n, n+1, \ldots, m\}$ be the set of integers, and particularly, denote $[m] = [n, m]$ if $n = 1$.

2.1. HSDC Networks

An HSDC network adopts low-cost commodity *m*-port switches and dual-port servers. The structure of HSDC networks can be divided into two categories: *complete structure* and *incomplete structure*. Specifically, the ports of all servers in the complete HSDC network are occupied, while there are idle ports of servers in the incomplete HSDC network. Two different types of HSDC networks are defined as follows:

Definition 1 (see [\[7\]](#page-14-6)). *The m-dimensional complete HSDC network is denoted by* $\text{HSDC}_m(m)$ with the node set $\{x_mx_{m-1}\cdots x_1; y\,|\,x_i\in\{0,1\}, i\in[m], y\in[0,m]\}$. For a node $x_mx_{m-1}\cdots x_1; y,$ *if* $y = 0$, *it is a switch; otherwise, it is a server. A switch* $x_m x_{m-1} \cdots x_1$; 0 *is connected to a server* $x_mx_{m-1}\cdots x_1$; y for any $y\in[m]$. Moreover, two servers $x_mx_{m-1}\cdots x_1$; y and $x'_mx'_{m-1}\cdots x'_1$; y' *are connected if and only if the following conditions hold:*

(1) $y = y' \in [m];$ (2) $x_y = 1 - x'_y$ and $x_i = x'_i$ for any $i \in [m] \setminus \{y\}.$

Figure [1](#page-3-0) shows that $HSDC_4(4)$ has 16 switches and 64 servers. In $HSDC_m(m)$, a switch and its adjacent servers constitute the basic building unit called a *block*. Each *HSDCm*(*m*) contains 2 *^m* blocks. For *HSDCm*(*m*), if we treat each block as a single node and connect them through the remaining edges, we can obtain an *m*-dimensional hypercube network *Qm*. Next, we will introduce an *m*-dimensional incomplete HSDC network, which is denoted by $HSDC_m(n)$.

Figure 1. A 4-dimensional complete HSDC network $HSDC₄(4)$.

Definition 2 (see [\[7\]](#page-14-6)). For $m > n$, the network HSDC_m(n) has the node set $\{x_n x_{n-1} \cdots x_1; y \}$ $x_i \in \{0,1\}, i \in [n], y \in [0,m]\}.$ For a node $x_n x_{n-1} \cdots x_1$; y, if $y = 0$, it is a switch; otherwise, α corner Δ equitch α α Moreover, two servers $x_n x_{n-1} \cdots x_1$; y and $x'_n x'_{n-1} \cdots x'_1$; y' are connected if and only if the بالمنبه للنازلوجيوم ومردانسوما 011;31 0011;4 0111;4 *following conditions hold:* it is a server. A switch $x_n x_{n-1} \cdots x_1$; 0 is connected to a server $x_n x_{n-1} \cdots x_1$; y for any $y \in [m]$.

- (1) $y = y' \in [n];$ Server Switcher Switcher
	- 1000;1 1000;4 1001;4 \overline{u} 1100;1 1101;4 \mathfrak{m} \mathfrak{n} $\begin{array}{ccc} \overline{2} & \overline{1} & \overline$ (2) $x_y = 1 - x'_y$ and $x_i = x'_i$ for any $i \in [n] \setminus \{y\}.$

 101102 chows $HSD($ 5011 <u>ان ب</u> \sim contains *m* − *n* servers with an idle port. Thus, the node subset $\{x_n x_n - 1 \cdots x_1; y \mid x_i \in$ Figure [2](#page-3-1) shows $HSDC_4(2)$ with 4 switches and 16 servers. For $HSDC_m(n)$, each block $\{0,1\}, i \in [n], y \in [n+1,m]\}$ of $V(HSDC_m(n))$ is a set of servers with an idle port.

Figure 2. A 4-dimensional incomplete HSDC network $HSDC₄(2)$.

Lemma 1 (see [7]). $HSDC_m(m)$ composed of 2^{m-n} $HSDC_m(n)s$.

It is clear that *HSDCm*(*n*) is a subgraph of *HSDCm*(*m*). Furthermore, if each server with an idle port in 2^{m-n} $HSDC_m(n)$ s is connected by Conditions (1) and (2) in Definition 1, then we will obtain the $HSDC_m(m)$ network.

2.2. The Logic Graph of HSDC Networks

For HSDC networks, if each switch is regarded as a transparent device, then we can define the *logic graph* of $HSDC_m(m)$ (resp. $HSDC_m(n)$), denoted as $L\text{-}HSDC_m(m)$ (resp. L -*HSDC*_{*m*} (n)), as follows.

Definition 3 (see [\[9\]](#page-14-8)). For $m \ge 2$, the graph L-HSDC_m(m) has the node set $\{x_mx_{m-1}\cdots x_1; y\}$ $x_i \in \{0,1\}$, i, $y \in [m]\}$, and two nodes $x_mx_{m-1}\cdots x_1$; y and $x'_mx'_{m-1}\cdots x'_1$; y' are adjacent if and *only if one of the following conditions holds:*
(3) $\frac{1}{100}$ $\frac{1}{100}$; $\frac{1}{100}$

- (1) $y \neq y'$ and $x_i = x'_i$ for any $i \in [m]$;
- (2) $y = y', x_y = 1 x'_y$ and $x_i = x'_i$ for any $i \in [m] \setminus \{y\}.$

Definition 4. For $m > n \ge 2$, the graph L-HSDC_m(n) has the node set $\{x_n x_{n-1} \cdots x_1; y \mid x_i \in$ $\{0,1\}, i \in [n], y \in [m]\}$, and two nodes $x_n x_{n-1} \cdots x_1$; y and $x'_n x'_{n-1} \cdots x'_1$; y' are adjacent if and *only if one of the following conditions holds:*

- (1) $y \neq y'$ and $x_i = x'_i$ for any $i \in [m]$; .
' and x; : $[m]$:
- (2) $y = y', y \in [n], x_y = 1 x'_y, and x_i = x'_i$ for any $i \in [n] \setminus \{y\}.$ (2) $y = y', y \in [n]$ $x_y = 1 - x'_y$, and $x_i =$ $iny i \in [n] \setminus \{y\}.$

If the edge (*u*, *v*) satisfies Condition(1) (resp. Condition(2)) of Definitions [3](#page-4-1) or [4,](#page-4-2) then it is called *intra-edge* (resp. *inter-edges*). As shown in Figure [3,](#page-4-3) we can observe that one block of *L*-*HSDC*4(4) induced by a node set {0000; 1, 0000; 2, 0000; 3, 0000; 4} is a complete graph K_4 . Moreover, $(1010; 4, 1010; 1)$ is an intra-edge and $1010; 4$ is an *intra-neighbor* of $1010; 1$. (1000; 4, 0000; 4) is an inter-edge and 1000; 4 is an *inter-neighbor* of 0000; 4.

Figure 3. A logic graph of $HSDC₄(4)$.

3. The Connectivity and Diagnosability of L-HDSC

In this section, we investigate the connectivity and diagnosability of *L-HSDC_m*(*m*). For *L-HSDC_m(m),* it is clear that each block can be regarded as a node in an *m-*dimensional

hypercube network *Qm*. For convenience, we use the notation *Bⁱ* to represent each block of *L*-*HSDC_{<i>m*}</sub>(*m*) with *i* ∈ [0,2^{*m*} − 1], where $B_i \cong K_m$.

Lemma 2 (see [\[31\]](#page-15-12))**.** *For any two distinct nodes u and v, there are m node-disjoint paths connecting u and v in an m-dimensional hypercube network Qm.*

Lemma 3. *For m* \geq 2*, the connectivity of L-HSDC_{<i>m*}</sub>(*m*) *is m, i.e.,* κ (*L-HSDC_{<i>m*}(*m*)) = *m*.

Proof. Clearly, we have $V(Q_m) = \{B_0, B_1, \ldots, B_{2^m-1}\}$. Let *u* and *v* be any two distinct nodes of *L*-*HSDCm*(*m*). We will show how to construct *m* node-disjoint paths joining *u* and *v* in the following two cases.

Case 1. *u* ∈ *V*(*B*_{*i*})</sub> and *v* ∈ *V*(*B*_{*j*}) with *i* ≠ *j* ∈ [0,2^{*m*} − 1].

For each B_s and $b \in V(B_s)$, these paths between node b and b' are node-disjoint with $b' \in V(B_s) \setminus \{b\}$. By Lemma [2,](#page-5-0) for B_i and B_j , we can deduce that there exist *m* node-disjoint paths between *Bⁱ* and *B^j* in *Qm*. From the property of *Qm*, we have that each node has *m* neighbors in Q_m . By Definition [3,](#page-4-1) each node of B_s is connected to a unique node of $B_{s'}$ in L -*HSDC*^{*m*}(*m*), where $(B_s, B_{s'}) \in E(Q_m)$ and $s, s' \in [0, 2^m - 1]$. For each node $b \in V(B_s)$, it shows that there exist *m* node-disjoint paths between node *b* and *c*, where *c* is a node of $B_{s'}$. Hence, for any two distinct nodes $u, v \in V(L \cdot HSDC_m(m))$, there are *m* node-disjoint paths joining *u* and *v* when $u \in V(B_i)$ and $v \in V(B_i)$ with $i \neq j \in [0, 2^m - 1]$.

Case 2. *u*, *v* ∈ *V*(*B*_{*i*}) with *i* ∈ [0, 2^{*m*} − 1].

For each $B_i \cong K_m$, there are $m-1$ node-disjoint paths between *u* and *v* in B_i . Without a loss of generality, let $u = x_m x_{m-1} \cdots x_1; y$ and $v = x_m x_{m-1} \cdots x_1; y'$, where $y \neq y'$. By Definition [3,](#page-4-1) we assume that the node $z = x_m x_{m-1} \cdots x_{y+1} x'_y x_{y-1} \cdots x_1$; *y* is an interneighbor of *u*, where $x'_y = 1 - x_y$. By Case 1, there are *m* node-disjoint paths between node *z* and *v* in *L*-*HSDC*_{*m*}(*m*). Thus, there exists at least one path $P = \langle u, z, \dots, v \rangle$ such that $V(P) \cap V(B_i) = \{u, v\}$. Hence, there are *m* node-disjoint paths joining *u* and *v* when *u*, *v* ∈ *V*(*B*_{*i*}) with *i* ∈ [0, 2^{*m*} − 1]. \Box

Lemma 4 (see [\[16\]](#page-14-15)). *Given a system* $G = (V, E)$ *, two conditions are necessary for G to be t*-diagnosable: (1) $|V| > 2t + 1$, and (2) each node is tested by at least t other nodes.

Lemma 5 (see [\[32\]](#page-15-13))**.** *Two conditions are sufficient for a system G with n nodes to be t-diagnosable:* (1) $n \geq 2t + 1$ and (2) $\kappa(G) \geq t$.

Theorem 1. *For m* \geq 2*, the diagnosability of L-HSDC_{<i>m*}</sub>(*m*) *is m.*

Proof. For $m \ge 2$, $|V(L \cdot HSDC_m(m))| = m2^m > 2m + 1$, and $\kappa(L \cdot HSDC_m(m)) = m$. Hence, by Lemmas [4](#page-5-1) and [5,](#page-5-2) the diagnosability of L - $HSDC_m(m)$ is m . \Box

Moreover, we can observe that L - $HSDC_m(2)$ contains four $K_m's$, and there exists a Hamiltonian path between x_2x_1 ; 1 and x_2x_1 ; 2 in each K_m , where $x_1, x_2 \in \{0, 1\}$. Since *L*-*HSDC_{<i>m*}</sub>(2) contains four edges—(00; 1, 01; 1), (10; 1, 11; 1), (00; 2, 10; 2), and (01; 2, 11; 2) we can construct a Hamiltonian cycle with 4*m* nodes in *L*-*HSDCm*(2) (see Figure [4\)](#page-6-1). Thus, we have the following lemma.

Lemma 6. For $m \geq 2$, there exists a Hamiltonian cycle in L-HSDC_{*m*}(2).

Figure 4. A Hamiltonian cycle of *L*-*HSDC*4(2), whose edges are colored by blue lines.

4. Adaptive Diagnosis Algorithm for *HSDC*

In this section, we design an efficient adaptive diagnosis scheme for *L*-*HSDCm*(*m*) with at most *m* faulty nodes. First, we will show the skeleton of the adaptive diagnosis algorithm for *L*-*HSDCm*(*m*) in Algorithm [1.](#page-6-2) Then, the details of the algorithm will be described in the following subsections.

Algorithm 1: An adaptive diagnosis scheme for L -*HSDC_m*(*m*) with at most *m* faulty nodes.

Input: An *L*-*HSDC_{<i>m*}</sub>(*m*) with at most *m* faulty nodes.

- **Output:** The status of all nodes in L -*HSDC_{<i>m*}</sub>(*m*).
- **¹** Partition *L*-*HSDCm*(*m*) into 2 *^m*−² *^L*-*HSDCm*(2)s, denoted by *^H*0, *^H*1, . . . , *^H*2*m*−2−¹ ;
- **2** Construct a Hamiltonian cycle for each *H_i* with *i* ∈ [0,2^{*m*−2} − 1];
- **³** For the first two rounds, conduct basic tests along each Hamiltonian cycle in parallel;
- **⁴** Count the total number of suspicious cycles, denoted by *fc*;
- **5 if** f_c ≥ *m* − 1 **then** // See Section [4.2](#page-7-0)
- **6** Partition each suspicious cycle into path(s) by Algorithm [2](#page-7-1) in parallel;
- **⁷** Color nodes on each path by Algorithm [3](#page-7-2) in parallel;
- **8** Identify the faulty nodes of all paths.
- **⁹ else** // See Section [4.3](#page-8-0)
- 10 For each H_i with a suspicious cycle, the fault diagnosis process is as follows:
- **¹¹** (1) Each node that has an inter-neighbor *u* in the clean cycle is identified by *u*;
- 12 \vert (2) The unknown nodes can be identified by valid fault-free neighbors in B^j_i *i*
	- with $j \in [0,3]$ (B^j_i $\frac{1}{i}$ is defined in Section [4.3\)](#page-8-0);
- 13 (3) Identify the remaining nodes in H_i .
- **¹⁴ end**

4.1. Basic Tests of the First Two Rounds

By Definitions [3](#page-4-1) and [4,](#page-4-2) *L*-*HSDCm*(*m*) can be divided into 2 *^m*−² *L*-*HSDCm*(2)s with *m* ≥ 2. For convenience, each *L*-*HSDC_{<i>m*}</sub>(2) is represented by *H_i* with *i* ∈ [0,2^{*m*−2} − 1]. By Lemma [6,](#page-5-3) there is a Hamiltonian cycle with 4*m* nodes in each *Hⁱ* . For a Hamiltonian cycle $\langle v_0, v_1, \cdots, v_{4m-1}, v_0 \rangle$, in the first round, all even nodes test odd nodes in a clockwise direction (i.e., v_{2j} tests v_{2j+1}), and in the second round, all odd nodes test even nodes in a clockwise direction (i.e., v_{2j+1} tests v_{2j+2}), where $j \in [0, 2m-1]$ and "+" means modulo 4*m* addition. If a Hamiltonian cycle contains 1-arrows, then we call the Hamiltonian cycle a *suspicious cycle;* otherwise, it is regarded as a *clean cycle*. Obviously, a suspicious cycle contains at least one faulty node and every node is fault-free in a clean cycle. Then, all faulty

nodes should be in these suspicious cycles after Line 3 when Algorithm [1](#page-6-2) is performed. At this time, the nodes of these suspicious cycles need to be tested to identify their exact status. Let *f^c* be the total number of suspicious cycles in *L*-*HSDCm*(*m*). Moreover, we call the tests in the first and second round *basic tests*, and the tests required by the algorithm in the subsequent execution process are called *additional tests*.

4.2. Identifying Suspicious Cycles for $f_c \geq m - 1$

When $f_c \geq m - 1$, we provide two algorithms to diagnose the nodes of suspicious cycles. Specifically, Algorithm [2](#page-7-1) is used to partition a suspicious cycle into paths such that each path contains at least one faulty node [\[16\]](#page-14-15). Algorithm [3](#page-7-2) colors the nodes of the path as black, white, or gray, where the black nodes are faulty, the white nodes are fault-free, and the grey nodes are unknown nodes that need additional tests to identify their exact statuses [\[33\]](#page-15-14). Figure [5a](#page-7-3) gives an example for executing Algorithm [2](#page-7-1) in *L*-*HSDC*4(2) with at most 4 faulty nodes, and Figure [5b](#page-7-3) gives the results of executing Algorithm [3](#page-7-2) on the three paths.

Algorithm 3: Path–color.

Input: Paths obtained by Algorithm [2.](#page-7-1)

Output: The color of each node.

- **Step 1**: Let *m* be the maximum number of faulty nodes, *f^s* be the number of the paths obtained by Algorithm [2,](#page-7-1) and $q = m - f_s + 1$;
- **Step 2**: If a path has more than *q* + 1 nodes and at least one 0-arrow, then we color the head of the path with black and $q - 1$ nodes from the tail with gray, and all remaining nodes that are not colored yet are colored white;
- **Step 3**: If a path has at most *q* + 1 nodes, then we color all nodes of the path with gray.

Figure 5. (**a**) An example of executing the algorithm cycle–partition, where a red arrow indicates 1-arrow. (**b**) An example of executing the algorithm path–color.

Let f_s be the total number of paths of an *L*-*HSDC_{<i>m*}</sub>(*m*). For $f_c \geq m - 1$, since there are at most *m* faulty nodes in *L*-*HSDCm*(*m*), we can deduce that each suspicious cycle has **15 11** one or two faulty nodes and $f_c \le f_s \le m$. Then, we have the following two cases:

Case 1: $f_c = f_s$.

At this time, each suspicious cycle contains only one path after executing Algorithm [2.](#page-7-1) **1** As shown in Figure [6a](#page-8-1), it is evident that only the black node is faulty when $f_s = m$. Based on Algorithm [2,](#page-7-1) there is only one gray node adjacent to the black node on each suspicious **2 8** cycle when $f_s = m - 1$. Thus, the black node is faulty, and the gray node can be tested by another fault-free neighbor (see Figure [6b](#page-8-1)). **4 6**

Figure 6. Four examples of additional tests for $f_c \geq m - 1$: (a,b) when $f_c = f_s$ in Case 1; (c,d) when $f_c \neq f_s$ in Case 2, where a red arrow indicates 1-arrow, and a green counterclockwise arrow indicates an additional test.

Case 2: $f_c \neq f_s$.

At this time, a suspicious cycle contains two paths after executing Algorithm [2.](#page-7-1) Thus, we have $f_c = m - 1$ and $f_s = f_c + 1 = m$. If both paths in this suspicious cycle contain at least three nodes, then only the black node is faulty (see Figure [6c](#page-8-1)); otherwise, this suspicious cycle has a path that contains a black node and a path that only contains two gray nodes, where one gray node is adjacent to the black node and the other is adjacent to a fault-free node (see Figure [6d](#page-8-1)). Thus, only one of these two gray nodes is faulty and can be verified by its fault-free neighbor.

4.3. Identifying Suspicious Cycles for $f_c < m - 1$

In this subsection, we discuss the situation of $f_c < m - 1$. Recall that each H_i contains four blocks, denoted by B_j^j i_i , where $i \in [0, 2^{m-2} - 1]$ and $j \in [0, 3]$.

Theorem 2. *For any Hⁱ that has a suspicious cycle, at least one fault-free node in Hⁱ can be identified by its inter-neighbors when* $f_c < m - 1$ *.*

Proof. By Definition [3,](#page-4-1) each H_i is connected to $m-2$ distinct H_s 's in $L\text{-}HSDC_m(m)$, where $i \neq s \in [0, 2^{m-2} - 1]$. For $i, s \in [0, 2^{m-2} - 1]$ and $j \in [0, 3]$, each B_i^j i_i is connected to *m* − 2

distinct H_s 's in *L-HSDC_m*(*m*). Since there is one suspicious cycle in H_i , we can infer that *m* − 2 neighbors of *H_i* (i.e., *m* − 2 *H_s*[']s) contain *c_i* clean cycles and *m* − 2 − *c_i* suspicious cycles, where $1 \leq c_i \leq m-2$. Suppose that each node in H_i that has an inter-neighbor *u* in the clean cycles is identified as faulty by *u*. Then, all suspicious cycles have at least $(m-2-c_i) + 4c_i = m + 3c_i - 2$ faulty nodes. As $m + 3c_i - 2 > m$, it contradicts that *L*-*HSDC*^{*m*}(*m*) has at most *m* faulty nodes. \square

For any *Hⁱ* that has a suspicious cycle, the detailed diagnosis process is as follows:

- **Step 1:** Each node that has an inter-neighbor *u* in the clean cycles is identified by *u*.
- **Step 2:** If there are nodes that have been identified as fault-free in B_i^j with $j \in [0,3]$, the remaining unknown nodes in *B j* \mathbf{F}_i are identified by these fault-free nodes. In particular, if there is no fault-free node found in B^j_i $\frac{j}{i}$, then we call B_i^j $\frac{d}{dt}$ an *unknown block*; otherwise, B_t^j *i* is a *known block*. It is clear that each node in the unknown block is either an identified faulty node or an unknown node. Let n_i be the total number of unknown blocks in H_i . If $n_i = 0$, then the process is terminated. Thus, the purpose of Step 2 is to identify all unknown nodes in known blocks by their fault-free neighbors.
- **Step 3:** Count the number of faulty nodes identified in all known blocks of *Hⁱ* , denoted by f_i . By Theorem [2,](#page-8-2) we can deduce that $n_i \leq 3$. Then, the remaining unknown nodes in the unknown blocks can be identified by the following three cases.

Case 1. $n_i = 1$.

At this time, *Hⁱ* contains one unknown block that has at least *cⁱ* identified faulty nodes. Since H_i connects with $m - 2 - c_i$ suspicious cycles, we can deduce that all suspicious cycles have at least $(m - 2 - c_i) + c_i = m - 2$ faulty nodes. Hence, there are at least $m - 2 + f_i$ faulty nodes in *L-HSDC_m*(*m*). Thus, we have $0 \le f_i \le 2$ and the following three subcases. **Case 1.1.** $f_i = 0$.

It is clear that every node in the known blocks of *Hⁱ* is fault-free, and there exist at most two faulty nodes in all unknown blocks. Without loss of generality, we suppose that *u* and *v* are two nodes in the unknown block, where $u = x_m x_{m-1} \cdots x_1; 1, v = x_m x_{m-1} \cdots x_2 x_1; 2$, $x_i \in \{0, 1\}$, and $i \in [m]$. By Definition [3,](#page-4-1) as $f_i = 0$, both *u* and *v* have a fault-free interneighbor in the unknown blocks of *Hⁱ* and can be identified by this fault-free inter-neighbor. If *u* and *v* are identified as faulty, then the remaining unknown nodes in the unknown block are fault-free; otherwise, at least one of *u* and *v* will be identified as fault-free by their fault-free inter-neighbor. Hence, the remaining unknown nodes in the unknown block can be identified.

Case 1.2. $f_i = 1$.

As $f_i = 1$, it follows that only one node is faulty in all known blocks of H_i , and at most one node is faulty in all unknown blocks. Suppose that *u* and *v* are two nodes in the unknown block, where $u = x_m x_{m-1} \cdots x_1$; 1, $v = x_m x_{m-1} \cdots x_1$; 2, $x_i \in \{0, 1\}$ and $i \in [m]$. For $f_i = 1$, all suspicious cycles have at least $(m - 2 - c_i) + c_i + f_i = m - 1$ faulty nodes, and at least one of *u* and *v* has a fault-free inter-neighbor. Without a loss of generality, we assume that u has a fault-free inter-neighbor. If u is identified as faulty by its fault-free inter-neighbor, the remaining unknown nodes in the unknown block must be fault-free; otherwise, the remaining unknown nodes in the unknown block can be identified by *u*.

Case 1.3. $f_i = 2$.

All suspicious cycles have at least $(m - 2 - c_i) + c_i + f_i = m$ faulty nodes, and each unknown node in the unknown block is fault-free.

Case 2. $n_i = 2$.

At this time, H_i contains two unknown blocks that have at least $2c_i$ identified faulty nodes. Since H_i connects with $m - 2 - c_i$ suspicious cycles, we can infer that all suspicious cycles have at least $(m - 2 - c_i) + 2c_i = m + c_i - 2$ faulty nodes. Further, there are at least $m-2+c_i+f_i$ faulty nodes in $L\text{-}HSDC_m(m)$. Thus, we have $1\leq c_i+f_i\leq 2$ and the following two subcases.

Case 2.1. $c_i = 1$ and $f_i = 0$.

It is obvious that all suspicious cycles have at least *m* − 1 faulty nodes, and there is at most one faulty node in all unknown blocks of *Hⁱ* . Without a loss of generality, we suppose that *u* and *v* are two nodes in the unknown blocks and *u* has a fault-free inter-neighbor. If *u* is identified as faulty by its fault-free inter-neighbor, the remaining unknown nodes in the unknown block must be fault-free; otherwise, the remaining unknown nodes in the unknown block can be identified by *u*.

Case 2.2. $c_i = 2$ and $f_i = 0$ or $c_i = 1$ and $f_i = 1$.

All suspicious cycles have at least *m* faulty nodes and the remaining unknown nodes in the two unknown blocks are fault-free.

Case 3. $n_i = 3$.

It is evident that H_i contains three unknown blocks that have at least $3c_i$ faulty nodes. As H_i connects with $m-2-c_i$ suspicious cycles, it follows that all suspicious cycles have at least $(m − 2 − c_i) + 3c_i = m + 2c_i - 2$ faulty nodes. Further, there are at least $m − 2 + 2c_i + f_i$ faulty nodes in *L*-*HSDC_{<i>m*}</sub>(*m*). Since $c_i \geq 1$ and $2c_i + f_i \leq 2$, we can deduce $c_i = 1$ and $f_i = 0$. Thus, the remaining unknown nodes in the three unknown blocks are fault-free.

4.4. Evaluating the Number of Tests for L-HSDC

Next, we will analyze the maximum number of tests to diagnose all states of nodes in *L*-*HSDCm*(*m*).

Theorem 3. *For L-HSDCm*(*m*) *with at most m faulty nodes, Algorithm [1](#page-6-2) diagnose all states of nodes within* $N + 4m(m-2)$ *tests when* $m \geq 3$ *(resp.* 9 *tests when* $m = 2$ *), where* $N = m \cdot 2^m$ *is the number of nodes in L-HSDC_{<i>m*}(*m*).

Proof. Recall that $N = |V(L \cdot HSDC_m(m))| = m \cdot 2^m$. Since the first round (resp. the second round) involves each even node (resp. odd node) testing adjacent odd nodes (resp. even nodes) in a clockwise direction, we can deduce that every node is tested only once by other nodes in the first two rounds. Hence, the number of basic tests is *N*. When $f_c = f_s = m - 1$, each suspicious path has one unknown node; thus, the total number of additional tests is $m - 1$. If $f_c \geq m - 1$ and $f_s = m$, then there exists a path that only contains two unknown nodes that need one additional test to identify their exact statuses. For $f_c \leq m-2$, each node in suspicious cycles needs at most one additional test to identify its actual status. Since each suspicious cycle has $4m$ nodes, we have that at most $4m \times f_c$ additional tests are necessary to identify the faulty nodes in the *L*-*HSDCm*(*m*). Since the maximum number of additional tests appears at $f_c = m - 2$, we can deduce that at most $N + 4m(m - 2)$ additional tests. Hence, if $m \ge 3$ (resp. $m = 2$), then Algorithm [1](#page-6-2) takes at most *N* + 4*m*(*m* − 2) (resp. 9) tests to identify all faulty nodes in *L*-*HSDC_{<i>m*}(*m*). \Box

5. Simulation Results

Through a series of experiments in this section, we aim to validate the proficiency of our algorithm in diagnosing the actual status of all nodes in *L*-*HSDCm*(*m*), and then compare it with the traditional algorithm, focusing on the number of tests.

5.1. Experimental Setup

This simulation is performed on a personal laptop using a 3.00 GHz Intel[®] CoreTM i5-9500 CPU with 24 GB RAM in a Windows 10 operating system. Our algorithm and related experiments are implemented by using Python programs. The workflow of experiments is presented as follows:

- Randomly generate a logic graph of $L\text{-}HSDC_m(m)$ with f faulty nodes, where $1 \leq f \leq m$.
- Apply Algorithm [1](#page-6-2) to diagnose faulty nodes in *L*-*HSDCm*(*m*).
- Calculate the related metrics (introduced in Section [5.2\)](#page-11-0) and the number of required tests.

Note that all faulty nodes are uniformly distributed over the whole network. All experiments are repeated 1000 times, and the average values are used as the final results.

5.2. Experimental Metrics

First, some notations used in the experiment are listed in Table [1.](#page-11-1)

For diagnostic results, we will introduce three metrics as follows:

• *Precision rate* (PR for short): PR is the ratio of the number of faulty nodes correctly diagnosed by Algorithm [1](#page-6-2) to the total number of faulty nodes diagnosed by Algorithm [1,](#page-6-2) which is calculated as follows:

$$
PR = \frac{|V_f \cap V'_f|}{|V_f \cap V'_f| + |V_g \cap V'_f|}.
$$
\n(1)

• *Recall rate* (RR for short): RR is the ratio of the number of faulty nodes correctly diagnosed by Algorithm [1](#page-6-2) to the total number of actual faulty nodes in *L*-*HSDCm*(*m*), which is calculated as follows:

$$
RR = \frac{|V_f \cap V'_f|}{|V_f \cap V'_f| + |V_f \cap V'_g|}.
$$
\n(2)

• *Unknown rate* (UR for short): UR is the ratio of the number of nodes not diagnosed by Algorithm [1](#page-6-2) to the total number of nodes in *L*-*HSDCm*(*m*), which is calculated as follows:

$$
UR = \frac{|V'_u|}{|V|}.
$$
\n(3)

5.3. Experimental Analyses

For the PMC model, the variable *p* is used to represent the probability that a faulty node, when conducting a test on a neighbor, obtains a test result of 1. In order to observe the impact of parameter *p* on the performance of the adaptive diagnosis strategy presented in this paper, we set $p \in \{0, 0.2, 0.4, 0.6, 0.8\}$ and perform experimental evaluations in *L*-*HSDC*_{*m*}(*m*) with $m \in \{6, 7, 8, 9\}$.

As shown in Table [2,](#page-12-0) we can observe that regardless of the parameter *p*, both PR and RR consistently attain a value of 1, while UR remains at 0. This attests to the precision and efficiency of our adaptive diagnostic algorithm in identifying the states of all nodes in *L*-*HSDC_{<i>m*}</sub> (m) . Importantly, as the network size increases, the correctness of the algorithm remains unaltered, further substantiating the effectiveness and superiority of the diagnostic scheme proposed in this paper.

Next, we conduct a comparison between our adaptive fault diagnostic algorithm and traditional fault diagnosis algorithms, assessing their efficacy in terms of the number of diagnostics tests. Our analysis encompasses data from 1000 experiments, and key parameters are detailed in Table [3.](#page-12-1)

L -HSDC _m (m)	$\mathfrak p$	0.0	0.2	0.4	0.6	0.8	1.0
$m = 6$	PR	1.0	1.0	1.0	1.0	1.0	1.0
	RR	1.0	1.0	1.0	1.0	1.0	1.0
	UR	0.0	0.0	0.0	0.0	0.0	0.0
$m=7$	PR	1.0	1.0	1.0	1.0	1.0	1.0
	RR	1.0	1.0	$1.0\,$	1.0	1.0	1.0
	UR	0.0	0.0	0.0	0.0	0.0	0.0
$m=8$	PR	1.0	1.0	1.0	1.0	1.0	1.0
	RR	1.0	1.0	1.0	1.0	1.0	1.0
	UR	0.0	0.0	0.0	0.0	0.0	0.0
$m = 9$	PR	1.0	1.0	1.0	1.0	1.0	1.0
	RR	1.0	1.0	1.0	1.0	1.0	1.0
	UR	0.0	0.0	0.0	0.0	0.0	0.0

Table 2. The impact of parameter *p* on PR, RR, and UR in *L*-*HSDC_{<i>m*}</sub>(*m*) with $m \in [6, 9]$.

Table 3. A description of the related test parameters.

Symbol	Description				
Avg	The average number of tests required for Algorithm 1.				
Max	The maximum number of tests required for Algorithm 1.				
Add_Avg	The average number of additional tests required for Algorithm 1.				
Add_Max	The maximum number of additional tests required for Algorithm 1.				
Tra_Test	The number of tests required for the traditional diagnosis algorithm.				
FR_Test	The number of tests required for the five-round diagnosis algorithm in Ref. [26].				
New Test	The number of tests required for the local diagnosis algorithm in Ref. [34].				

From Ref. [\[21\]](#page-15-3), we have that the number of tests for the traditional diagnosis algorithm is usually *Nt*, where *N* is the total number of nodes in a system, and *t* is the diagnosability of the system. Thus, we can deduce Tra_Test $= m^2 \cdot 2^m$. In addition, Ye and Liang [\[26\]](#page-15-8) proposed a five-round adaptive diagnosis scheme for networks containing the Hamiltonian cycle under the PMC model. Based on Theorem 3.1 in Ref. [\[26\]](#page-15-8), we can infer that FR_Test $= 2N + 2(m-2) = m \cdot 2^{m+1} + 2(m-2)$ if the number of faults does not exceed *m* in *HSDCm*(*m*). Later, Chen et al. [\[34\]](#page-15-15) introduced a local diagnosis algorithm to diagnose a node under the PMC model, which needs at most *m* tests when the number of faults does not surpass the degree of this node. Note that *m* is the degree of a node in networks. If we apply this method to diagnose all servers in $HSDC_m(m)$, then the number of tests required is mN (i.e, New_Test $= m^2 \cdot 2^m$). At that time, we set $p = 0.5$ and $m \in [5, 10]$.

Figure [7](#page-13-1) showcases a noteworthy trend: Max is significantly smaller than FR_Test, Tra_Test, and New_Test in the same scale of networks. This observation emphasizes that our method drastically reduces the number of required tests and improves diagnostic efficiency compared to other diagnosis algorithms. Our approach is advisable when the number of faults in *HSDCm*(*m*) is less than *m*, despite the more permissive fault constraints of the five-round diagnosis algorithm. Moreover, with an increase in the parameter *m*, every diagnosis scheme experiences an augmentation in the number of tests. In particular, when extending the local diagnosis scheme to global diagnosis, the number of tests remains consistent with that of the traditional diagnosis algorithm (i.e., Tra_Test=New_Test). From Figure [8,](#page-13-2) as the scale of the network expands, we observe an ascending trend in the values of Add_Avg, Add_Max, Avg, and Max, where Add_Avg maintains an approximate half of Add_Max. This phenomenon stems from the fact that the number of additional tests is $4m \cdot f_c$ when $f_c \leq m - 2$ (resp. either 0 or $m - 1$ when $f_c \geq m - 1$). In each experiment, f_c is influenced by the real count of faulty nodes f , randomly chosen from the interval

Figure 7. Comparison of Max, FR_Test, Tra_Test, and New_Test in $L\text{-}HSDC_m(m)$ with $m \in [5, 10]$.

Figure 8. Comparison of Add_Avg, Add_Max, Avg, and Max in L -HSDC_{*m*}(*m*) with $m \in [5, 10]$.

6. Conclusions

Inevitable network faults frequently result in packet loss, potential downtime, and service disruption. By implementing efficient fault diagnosis algorithms and localizing faulty nodes, it becomes possible to replace faulty servers quickly, thereby reducing the risk of substantial adverse consequences. In this paper, we first prove that both the connectivity and diagnosability of an *m*-dimensional HSDC network are *m*. Further, an adaptive fault diagnosis algorithm is proposed, which can completely diagnose the actual status of all servers in the HSDC network within at most $m2^m + 4m(m - 2)$ tests if $m \ge 3$ (resp. 9 if $m = 2$). The experimental results affirm that our algorithm significantly decreases the required number of tests, surpassing the efficiency of traditional diagnosis algorithms.

Moreover, our approach offers a promising direction for devising adaptive fault diagnosis schemes in other data center networks, such as BCCC, BCube, and AQDN. It is evident that our strategy has certain limitations, proving effective in identifying the status of all servers only when the number of faulty servers in *HSDCm*(*m*) networks does not exceed *m*. In future research, we will try to further address this shortcoming by increasing the number of test rounds, e.g., five rounds of testing [\[26\]](#page-15-8), or based on other fault diagnosis models, such as the comparison model and the BGM model. Meanwhile, the objective of this paper is to target permanent fault scenarios, and its diagnostic scope is a global system. For this reason, we may explore other types of faults, including link failures, switch failures, intermittent fault scenarios, and local fault diagnosis strategies for DCNs in the future. These areas of study may help to provide a more comprehensive approach to fault diagnosis and improve the overall performance of the system. Finally, before concluding this article, we provide some valuable references related to system diagnosis for further exploration [\[35–](#page-15-16)[37\]](#page-15-17).

Author Contributions: Conceptualization, W.L.; methodology, W.L. and X.-Y.L.; software, X.W. and W.L.; validation, W.L. and J.-M.C.; formal analysis, X.-Y.L. and X.W.; investigation, W.L. and J.-M.C.; resources, X.-Y.L.; data curation, W.L. and X.W.; writing—original draft preparation, W.L.; writing review and editing, J.-M.C.; visualization, X.-Y.L.; supervision, J.-M.C. and X.-Y.L.; funding acquisition, J.-M.C. and X.-Y.L. All authors have read and agreed to the published version of the manuscript.

Funding: This work was supported by the National Natural Science Foundation of China (grant number: 62002062), the National Science and Technology Council of Taiwan (grant number: NSTC-112- 2221-E-141-004), and the Natural Science Foundation of Fujian Province (grant number: 2022J05029).

Data Availability Statement: The data used in the study are available from the authors and can be shared upon reasonable request.

Acknowledgments: The authors would like to thank the anonymous reviewers and the editor for their careful reviews and constructive suggestions to help us improve the quality of this paper.

Conflicts of Interest: The authors declare that they have no conflicts of interest.

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