

Article



Sensor Fusion-Based Pulsed Controller for Low Power Solar-Charged Batteries with Experimental Tests: NiMH Battery as a Case Study

Shyam Yadasu ^{1,2}, Vinay Kumar Awaar ³, Vatsala Rani Jetti ^{1,*} and Mohsen Eskandari ^{4,*}

- ¹ Department of Polymers and Functional Materials, CSIR-Indian Institute of Chemical Technology, Hyderabad 500007, India; yadasushyam@gmail.com
- ² Academy of Scientific and Innovative Research (AcSIR), Ghaziabad 201002, India
- ³ Department of Electrical and Electronics Engineering, Gokaraju Rangaraju Institute of Engineering and Technology, Hyderabad 500090, India; vinaykumar.a@griet.ac.in
- ⁴ The School of Electrical Engineering and Telecommunications, University of New South Wales, Sydney, NSW 2052, Australia
- * Correspondence: vatsala@iict.res.in (V.R.J.); m.eskandari@unsw.edu.au (M.E.)

Abstract: Solar energy is considered the major source of clean and ubiquitous renewable energy available on various scales in electric grids. In addition, solar energy is harnessed in various electronic devices to charge the batteries and power electronic equipment. Due to its ubiquitous nature, the corresponding market for solar-charged small-scale batteries is growing fast. The most important part to make the technology feasible is a portable battery charger and the associated controllers to automate battery charging. The charger should consider the case of charging to be convenient for the user and minimize battery degradation. However, the issue of slow charging and premature battery life loss plagues current industry standards or innovative battery technologies. In this paper, a new pulse charging technique is proposed that obviates battery deterioration and minimizes the overall charging loss. The solar-powered battery charger is prototyped and executed as a practical, versatile, and compact photovoltaic charge controller at cut rates. With the aid of sensor fusion, the charge controller is disconnected and reconnects the battery during battery overcharging and deep discharging conditions using sensors with relays. The laboratory model is tested using a less expensive PV panel, battery, and digital signal processor (DSP) controller. The charging behavior of the solar-powered PWM charge controller is studied compared with that of the constant voltageconstant current (CV-CC) method. The proposed method is pertinent for minimizing energy issues in impoverished places at a reasonable price.

Keywords: PV System; battery charger controller; light-dependent resistance (LDR) sensor; sensor fusion; NiMH battery; origalysis; DSPTMS32028027F

1. Introduction

One of the main issues facing the modern civilized world is the global energy crisis and the threats posed by climate change. The primary identified causes of the aforementioned concern are the depletion of fossil fuel reserves and the release of greenhouse gases. Solar, wind, and tidal energy are types of renewable energy sources (RES) that are the solution to these problems. Solar energy is one of these RES that is considered to have the potential to resolve the crisis because it is abundant and cost-free [1,2]. Due to its emergency qualities and affordability, solar power has emerged as among the world's primary sustainable power sources [3]. Solar energy is transformed into controlled electrical energy using photovoltaic (PV) cells and a power electronics converter. These PV solar cells have nonlinear properties; the reliability of solar PV is dramatically decreased because of the intermittent properties of solar power and environmental circumstances [4]. As a result,



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Copyright: © 2024 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). the solar PV system's maximum power output varies according to changes in irradiance and weather. In different climatic conditions, such as partial shade, the characteristics of solar cells grow more complex [5,6]. These problems make it necessary for researchers to enhance the power output of solar PV cells under fluctuating atmospheric conditions. The writers of [7,8] have thoroughly detailed several optimum power tracking approaches to attain maximum power. A charge controller-equipped battery charger is also essential for maximizing the transmission of electricity from solar PV to the battery bank.

A charger for batteries with a charge controller's primary job is to (1) aid in extracting the maximum available energy from the solar panel and operate the panel at possible maximum voltage and current conditions by adjusting the duty ratio in the charging algorithm. (2) shorten the battery charge duration and support the photovoltaic arrays. Moreover, it prevents the battery from being overcharged and discharged, extending its life. A PV charge controller's algorithm controls the efficiency of PV array usage and battery charging, enhancing the system's capacity to fulfill the demand for electrical load.

Charge controllers in series, shunt, pulse width modulation (PWM), and maximum power point tracker (MPPT) comprise the varieties of charge controllers for solar PV that are employed most frequently [9]. The series controller uses a control component that connects the batteries and a solar panel in a series. Due to the existing limits of shunt controllers, this charge regulator is frequently employed with compact PV systems and can be employed for effective systems. The shunt regulator controls the battery charging using the solar panel by impeding the PV array inward to the regulator. This charge controller is primarily employed to control current or voltage to prevent storage batteries from being overcharged and deeply discharged, which might harm the batteries [9].

1.1. Background and Motivation

Research is focused on PV-powered battery charging systems to create an effective and optimal control mechanism. Most 12-volt panels generate around 16 to 20 volts; thus, if there is no regulation, overcharging will cause harm to the batteries [10]. The primary role of a charge regulator is to sustain the battery at the maximum potential energy state. Charge-controller device preserves the battery from overcharging and detaches the load to obstruct the deep discharge. The ideal scenario is for the charge controller to directly regulate the battery's condition, equivalent to constant potential charging [10]. In the absence of a charge controller, depending on the irradiance, power from the PV module will flow into a battery, whether or if the battery has to be charged. Uncontrolled charging will result in an extremely high battery voltage if the battery is fully charged, which hastens grid corrosion, electrolyte loss, internal heating, and severe gassing [11,12]. The charge controller maintains the battery in good condition and increases its lifespan.

The charge controller adjusts charge rates depending on battery capacity and monitors battery temperature to prevent overheating [12,13]. The charge controller uses the PWM technique to generate pulses to the battery with multiple duty ratios [14]. In essence, it performs the function of an intelligent switch between solar cells and batteries, managing the voltage and current flowing into the batteries. The benefits of PWM charge regulators are as follows: (i) increased battery life, (ii) increased recharge effectiveness, (iii) lessens the battery's stress, (iv) battery overheating is less frequent, and (v) the capacity of a battery to be restored. Here the charge controller acts as a switch that connects the battery with the PV panel through PWM operation. When the switch is closed, the battery and panel will be at almost matching voltage. Additionally, it can power a DC load that is straitly attached to the battery is thoroughly discharged. The benefits of MPPT in this kind of system would be substantially lower. However, there would be no drawbacks to utilizing an MPPT regulator on a solar module with a voltage near the battery potential [15,16].

The PWM protocol algorithm integrated with the solar charge controller improves the amount of current from the PV module into the battery. To precisely match the PV module to the battery, the PWM charge regulator is a DC/DC adapter that accepts DC as Various charging technologies have been developed to improve lithium-ion batteries' condition, life cycle, charging time, and charging efficiency [19,20]. Among various technologies, constant current (CC) and constant voltage (CV) charging methods are widely used [3]. CC-CV charging technology charges with CC below the threshold voltage. The current gradually falls when the threshold voltage is reached, charging to a constant voltage. The magnitude of the CC and CV depends on the battery specifications. The disadvantage of this charging technique is that it reduces the charge current in the CV section to prevent battery damage owing to overvoltage. Therefore, the charging time is extended in this section [20]. In the case of CC-CV charging, the charging time is long because the charging speed is slow. The sluggish charging period of distributed battery cells has been acknowledged as a drawback, and a quick and effective solution is the pulse-based charging approach.

Pulse-based charging offers low impedance, which enhances power transfer by enabling the subsequent charge pulse to enter the battery more effectively and prevents overheating and overvoltage gassing [21,22]. The increased discharge capacity of the pulsecharged battery shows that the pulse technique may effectively utilize the battery's active components without overcharging while still providing a long cycling existence [21,23]. The effect of the pulse method charging on the cycle period of Li-ion batteries was investigated, and it proved that the pulse technique enhanced the lifespan of batteries as to DC imposing techniques [21,24]. Although there has been much research on battery charging, there is still a need to optimize battery charging performance.

1.2. Contributions and Novelties

By taking into account the key benefits of the charge controller, the authors are concerned with building an appropriate charge controller that enhances the charging ability and effectively extends battery existence with optimal charging speed.

The pulse frequency for controlling the battery charge duration will dynamically change within a specific range to inject the maximum charge current into the battery cells. To furnish the battery pack with a long life cycle, we designed a quick method for charging batteries according to the recommended frequency and workload cycle while defending against over-voltage or overheating situations for battery cells. In addition, a comparative analysis has been performed by contrasting the charge regulators suggested by the different authors in the existing compositions.

The contributions of this paper are summarized as follows.

- (1) A literature review on the existing pulse controller for battery chargers is provided, and their performance and shortcomings are analyzed.
- (2) A sensor-fusion-based pulse charging technique is proposed for the battery charger that minimizes charging losses and battery degradations to increase battery lifetime.
- (3) With the aid of sensor fusion, the charge controller is disconnected and reconnects the battery during battery overcharging and deep discharging conditions using sensors with relays.
- (4) A prototype solar-powered battery charger with the proposed technique is developed and tested using a less expensive PV panel, battery, and digital signal processor (DSP) controller.
- (5) The comparative study is presented by comparing the charging performance of the solar-powered PWM charge controller with the constant current-constant voltage

method. The proposed method is pertinent for minimizing energy issues in impoverished places at a reasonable price.

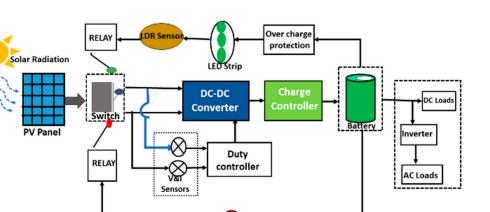
2. Revolution of Pulse Charging (Current) Mode Technique

For the pulse charging protocol, a pulsed current charging technique was previously proposed to improve the cycle life of lead-acid batteries [25–28]. Then, it was extended to the Li-ion batteries [29]. Pulse protocol that consists of charging the battery pack with current pulses. This introduces relaxation periods during the charging process, which permits deep intercalation of the lithium ions inside the electrode. Consequently, the effect of concentration polarization decreased, and the Li-ions diffusion rate was, respectively, increased. The main parameters of this method are the pulse frequency, the duty cycle, and the charging current amplitude. The current pulse and voltage pulse are the two types of pulse modes. The difference between the two pulse modes is mainly reflected in different control methods during the charging process. Generally, the pulsed current is implemented by charging the battery cell using a charging protocol defined in advance, while the voltage pulse mode is implemented by controlling the duty cycle or frequency of the switching device to achieve the required average current. L. R. Chen proposed several voltage pulse charging strategies to reduce the charging time [30,31]. Several studies in the literature use different values of these parameters and report different results. The authors in [32–34] adopt a fixed pulse frequency and a given duty cycle. The result of these studies is higher degradation of the electrodes in the case of the pulse protocol. However, by adapting the pulse frequency, this protocol's effectiveness has been demonstrated in comparison with the constant current constant voltage (CCCV) method in terms of charging time and battery lifetime [14,35–37]. It can also be observed that by modifying the relaxation time and current amplitude during the charging stage, it is possible to minimize the concentration polarization and optimize the charging process [14]. Furthermore, by expanding the relaxation time and reducing the current amplitude at the end of the charging action, it is possible to obtain better results than what would have been with a pulse charging employing constant parameters. Finally, other authors have shown that the charging efficiency can be increased by altering the pulse frequency to minimize the internal battery impedance [14,34].

The primary battery failure mechanism is over currents that buckle the battery electrodes, leading to short-circuiting in the cell, which is not repairable. The proposed charger will dump high-frequency currents within a very short period into the battery. These high-frequency currents are low enough duty cycles to prevent any buckling of the battery electrodes. The time between two pulses allows the battery capacitive reactance to decrease, leading to decreases in the complex impedance of the battery. If battery impedance is lower, the next charge pulse is more effectively observed because of this battery being charged accurately [27,38].

3. The Proposed Pulse Charging Protocol

Figure 1 depicts the structural illustration of the preferred system, in which PV panel output current and voltage are sensed and provided to the microcontroller. The microcontroller is set up to carry out the Pulse width modulation operation to maintain the system operating at the highest power level to extract the total power from the PV source. The DSP controller produces pulses, whose frequency changes to monitor the solar panel's maximum voltage point, and the metal oxide field effect transistor (MOSFET) switch of the DC-to-DC converter receives this pulse. ACS712 module is utilized here as a current sensor, while the LM358 op-amp is used as a voltage sensor, and the DSP TMS32028027F is for regulating. The power generated by the DC converter is given to the solar charge controller. The proposed system comprises three circuits: a front-end DC-DC converter, isolation circuits, an intermediate battery charging circuit, and a sensor and control circuit. AC loads are connected to the battery through inverting output.



ep discharge

protection

Figure 1. Schematic illustration of the proposed charge controller.

DR Sen

The size of the PV panel is 36 cells 12 V/5 W has been used to charge two cells in a series of 1200 mAh ratings for a wide selection of input voltages from 5 to 22 V. The final accepted charging current is limited to 250 mA. For longer battery life, it also has overcharge and reverses polarity protections.

3.1. PV Equivalent Circuit Analysis

Generally, a current source and a reverse diode analogous can represent a solar cell. It has its own internal parallel and series resistance. Parallel resistance is caused by leakage current, while series resistance is caused by obstructions in the movement of electrons from the N to P union. When irradiance strikes its surface, an electrical field is created inside the solar PV cell [39,40]. This development can generate power for an outermost circuit when an electric field is present. The strength of the incident radiation affects the produced current. More electrons can be released from the surface at higher light intensities, which results in a more significant current generation [40–42]. The source current I_{pv} indicates the unit photocurrent and R_{sh} is used to indicate the solar unit's intrinsic shunt and series resistance, respectively. Generally, R_{sh} and R_S have very high and low values; therefore, it is feasible to omit them to simplify the analysis. PV array is simplified by using mathematical Formulation (1)–(4), and PV characteristics are shown in Table 1. An equivalent circuit model of the proposed charge controlled is shown in Figure 2. It is the composition of advanced protection circuits using sensors, and TIP42C is used to discharge the high currents to the grounds. A current source connected simultaneously to a diode simulates the ideal PV cell. The resistances in series and shunts are added to the model, as shown in Figure 3 because no solar cell is perfect.

Here photo Current
$$I_{Ph} = [I_{scr} + K_i(T - 298)] \times \frac{\tau}{1000}$$
 (1)

PV Module Reverse Saturation Current
$$I_{rs} = \frac{I_{scr}}{\exp(\frac{qVoc}{NskAf}) - 1}$$
 (2)

Module Saturation Current
$$I_S = I_{rs} \left[\frac{T}{T_r} \right]^3 \exp^{\left[(q * \frac{E_{go}}{Bk}) (\frac{1}{T_r} - \frac{1}{T}) \right]}$$
 (3)

PV module Output Current is
$$I_{pv} = I_{ph} \times N_p - N_p \times I_o \left[e^{\{q * \frac{V_{pv} + I_{pv}R_s}{N_sAkT}\} - 1} \right]$$
 (4)

Here, I_0 is the PV module's current.

PV module voltage is V_o .

The reference temperature in Kelvin is T_{rk} .

 T_{ak} is the operating temperature in Kelvin.

The short-circuit and current temperature co-efficient (K_i) at I_{scr} is defined as I_{scr} , the PV module short-circuit current at 25 °C, and 1000 W/m² = 2.55 A.

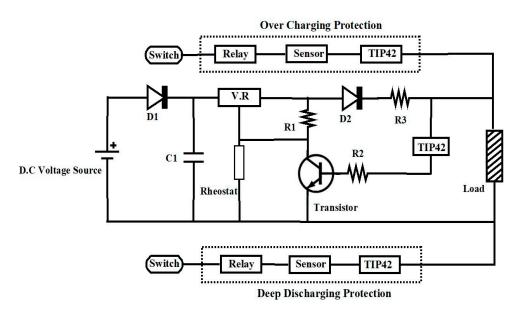


Figure 2. Equivalent circuit diagram.

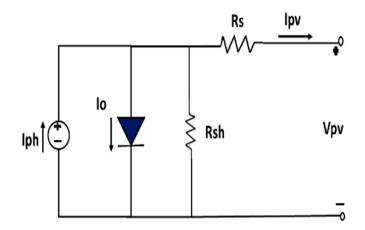


Figure 3. The PV cell model.

Table 1. Electrical Characteristics of Data of JIGHISOL System Pvt Limited: 12V 5W, Model No:0117003479.

PV Module Electrical Parameters and Ratings: 5 W/12 V				
Maximum Power (MP _{max})	5 W/12 V			
Potential at Ultimate. Power (V _{mp})	17.40 V			
Current at Optimum Power (I _{mp})	0.26 A			
The voltage at Open Circuit (V_{oc})	21.50 V			
Panel current at short circuit (I _{sc})	0.32 A			
Tolerance	+5%			
Specifications are at STC 1000 W/m Irradiance AM 1.5, Cell Temp 25 $^{\circ}$ C				

3.2. DC-DC Converter and Power Stage Design

By adjusting the duty period (*D*) of the converter, the switching regulator is used to match the maximum power and voltages (VMPP) at all climatic circumstances with the load voltage (V_{PV}) seen by the PV panel. The load is connected to the module by a DC-to-DC converter through a charge controller that facilitates the delivery of the PV module's maximum possible power to the load. This chopper circuit is controlled with a pulse width modulation technique using a DSP TMS32028027F [43]. The equivalent circuit model of the chopper converter is shown in Figure 4.

Here,
$$V_O = \frac{V_{in}}{1-D}$$
 range of *D* must be in the limit of $0 \le D < 1$. (5)

Output current $I_O = (1 - \% Duty)I_i$ (6)

Duty ratio
$$D = \frac{T_{on}}{T_s}$$
, where $T_s = T_{ON} + T_{OFF}$ (7)

$$T_{ON} = T_s * D$$
, where *D* is the ratio of the duty cycle (8)

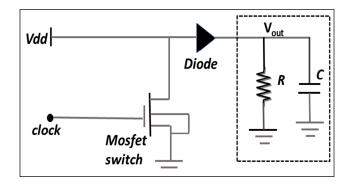


Figure 4. Illustration of DC-DC converter equivalent circuit.

The control circuit's time ratio is the proportion of a switch's on-pulse to the duration of one full course.

OFF Duty period
$$(1 - D) = T_{OFF}/T$$
, Switch OFF period $T_{OFF} = ((1 - D)/f)$ (9)

Here, I_i denotes the input current, and I_o denotes the output current. T_{ON} stands for turn-on time, T_{OFF} for turn-off time, and T_s for total switching time.

In this article, the converter system comprises the power stage, the compensator, and the pulse width modulator (PWM); the converter is assumed to be in continuous conduction mode (CCM) operation.

From the equivalent circuit of a DC-to-DC converter

$$V_{i}(t) = \frac{d_{i}(t)}{dt} + \frac{1}{C} \int i_{1}(t) dt$$
(10)

$$V_o(t) = Ri_2(t) = \frac{1}{C} \int i_1(t) dt$$
 (11)

$$V_i(S) = V_o(S)[1 + S^2C + S\frac{L}{R}]$$
(12)

$$\frac{V_i(S)}{V_o(S)} = \frac{1}{[1 + S^2 C + S\frac{L}{R}]}$$
(13)

The gain of the system at S = 0 is 1.

$$\frac{V_O}{V_C} = \frac{V_S}{V_P} \times \frac{\left[1 + \frac{S}{\omega_Z}\right]}{\left[+\frac{S}{O\omega_O} + \frac{S^2}{\omega_O}\right]}$$
(14)

where $\omega = \frac{1}{\sqrt{C}}$, $Q = \frac{R}{\omega_0}$, and $\omega_Z = \frac{1}{RC}$.

The power stage has the following parameters are shown in Table 2. $V_S = 16.5$ V, $I_S = 65$ mA, $V_o = 14.3$ V, $I_o = 50$ mA, Resistors = 1.2 Ω , 100 Ω , the peak value of PWM is Vp = 5 V DC, Switching Frequency = 25 kHz. The compensator network consists of R_1 , R_2 , R_3 , R_{bias} , C_1 , C_2 , C_3 and V_{ref} . We assumed that the Value of V_{ref} is 2.5 V R_{bias} is 10 K Ω the rest are design parameters.

Table 2. Solar charge controller circuit design parameters.

Circuit Parameters for a Prototype Solar Charge Controller		
Steady Input voltage	16.5 V	
Input current	60 mA	
Output voltage	14.3 V	
Output stable current	50 mA	
Resistors	100, 180, 470, 1 k ohms	
Diode	IN4007	
Rheostats	1 k, 2 k, 5 k ohms	
Switching Frequency	25 KHz	
LDR	6	
Relays	1	
Transistors	BC 541, TL431, TIP42C	

3.3. Control System Based on PID

The main objective of the proportional integral derivative (PID) controller is tuning the converter circuit to track the total amount of energy from the PV source, considering the potential and current of the PV array. The PID controller receives the error waveform produced by comparing the quoted potential with the output potential of the switching regulator. The switch's gate pulse is activated using the PID controller's output to increase the solar cell's power. Figures 5 and 6 illustrate the functional block diagram and the PID controller's construction of the proposed method, which may be written as follows.

$$u(t) = K_p e(t) + K_i \int_0^t e(t) dt + K_d \frac{d}{dt} e(t)$$
(15)

where e(t) denotes error to the controller input, u(t) denotes regulator output, K_p denotes potential gain, K_i denotes Integral gain, and K_d is the derivative gain.

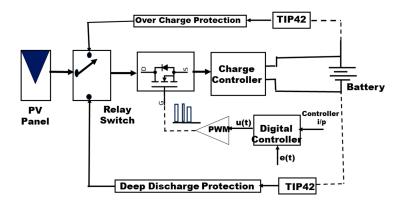


Figure 5. Illustration of the proposed method functional blocks.

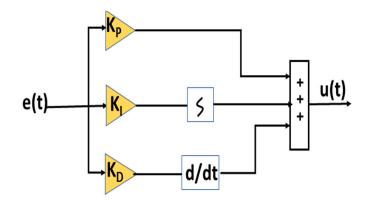


Figure 6. PID controller block diagram.

3.4. Solar Charge Regulator

A charge monitor, further known as a charge regulator, controls the current and voltage to prevent batteries from overcharging. It controls the solar panels' voltage and current as they feed the battery [35]. Shunt and series regulation are the two fundamental techniques for managing or regulating battery charging [10,45]. Although both techniques are helpful, each may include several variants that change its fundamental functionality and usefulness. The controller is a series type when the MOSFET IC (integrated circuit) component is linked sequentially with the PV layout and the battery. It is referred to as Shunt form when combined in parallel over the PV array or the battery. When the battery is fully charged, the MOSFET switch is kept open in the series type. During this time, the PV Array stops delivering current. When the battery is completely charged, the MOSFET IC is kept shut in the shunt type to shunt (switch) the entire PV array short circuit current away from the battery [45].

3.5. Sensor Fusion Process

Implementing sensors in the proposed solar charge controller is crucial to achieving the system's desired functionality. In this work, we used two voltage detection sensors module 25 V, one current sensor (ACS712 5A), and two LDR [46] sensors. The current sensor is used to monitor the battery charging (60 mA) and discharging currents (50 mA) during the battery cycling period. Among the two voltage sensors, one is used to sense the battery maximum cut of voltage ($V_{max} = 3$ V), and the second is used to measure the ($V_{min} = 1.4$ V). Once the battery obtains full charge voltage sensor is activated and sends a signal to TIP42 IC through which the LED becomes powered ON, and LDR senses the light and provides a signal to the relay coil; the relay will break the supply to open the switch. In a similar way second voltage sensor is operated and makes the connection when the battery reaches V_{min} level, i.e., 14 V. With this sensor fusion process, the battery can be protected from overcharge and deep discharge.

4. Assessment of the Experiment and Test Results

To comprehend and research how well the proposed charge controller, nickel metal hydride (NiMH) batteries employed for charge–discharge investigations were the type of battery used in this research. The tested battery cells' specifications are 1.2 V, 600 mAh, an anode composed of hydrogen-absorbing alloys (MH-metal hydride), a cathode made of nickel hydroxide (Ni (OH)2), and an electrolyte made of caustic potash (KOH) comprised the NiMH batteries [47]. The test was conducted over a while and examined under both charging methods. These batteries are all fully charged using constant amperage and constant voltage [48] by utilizing a charge controller board. The PV panel, filter capacitor, and voltage regulator make up the charger circuit, LDR sensors, and relay coils, as depicted in Figure 7. The solar panel is the primary power source linked to the charge controller circuit by a DC-to-DC converter.

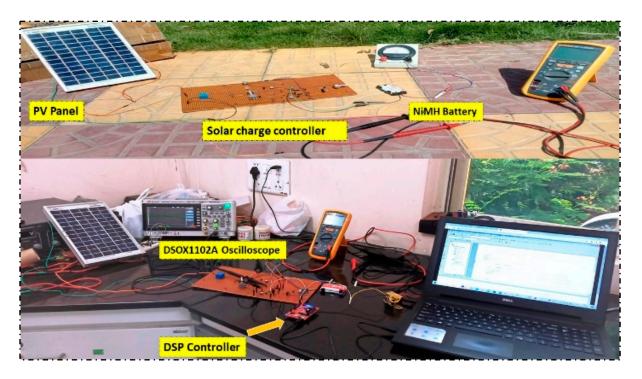


Figure 7. Experimental setup for determining the impact of charge algorithms on battery.

The optocoupler (Photocoupler 817), a crucial component of the power regulation circuit and a source of 5 V input for the DSP TMS32028027F, receives a 12 V supply from the 12 V regulator. The voltage controller IC, which continues the voltage stability and provides a continual DC potential, is connected to the capacitor's power output. The 78xx category includes the voltage regulators that are utilized. The regulator code number might change conditional on the utilization and needed potentiality. For instance, the regulator utilized is 7812 if the controller supply voltage requirement is 12 V. The power circuit is made up of the specified components put together. According to Figure 7, the power control circuit consists of an Opto-isolator (PC817), a DSP TMS32028027F, and an isolation circuit.

In a short circuit over the semiconductor switch, the optocouplers serve as safety circuits to the PWM controller and power circuit. The optocoupler's collector is connected to a 12 V regulated DC. The gate terminal of the semiconductor switch and the photocoupler emitter pins are interconnected. These can be employed as shifting resistors and have specified values of 48 Ohms and 1.5 k ohms in the circuit. The digital signal processor (DSP) controller manages the timing of the gate signals. The triggering pulse for the power switch is provided by a selected output pin grounded to the earth plane. The proposed controller circuit is forged and proven with a NiMH battery group of cells. By stepping down the battery potential to the requisite voltage proportion and rectification, this charging circuit is also used for electrical circuits for AC. Then, it is immediately connected to the charging circuit. We must employ an amp meter and potentiometer to adjust stable voltage and current during this charging procedure and keep track of voltage and current fluctuations. The charger circuit receives power directly from the solar panel, and the duty proportion, which is configured in the Code Composer Studio software 8.1, controls the output voltage [49]; here, NiMH battery packs P and Q are charged using a 60 mA steady current approach, then drained using origalysis electrochemical workstation with 50 mA loads shown in Figure 8. Table 3 displays the prototype outcome (charge; discharge) and battery cell capability for pairing pulse and traditional methods. Currently, the TL431 is used as a variable selector; it operates critically combined to divert the over currents to the common ground and guard against the continually charged battery. Figures 9 and 10 indicate input voltage firing pulses to the semiconductor switch.

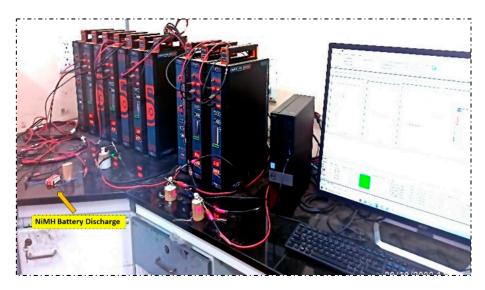


Figure 8. Origalysis electrochemical workstation.

Table 3. NiMH Battery charge–discharge study with 60 mA CC and 50 mA loads.

Battery Specifications	Traditional Mode Charging	Pulse Charging
Total charging period (h)	7:30	7:30
Total discharging period (h)	10:06	13:45
Battery Capacity (mAh)	498	678
Battery Cell Voltage (V)	2.7	2.9
Battery Cycle Life (cycles)	270	339

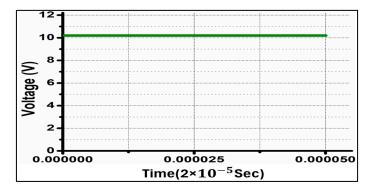


Figure 9. Input Voltage.

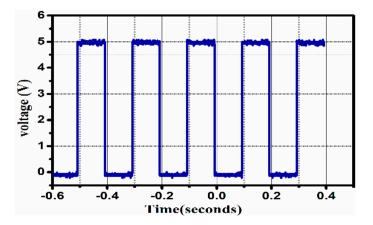


Figure 10. Standard protocol with an equal duty cycle.

4.1. Traditional Charging Strategy

First, a common technique for charging energy storage devices is constant current constant voltage (CCCV). With the constant current mode, as V_{oc} rises, the terminal voltage gradually rises until it exceeds the voltage limit. The charger then changes to constant-voltage mode to fix the terminal voltage and cut down the amplitude of the current. This is due to the internal potential, V_{oc} , rising when energy is charged, and it is advised for battery safety during charging. The charger must make sure the charging current is less than the highest charging current the battery can tolerate. Due to this technique, chargers disperse energy inequitably, unbalance energy distribution, and experience a higher capacity decrease. The battery charger switches to constant voltage (CV) mode from constant current (CC) mode, where the moving charge rapidly drops [50,51]. These techniques have been widely used for inverters [52,53]. The result of the current and terminal force is the injected power. As a result, changing the charging strategy to accommodate a continuous power supply is essential because the increase in injected power in the CC mode is noticeable.

4.2. Proposed Efficient Pulse Charging Protocol

Pulse magnitude modulation (PMM) and pulse width modulation (PWM) are two alternative ways that the pulse approach can be used [54,55]. The pulsed current's duration is changeable, while its amplitude is constant in the PWM mode, as depicted in Figure 11. The proposed pulse charging approach is the constant current with the relaxation time, as illustrated in Figures 10 and 11. In contrast, in the PMM mode, the length of the pulsed current is constant, but the pulsed current amplitude varies. The high-frequency current pulse's amplitude is constant, and during the relaxation period, the current is zero. The following formula calculates the current pulse's frequency: *T* being the pulse period, f = 1/T. Ton is the current pulse's duration, and T_{off} is the relaxation time. Equation no. 20 can be used to calculate the duty cycle of the given pulse. According to the results of the experiments, pulse mode charging can increase battery capacity and extend battery life by increasing the number of cycles when compared with CC-CV charging. The optimal conditions for pulse mode charging are f_{Zmin} (around 25 kHz), 50% duty cycle, and 28 °C ambient temperature. Moreover, the charging speed can be improved by 24% compared with the CC-CV charging.

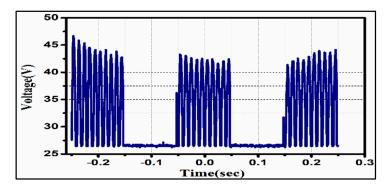


Figure 11. Output high-frequency Pulse Current.

5. Comparative Evaluation and Results with Discussions

In this part, a comparison between the CC-CV and the fixed pulse was made. In both methods, the total charging duration is 7:30 hours maximum, based on solar sunlight availability daily, as shown in Figures 12a and 13a. With a total discharge capacity of 498 mAh, the CC-CV technique uses 60 mA constant current charging that lasts for 10:06 h, as shown in Figure 12a–c. The proposed charging method fed to the battery with the same C-rate as the CC-CV method and lasted 13:45 h with a discharging capacity of 678 mAh, as shown in Figure 13a–c. This demonstrates that continuous current in the CC stage keeps the charging process and capacity gains from being sped up. Meanwhile, increasing the parameters of battery impedance and temperature as shown in the thermal

and impedance analysis section. The proposed pulse charging current, with optimum performance characteristics, is manifested in Table 2. The research findings demonstrate that the suggested charge technique accomplishes rapid charging at operational temperature. This is particularly appropriate for the charge of PV-powered batteries and EVs or PHEVs from 15% state of charge (SOC) to 82% SOC levels [56].

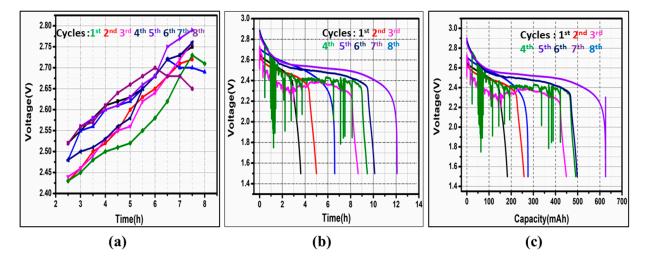


Figure 12. Charge–discharge studies of NiMH batteries using conventional methods with respect to voltages: (**a**) Total charge time; (**b**) Total discharge time; (**c**) Total discharge capacity.

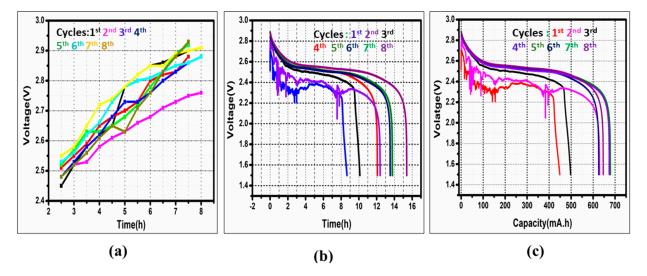


Figure 13. Charge–discharge studies of NiMH batteries using pulse methods with respect to voltages: (a) Total charge time; (b) Total discharge time; (c) Total discharge capacity.

5.1. Impedance Study

Electrochemical impedance spectroscopy is a method used to measure the complex impedance of an electrochemical cell. Different impedance components can be identified by applying a small AC excitation signal of various frequencies and with an amplitude typically of about 5–10 mV. The fundamental theory of EIS is the linear system theory [57], which enables simplifications and easier analysis of the measurement results. For the linear system theory to apply, the battery response must be linear, stable, and casual, and the impedance must be finite [57]. The battery is a nonlinear device; however, the battery can be assumed as a linear system with a sufficiently small current and voltage range. By utilizing this characteristic, the battery impedance can be measured by perturbing the battery with a small-step current at a specific frequency (f) and measuring the related voltage response, as given by:

$$Z_{bat}(f) = \frac{V_{peak}(f)}{I_{peak}(f)} e^{j\theta_z}$$
(16)

where V_{peak} (f) is the peak amplitude of the battery voltage, I_{peak} (f) is the peak amplitude of the battery current, and θz is the phase between the battery voltage and current. The equivalent circuit model [58] is a powerful approach to the numerical analysis of battery behavior, which describes the characteristics of the battery as capacitive and resistant. This model consists of an ideal voltage source, a resistor–capacitor block (R_1 , C_1), and an internal resistance R_0 . These fundamental parameters describing the status of the battery cells depend on the cell's dynamically changed temperature and SOC [59]. The capacitive characteristic of the battery cell causes different responses at different frequency pulses, which are activated when the battery is charging. Figure 14 shows that the battery impedance is the summation of Z_0 and Z_1 , as described as follows:

$$Z = \frac{R_{1j\omega C1}^{1}}{R_{1} + \frac{1}{j\omega C1}}$$
(17)

$$Z = Z_0 + Z_1 = R_0 + \frac{R_{1j\omega C1}^1}{R_1 + j_{j\omega C1}^1}$$
(18)

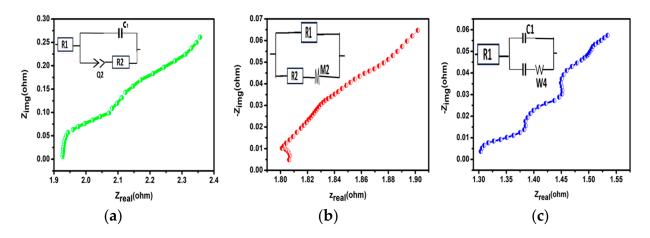


Figure 14. A Nyquist graph of an EIS study with NiMH battery: (**a**) Battery impedance before charging; (**b**) Impedance with conventional charging; (**c**) Impedance with pulse charging.

Here, the impedance study was conducted by using dynamic electrochemical spectroscopy (EIS), and we measured the overall impedance by applying an AC of 5 mA root mean square (RMS) at a frequency range of 20 kHz to 100 mHz, using an electrochemical measurement unit (origamaster-OGFEIS). These output data can be mapped in different ways. The most popular methods are Nyquist plots, which show a comparison of the imaginary and actual impedances (Z_{imag} and Z_{real}).

Figure 14a–c shows the impedance studies of NimH batteries with different charging protocols and the subsequent Nyquist plot of an EIS. The overall impedance before charging was reported as 1.93 Ω , as shown in plot Figure 14a, and impedance with conventional charging, i.e., 1.8 Ω , as shown in plot Figure 14b, impedance reported with pulse charging, i.e., 1.3 Ω , see Figure 14c. The electrochemical cell's charge transfer resistance (R_{ct}) rises due to the decreased electron transport. The Nyquist plot demonstrates the growth in R_{ct} with an increment in the boundary of the curve's semicircle.

5.2. Thermal Study

The maximum rising temperature is related to the safe operation and lifetime of the batteries. Therefore, the maximum rising temperature during the charging process is one

of the important indicators for evaluating charging methods. The rising temperature of the battery cell mainly results from the over potential heat Q_P [60], which is determined as:

$$Q_{\rm P} = I^2 {}_{\rm rms} R \tag{19}$$

where R is the resistance of the battery cell, and I_{rms} is the RMS value of the current mode. The maximum rising temperature ΔT_{max} of the cell during the charging process can be obtained as follows:

$$\Delta T_{\max} = T_{\max} - T_{\text{init}} \tag{20}$$

where T_{init} and T_{max} are the initial and maximum temperatures of the battery cell, respectively, for the CC mode, the RMS value of the current is the same as the constant current amplitude:

$$I_{rms}C.C = I.CC \tag{21}$$

The RMS value of the pulse mode can be determined as follows:

$$I_{rms}Pulse = \sqrt{D_p A_p^2}$$
⁽²²⁾

It can be observed that the rising temperature increases with the increase in the value of I_{rms} . Two variables that impact the cell temperature are the I_{rms} of the current mode and the resistance R of the battery cell. Two variables that impact the cell temperature are the I_{rms} of the current mode and the resistance R of the battery cell [38]. The value of I_{rms} is set to be constant according to Equations (19)–(22). Therefore, the battery resistance varies with frequency and results in the different rising temperatures of the battery cell. To determine the resistance of the battery cell at different frequencies, the EIS test was performed, where the real part of the impedance can be regarded as the resistance of the battery to analyze the heat generation. The measured battery AC-impedance was between 20 kHz and 100 mHz. The real impedance Z_{real} (f) of the battery cell increases as the frequency decreases in the predefined frequency range. Therefore, a lower frequency will result in a higher maximum rising temperature within the considered frequency range.

To examine the battery's thermal behavior [61] during charging and discharge cycles, we use an 80BK-A integrated digital multi-meter (DMM) temperature probe to monitor the surface body temperature in both charging methods. Figure 15a shows the experimental comparison of the two methods. The thermal study was conducted at room temperature, i.e., t_r 26 °C, and the temperature reported in both the charging methods is $t_p = 29$ °C, tc = 32.5 °C as depicted in Figure 15b.

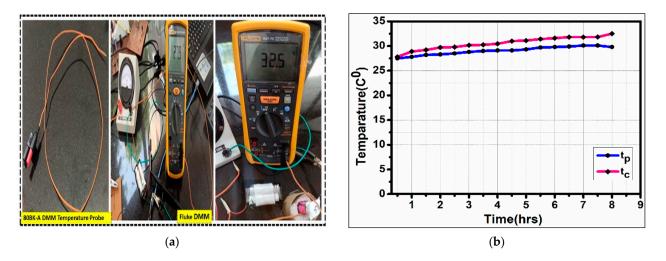


Figure 15. Thermal analysis of NiMH battery: (**a**) Temperature measurements using sensor probe; (**b**) Temperature variations in CCCV and Pulse charging methods (t_c & t_p).

5.3. Theoretical Analysis and Numerical Results

The complex impedance of the battery is expressed as

$$Z = R + jX$$

where R denotes the resistance in ohms, and X denotes the reactance in ohms

Here
$$X = X_L + X_C$$

Inductive reactance, $X_L = j\omega L$ Capacitive reactance, $X_C = 1/j\omega C$ Battery life = $\frac{\text{Battery Capacity (mAh)}}{\text{Load Current (mA)}}$ Rate of Discharge $C = \frac{\text{Load Current (mA)}}{\text{Battery Capacity (mAh)}}$ Charge time $(T) = \frac{C (mAh)}{I (mA)}$ ery Life (in cycles) = $\frac{(\text{Capacity} \times 100)}{(\text{Discharge I})}$

Battery Life (in cycles) =
$$\frac{1}{(\text{Discharge rate } \times \text{Depth of discharge})}$$

Numerical results of the conventional method: DOD of commercial NiMH batteries is 4% [62].

Discharge rate =
$$\frac{\text{Capacity}}{\text{Discharge time}}$$

where

Discharge rate
$$= \frac{0.498}{10.06} = 0.046A = 46mA$$
,
Battery.life $= \frac{0.498 \times 100}{0.046 \times 4} = 270.65$ cycles
Rate of Discharge $= \frac{\text{Load current (mA or A)}}{\text{Battery Capacity (mAh or A)}}$
Rate of discharge $= \frac{50}{498} = 0.1 \text{ C}$

Charging Speed
$$(\alpha_c)\% = \frac{\text{Capacity (mAh)}}{T_{\text{charge}}} \times 100 = \frac{498}{7.5} = 66.4\%$$

Numerical results for pulse method verifications DOD of commercial NiMH batteries is 4% [62].

$$\begin{array}{l} \text{Discharge rate} = \frac{0.678}{13.45} = 0.050 \text{ A} = 50.0 \text{ mA} \\\\ \text{Battery.life} = \frac{0.678 \times 100}{0.050 \times 4} = 339 \text{ cycles} \\\\ \text{Rate of discharge} = \frac{50}{678} = 0.07 \text{ C} \\\\ \text{Charging Seed } (\alpha_p)\% = \frac{\text{Capacity (mAh)}}{T_{\text{charge}}} \times 100 = \frac{678}{7.5} = 90.4\% \end{array} \tag{23}$$

Percentage of Charging Speed Improvement (%) = 90.4% - 66.4% = 24%

$$\Delta \text{Capacitymprovement} (\%) = \frac{\text{Capacity}_{\text{Pulse}} - \text{Capacity}_{\text{CON}}}{\text{Capacity}_{\text{CON}}} \times 100$$
(24)

$$\equiv \frac{678 - 498}{498} = 36.14\%$$

 Δ Capacity Improvement (%) = 36.14%

Numerical results of 50% Duty Effect and CC-CV Method

$$V_{\rm in} = 12$$
 V, $I_{\rm i} = 60$ mA,

$$V_{\rm o} = 12$$
 V, $I_{\rm o} = 60$ mA

Power delivered to the load (battery) $P_o = V_{o \times}I_o = 12 \text{ V} \times 60 \text{ mA} = 12 \text{ v} \times 0.06 \text{ A}$

 $P_0 = 0.72$ watts

...

Numerical results of 50% Duty Effect and Pulsed Method **.** .

$$V_{in} = 12 \text{ V}, I_i = 60 \text{ mA},$$

$$V_o = V_i / (1 - D) = 12 / (1 - 50\% \text{ of } D) = 12 / 0.5 = 24 \text{ V}$$

$$V_o = 24 \text{ V}.$$

$$I_o = (1 - D) \times I_i = (1 - 50\% \text{ of } D) \times 60 \text{ mA} = 0.5 \times 60 \text{ mA}$$

$$I_o = 0.03 \text{ A}$$

Power delivered to the battery (P_o) = $V_o \times I_o$ = 24 V \times 0.03 A

 $P_0 = 0.72$ watts

The mathematical verification shows that, for instance, the average power delivered to the battery in the CCCV and pulse mode is 0.72 watts. Due to the low impedance offered by the battery during the rest period, the next charge pulse is effectively observed, which leads to a cut down in the total charging time. Without overheating, the battery is charged optimally. At the same time, the power delivered to the battery with a 30% duty is quite low, and with a duty of 70% battery temperature is raised, as we observed.

5.4. Discussions

The purpose of this work is to provide an overview of the methods that can be used for generating current pulses, regardless of whether they provide charging benefits or not. The duty cycle can considerably impact the battery capacity. For instance, the pulse mode with a duty cycle of 50% can increase the capacity by 36.14% compared with the CC mode at the same current amplitude level. With an increase in duty cycles, the effects of the pulse mode on the capacity decreases. When the duty cycle is higher than 70%, the obtained capacity is lower than that of CC mode, as we observed in this research, and independent of the current amplitude. The impact factors chosen were frequency (0.1 kHz, 1 kHz, 10 kHz, 25 kHz (f_{Zmin}), 50 kHz), duty cycle (30%, 50%, and 70%), and ambient temperature (0 °C, 26 °C, and 32 °C), where f_{Zmin} is the frequency at the minimum impedance point of the battery cell. It was observed that the cycle life at higher frequencies (f_{Zmin} , 50 kHz) was higher than the cycle life obtained at lower frequencies (0.1 kHz, 1 kHz, and 10 kHz), and the best result was obtained at f_{Zmin}. However, in general, the impedance value of the battery operated in the Pulse strategy was slightly higher than the impedance value obtained by the traditional CC-CV strategy. The optimal set of parameters for the Pulse strategy was the pulsed current with $f_{Z_{min}}$ frequency (25 kHz) and 50% duty cycle at 28 °C, which could improve the cycle life by 69 cycles compared with the CC-CV strategy as mentioned in Table 4. In this pulse protocol, the charging pulse amplitude is maintained at a 60 mA positive constant current discharging with a 50 mA amplitude.

S. No	Parameters	CCCV Mode Charging	Pulse Mode Charging
1	Start voltage (V)	2.4	2.4
2	End voltage (V)	2.7	2.9
3	Start temperature(0 °C)	26 °C	26 °C
4	End temperature(0 °C)	32.5 °C	29 °C
5	Total charge time (h)	7:30	7:30
6	Total discharge time (h)	10:06	13:45
7	Charging Speed improvements (%)	66.4	90.4, i.e., (24%)
8	Total discharge capacity (mAh)	498	678
9	Total Capacity improvement (%)	498	678 i.e., (36.14%)
10	Cycle life improvements	270	339, i.e., (+69 cycles)
11	Temperature Rise (ΔT_{MAX})	6.5 °C	3 °C
12	Impedance Rise	1.8 Ω	1.3 Ω

Table 4. Comparison of evaluation results.

Future works may consider utilizing deep (machine) learning techniques to improve the performance of the controller [63,64].

6. Conclusions

In this article, the performance and analysis of the solar charge controller with the proposed charging protocol were studied; the system is tested with two charging protocols: the conventional CC-CV method and the proposed pulse protocol. Using two charging methods encapsulates the impact of the pulse charging parameters, i.e., duty ratio, pulse frequency, charge–discharge time, battery cycle life, and the charge and discharge efficiencies of the battery. This work has demonstrated that selecting the optimum pulse charging parameters can prolong the cycle life of the NiMH battery, reduce charge time, and improve the battery charge and discharge capacities, thereby increasing battery runtime. The solar charge controller circuit's lab model is put into practice. NiMH battery cells are used as the load to evaluate the charge controller circuit. The experimental outcomes reported in this study, the NiMH cell's charge and discharge potential are substantially increased with pulse charging compared with the traditional charging technique; simultaneously, it delivers a more extended discharge period. The results of the experiments indicate that the suggested approach may end up at nearly 70% of its final capacity in 7:30 h and 13:45 h of discharge time with a battery temperature decrease of roughly 3 °C. This technique will provide low impedance during charging, and the battery cycle life will be improved by 339 cycles. Investigations into the effects of different combinations of these factors revealed that a 50% duty cycle was essential for extending cycle life and guaranteeing faster charge times.

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