



Proceeding Paper Some Studies on the Mechanism of Chip Formation during Machining of Chromium Manganese Austenitic Stainless Steel ⁺

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Abstract: The mechanism of chip formation during machining depends on the type of (i) work material, (ii) tool material, (iii) machining parameters, (iv) tool geometry, etc. It is difficult to understand the mechanism of chip formation without performing experimentation. The present effort has been made to know the mechanism of chip formation during the machining of chromium-manganese austenitic stainless (AST) steel. The chip formation mechanism study for the present steel has been conducted by performing (i) mechanical testing for the work material, (ii) chip type study, (iii) optical microstructural study of the chip surface, and (iv) SEM observation for the chip surfaces. The present chromium-manganese stainless steel (austenitic) was dry-turned in a lathe. Three different experiments were conducted. The input process parameters were v (speed), f (feed), and DOC (depth of cut). The chip reduction coefficient (CRC) and the von Mises stress (VMS) were the output response parameters. The chip thicknesses were measured. The steel specimen was tested on the universal tensile testing machine. The chip reduction coefficients (CRCs) and the von Mises stress were experimentally determined. The undersides of the chips were examined under a light microscope. The chip's top and bottom surfaces were viewed by scanning electron microscopy (SEM). The strain-induced martensite (SIM) formed at the chip surface (under) during the machining at the lower experimental parameters. The surface (underside) of the chip was influenced by the dynamic recrystallization (DRX) and the dynamic precipitation (DP) during the machining at the higher experimental parameters.

Keywords: VMS; machining; dynamic recrystallization

1. Introduction

The machinability of austenitic stainless steel is poor. However, its corrosion resistance is better. This steel cannot be hardened by heat treatment because of its austenite retention ability. The austenite matrix is an FCC crystal structure. Strain hardening and solid solution hardening can strengthen this steel. Austenitic stainless steel is used for corrosion resistance applications. The weight percentages of chromium and manganese are higher in 202 austenitic stainless steel. The nickel content is low. Some nitrogen addition is also made in the present steel. The strain-induced martensite (SIM) forms during the deformation of the 202 steel (AST). The SIM formation causes hardening in the steel. The austenite phase is also strain-hardened due to a dislocation interaction during the deformation. The stacking fault energy is reduced due to the lower nickel content in the 202 austenitic stainless steel. The strain hardening is higher due to the lower stacking fault energy. A coated tool was used for turning (wet) 316L steel. The optimization method was applied to select the proper parameters. The surface finish and material removal rate were improved by setting high-level input parameters [1]. The surface quality of 202 steel (AST) was studied by using PVD- and CVD-coated carbide inserts. The machining was conducted by using a CNC lathe. The effects of the coating and



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Copyright: © 2024 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). process parameters were investigated. The influences of the process parameters were analyzed by using the Taguchi design plan. The optimal parameters were also obtained. The CVD-coated tool was useful due to the better surface quality [2]. A carbide tool was used to machine the stainless steel at high speed. The cutting force and the surface finish were optimized by using the Taguchi methodology [3]. The cutting tool wear during machining (austenitic stainless steel) was studied by cooling the tool internally. The tool temperature and wear increased with the higher heat generation and higher machining forces. The lesser tool wear was due to the internal and external cooling together [4]. The sticky behavior of the austenitic stainless steel was observed during the machining. MQL can be preferred during machining to address this difficulty. The use of the coated carbide tool was favorable [5]. The austenitic stainless steel was machined to analyze various machining responses. Different lubrication techniques were also applied. Different machining parameters were selected for the machining. Environmental and health problems can be reduced due to the use of MQL [6]. The PVD AITIN and the CVD TICN + Al₂O₃ coatings were applied separately on two different carbide substrates. The tool performance was assessed for the cutting of the austenitic stainless steel by using these coated tool inserts. The better selection of the coating and substrate properties was found to improve the tool's life [7]. The precipitation hardening semi-AST ($Cr_{12}Mn_5Ni_4Mo_3Al$) was dry-turned by using the two different carbide tools. The machining responses were the tool wear, the cutting force, and the tool temperature. Elemental diffusion was observed from the tool face to the chip and from the chip to the tool. The carbide tool was worn away by the abrasive, adhesive, and diffusion wear mechanisms [8]. The machining parameter optimization was performed to minimize the surface roughness and power consumption for the turning of the AISI 1045 steel. Better machining results were obtained. The machining environment should be dry [9]. The high-speed machining of the austenitic stainless steel (304 grades) was performed. The AlCrN/TiAlN coating was applied to the tool insert using a cathodic arc evaporation technique. During the dry turning, the coating performed better in terms of temperature resistance. The coating reduced the cutting forces [10]. A milling machine was used to machine the 303 austenitic stainless steel. The cutting parameters were optimized to obtain the minimum surface roughness and energy consumption. Dry machining was discovered to be superior in terms of the surface finish and cutting forces [11]. The high-speed finishing turning of the austenitic stainless steel (304 grades) was performed. The AlTiN PVD coatings were applied to the carbide tool inserts for different Ai/Ti atomic ratios. The machining was performed for different cooling conditions (dry and wet). All the coatings exhibited a better tool life compared to dry machining [12]. The stainless steel (super-duplex) was machined on the CNC lathe. The machining was performed with a PVD-coated tool insert. A high-pressure cooling environment was used. The result was better [13]. The experiments were performed to find out the cutting forces, temperature, and residual stresses of the machined surface during the machining of the 316L steel (AST). The Johnson–Cook (JC) equation was applied in a numerical model to explain the behavior of the 316L austenitic stainless steel. All the machining responses were very sensitive to the JC model constants [14]. The cutting forces were analyzed during the drilling of the heat-resistant austenitic stainless steel. The work hardening behavior of the material was studied. The drilling with the quick-stop device was referred to in [15]. The present aim is to establish the chip formation mechanism during the turning of chromium–manganese austenitic stainless steel. The correlation of von Mises stresses with the experimental parameters and the study of the chip formation mechanism of a newer grade of Cr-Mn steel (AST) emphasizes the novelty of the work.

2. Experimental Procedure

The cylindrical work piece was collected for machining. The composition of the work piece is shown in Table 1.

Table 1. Weight percent chemical elements.

%C	%Si	%Mn	%S	%P	%Ni	%Cr	%Cu
0.225	0.465	7.48	0.0098	0.024	0.87	13.64	1.15

The rake angle of the carbide tool was 4°, and the main cutting edge angle was 46° (INDOLOY). The machining was performed for the parameters that are shown in Table 2. Subsequently, the chip specimens were obtained from the experiments. The following are the processing steps to prepare the optical microstructures for the surface (underside) of the chips.

Table 2. Experimental parameters and levels.

Deremeter Levels	Expt. No. (Levels)				
rarameter Levels —	1 (Low)	2 (Medium)	3 (High)		
Speed, m/min	40	70	100		
Feed, mm/rev.	0.06	0.10	0.16		
DOC, mm	0.5	1	1.5		

(i) Plastic mounting of the formed chips;

(ii) Polishing;

(iii) Etching.

The chips were viewed by scanning electron microscopy. The engineering stress (σ) and the engineering strain (ϵ) data were collected from the engineering stress–strain curve. These data were used to find out the corresponding true stress (σ T) and the true strain (ϵ T). The relationship between the yield and ultimate tensile strength was considered. The relationship between σ T and ϵ T was obtained graphically. The following power law Equation (1) [16] was obtained graphically by using the graph.

$$\sigma_{\rm T} = K \left(\varepsilon_{\rm T} \right)^n, \tag{1}$$

where K represents the strength coefficient and n represents the strain hardening exponent. Thereafter, the VMS (S) [17] was determined by using Equation (2):

$$S = 1.74 \text{ K} (\ln \zeta)^n, \tag{2}$$

where ζ is the CRC.

3. Results and Discussion

The Rockwell hardness of the as-received work piece was 59.74. The toughness property of the material was 271 J. The power law equation was established as Equation (3). The experimental response parameters are shown in Table 3. The variations in the VMS with respect to the experiment number are shown (Figure 1).

$$\sigma = 2800 \ \varepsilon^{0.4452},$$
 (3)

Table 3. The CRC and the VMS.

Expt. No.	CRC	VMS
1	3.784	5532.84
2	2.549	4729.83
3	2.230	4416.21



Figure 1. Variation in VMS with respect to expt. No.

The higher VMS was caused by machining at lower parameter levels. This is due to the SIM for these cutting parameters. The VMS decreases with the increase in the speed, feed, and DOC. This indicates that the material recrystallizes dynamically because of the machining at higher levels. The chip macrographs are shown in Figure 2a (experiment number 1), Figure 2b (experiment number 2), and Figure 2c (experiment number 3). The broken chips are seen (Figure 2a) and the long tubular chips are seen (Figure 2b). The long tubular chips are also seen in Figure 2c. The broken chips are due to the lower heat generation during cutting at the lower parameter levels. The long tubular chips are due to the higher heat generation during the machining due to the high and moderate speed, feed, and DOC. The austenite matrix with the dispersed carbide precipitates is seen in the microstructure (Figure 3).



Figure 2. Chip macrographs (a) expt. no. 1, (b) expt. no. 2, (c) expt. no. 3.



Figure 3. Microstructure (optical) of the as-received work piece.

The microstructure (optical) of the chip's surface (under) for the first machining condition is shown in Figure 4a. The fracture in the chip was due to SIM formation in the present machining condition. The chips were catastrophically broken. The surface (under) of the chip remains unetched due to the adiabatic shear at the chip's surface (under). The microstructure (optical) of the chip's surface (under) for the second machining condition is shown in Figure 4b. The dynamic recrystallization (DRX) took place at the chip's surface (under). The DRX precedes the DP for the present machining condition. The DRX is seen in austenitic stainless steel due to the low stacking fault energy (SFE). For the moderate cutting parameter levels, the present alloy's high carbon and chromium content favors the formation of chromium carbides in the chip. This made the austenitic stainless steel amenable to DP. The chromium carbide particles decorated the recrystallized grain boundaries. The massive carbides are also observed inside the grain. These massive carbides are the carbides of the prior austenite grain boundaries. The DRX is preferred as the larger-sized carbide particles (>1 μ m) cannot pin the grain boundaries. The larger carbide particles influenced the DRX and DP. The formation of the secondary saw tooth at

the free edge of the chip is shown in Figure 4c. The secondary saw tooth cannot form at the constrained edge of the chip.



Figure 4. Optical microstructure of surfaces (under) of chips (**a**) expt. no. 1, (**b**) expt. no. 2, (**c**) expt. no. 2 (SST: secondary saw tooth), and (**d**) expt. no. 3.

The microstructure (optical) of the chip's surface (under) for the third machining condition is shown in Figure 4d. The chromium carbide size is smaller due to the higher deformation energy. The DRX is higher for the machining at higher parameter levels. The finer DRX grains are observed in the chip's microstructure. This is due to the higher temperature generation in the present machining condition. The chromium carbide particles are seen along the finer DRX grain boundaries. A few carbide particles are also observed inside the grain. The DRX proceeds and the DP lags behind the DRX. The details of SIM formation, dynamic recrystallization, and dynamic precipitation have been represented schematically [18].

The side flow at the free edge of the chip for the low speed, low feed, and low DOC is seen. The side flow was not observed at the constrained edge of the chip (Figure 5a). The fragment of the built-up edge was seen (Figure 5b). The sinuous flow and the cracking were observed at the chip's top surface (Figure 5c). The cracks were also observed at the chip's surface (under) (Figure 5d). This indicated that the chip was formed in the hardened state (SIM) during the machining at lower parameter levels. The DRX is higher for the machining at higher parameter levels. The secondary saw tooth was observed at the free edge of the chip (Figure 5e) during the machining at the higher parameter levels. The secondary saw tooth was observed at the free edge of the steel was noted at the chip's top surface (Figure 5f). The lamellar flow appearance of the steel was noted at the chip's top surface (Figure 5f). The saw-tooth chip was observed at a high speed, feed, and DOC (Figure 5g). For this condition, a higher temperature developed at the primary deformation zone (PDZ) of the austenitic stainless steel. The heat that has developed in the PDZ cannot dissipate due to the poorer conductivity of the material. Because of the adiabatic shear instability condition, the entire heat is trapped in the PDZ, and saw teeth form.



Figure 5. SEM images of (**a**) chip (under–surface) (SF: side flow), (**b**) BUE at the chip (under surface), (**c**) chip top–surface (C: crack, SNF: sinuous flow), (**d**) chip surface (under) (C: crack) (**e**) free edge of chip (SST: secondary saw tooth), (**f**) chip topsurface (CE: constrained edge), (**g**) saw-type tooth (ST: saw tooth).

Constrained edge Tool Chip

Figure 6. The location of the constrained edge.

4. Conclusions

At lower parameter levels, the chip was formed for the SIM formation and adiabatic shear instability state of the current steel. The chip formation was for the DRX, followed by DP for the higher parameter levels. The built-up edge formation was for machining at the lower parameter levels. The chip formation was due to the restoration of the hardening behavior of the material. The saw-tooth chip formed due to the high-speed cutting at the high feed and high DOC.

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The concept of a constrained edge is schematically shown in Figure 6.

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