

Special Issue

New Challenges in 5G Networks Design

Message from the Guest Editors

This Special Issue will present new challenges with respect to the theoretical foundations and practical implementation of 5G communication networks and their evolution. Prospective authors are cordially invited to submit their manuscripts on topics including, but not limited to:

- Challenges in cross-layer design with high data rates and massive, secure, or high mobility URLLC for 5G networks;
- Challenges in designing the next generation of access technologies;
- Performance analysis and evaluation of 5G networks;
- Challenges in new network architectures for URLLC services;
- Performance optimization for topological enhancements;
- Low-complexity phase shift design for reconfigurable intelligent surfaces (RIS) in 5G networks;
- Challenges in integrated sensing and communication for 5G networks;
- Challenges in real-world deployments, experiments, prototypes, and testbeds for 5G;
- AI/ML for 5G networks.

You are welcome to contribute!!!

Guest Editors

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Dr. Thien Huynh-The

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Deadline for manuscript submissions

closed (15 July 2023)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.4 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the second half of 2024).