

Special Issue

New CMOS Devices and Their Applications

Message from the Guest Editor

This Special Issue of *Electronics* aims at presenting an in-depth discussion of the new CMOS devices and technologies that will have an impact on the electronics world in the next decade. Papers are solicited on next-generation CMOS devices, 3D NAND Flash Memory, neuromorphic devices, and any other technology able to take up the challenges of the next ten years. Topics of interest include, but are not limited to:

- Sub-10-nm multi-gate MOSFET (FinFET, nanowire, nanoplate, etc.);
- Next-generation CMOS devices (tunnel FETs, negative capacitance FETs, etc.);
- Characterization of 3D stacked NAND Flash Memory and DRAM;
- Emerging memories and neuromorphic devices;
- Applications of new CMOS devices;
- Design, modeling, simulation, and reliability of new devices/circuits;
- Devices and circuits for high-frequency applications.

Please read published researches [here](#). *Open for Submission!*

Guest Editor

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Deadline for manuscript submissions

closed (31 December 2020)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.6 days (median values for papers published in this journal in the first half of 2024).