Special Issue

Design and Implementation of Efficient Future Memory Systems

Message from the Guest Editor

In this Special Issue, original research articles as well as review articles that deal with system software and design architectures for new memory technologies are invited. The system software includes each module in operating systems, files systems, or database systems, and design architectures include new hardware design for future memory techniques. Potential topics include but are not limited to:

- Advanced operating systems for future memory technologies;
- Advanced file systems for future memory technologies;
- Advanced database systems for future memory technologies;
- New hardware design for target applications including machine learning, AR, and so on;
- Fault-tolerance for future memory technologies;
- Performance analysis for future memory technologies.

Please click here to find information!Welcome to contribute!

Guest Editor

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Deadline for manuscript submissions

closed (31 July 2021)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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