Special Issue

Neuromorphic Sensing and Computing Systems

Message from the Guest Editors

Neuromorphic computing is currently being proposed as an alternative and efficient way to carry out computation using principles derived from neurobiological systems. Although the neuromorphic term has historically been used to describe hardware implementations of neural circuits in analog, digital, or mixed-mode analog/digital VLSI, in recent years, it has also been used to describe a wider spectrum of sensing and computing systems. The application of neuromorphic systems faces the challenge of building novel algorithms, tools, and architectures that can best cope with the nature of low-power, dense, and parallel elements. The complexity and sophistication of such systems is increasing over time with an unprecedented speed both at the theoretical and technological level. In this Special Issue, we welcome work related to hardware architectures, event-based sensing and computing. spiking neural networks, learning systems, and alternative neuromorphic computing paradigms. We will also consider submissions that involve emerging memories and unconventional computing technologies as candidate solutions for the execution of neural information processing in an extremely efficient way.

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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