

Special Issue

New Trends for High-Performance Computing

Message from the Guest Editors

Dear colleagues, High-performance computing (HPC) has revolutionized many domains of science and engineering, including scientific simulation, knowledge discovery, and artificial intelligence. Fully harnessing and exploiting the computing power of HPC systems is expected to further advance those disciplines, but this is becoming more challenging because of the rapid evolution in the designs of recent and future HPC systems. This Special Issue seeks novel research in the context of algorithms, software, and architectures for current and next-generation HPC, including (but not limited to) parallel algorithms that exploit the existing and incoming architectures; system software that enhances the HPC cyberinfrastructure; large-scale applications that take advantage of HPC systems; and novel architectures that feature performance, energy efficiency, or resilience.

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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