

Special Issue

Recent Advances for Next-Generation High-Speed Optical Networks: Technologies, Components, Systems and Architectures

Message from the Guest Editors

Recently advanced Si- and InP-based photonic platforms have scaled the per lane speed up to 100G and even 200G, facilitating the next generation of Tb/s capacity transceivers. The quest to develop novel active and passive building blocks that will address these future requirements in a low-cost and scalable way continues. For the deployment of next-generation metro/core networks, the combination of sliceable bandwidth variable transceivers and reconfigurable switching nodes is gaining momentum, allowing the effective allocation of resources and facilitating the programmability of the network. Authors are invited to submit manuscripts within the scope of the Special Issue including, but not limited to, the following topics:

- High-speed transceivers and switches;
- Photonic Integrated Circuits;
- Optical components, sub-systems, and devices;
- Network architectures;
- Switch architectures;
- Optical Communication Technologies;
- Photonics-Electronics synergies and Co-Packaged Optics;
- Neuromorphic circuits;
- Photonic Sensors for Network Monitoring;
- Quantum transceivers.

Guest Editors

Dr. Giannis Kanakis

Photonics Research Communications Laboratory, School of Electrical and Computer Engineering, National Technical University of Athens, Athens, Greece

Dr. Maria Spyropoulou

Photonics Research Communications Laboratory, School of Electrical and Computer Engineering, National Technical University of Athens, Athens, Greece

Deadline for manuscript submissions



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MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
photonics@mdpi.com

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About the Journal

Message from the Editor-in-Chief

Editor-in-Chief

Prof. Dr. Nelson Tansu
School of Electrical and Electronic Engineering (EEE), The University of
Adelaide, Adelaide, SA 5005, Australia

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), Inspec,
CAPlus / SciFinder, and other databases.

Journal Rank:

JCR - Q2 (Optics)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 14.8 days after
submission; acceptance to publication is undertaken in 2.6
days (median values for papers published in this journal in
the first half of 2024).