Special Issue

Challenges and Opportunities in Signal and Power Integrity: Theory and Applications

Message from the Guest Editors

This Special Issue wishes to offer the opportunity to engineers and scientists to exchange state-of-the-art developments in the field of signal and power integrity applied to any kind of high-speed circuit and system, to the modeling, design, validation, and testing of electronic hardware. The topics span from the theory, algorithms, and methods to improve the accuracy, efficiency, and optimization of signal and power integrity simulations to practical applications, innovative tools, prototypes, measurement approaches, and sensors that help and support the correct and advanced SI/PI design of electronic systems and components.

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Deadline for manuscript submissions

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Message from the Editor-in-Chief

Our primary goal is to encourage scientists and engineers to publish their theoretical results and developed methods in as much detail as possible. There is no limit to the maximum length of papers. Whenever possible, authors are encouraged to provide relevant data and developed code so that the results can be reproduced. Our goal is to provide a platform for scientists and engineers to share new approaches to signal processing in various application domains.

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